



**Alfa-MOS
Technology**

**AFN1932
30V N-Channel
Enhancement Mode MOSFET**

General Description

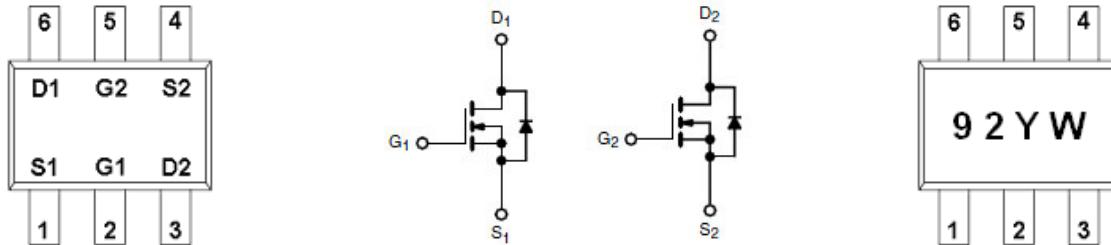
AFN1932, N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent $R_{DS(ON)}$, low gate charge.

These devices are particularly suited for low voltage power management, such as smart phone and notebook computer, and low in-line power loss are needed in commercial industrial surface mount applications.

Features

- 30V/1.5A, $R_{DS(ON)}=430m\Omega$ @ $V_{GS}=4.5V$
- 30V/1.2A, $R_{DS(ON)}=580m\Omega$ @ $V_{GS}=2.5V$
- 30V/0.6A, $R_{DS(ON)}=860m\Omega$ @ $V_{GS}=1.8V$
- Low Offset (Error) Voltage
- Low-Voltage Operation
- High-Speed Circuits
- Low Battery Voltage Operation
- SOT-363 package design

Pin Description (SOT-363)



Application

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories
- Battery Operated Systems
- Load/Power Switching Smart Phones, Pagers
- PA Switch
- Level Switch

Pin Define

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	D2	Drain 2
4	S2	Source 2
5	G2	Gate 2
6	D1	Drain1

Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFN1932S36RG	92YW	SOT-363	Tape & Reel	3000 EA

※ 92 parts code

※ Y year code (0 ~ 9)

※ W week code (A ~ Z = 1 ~ 26 / a ~ z = 27 ~ 52)

※ AFN1932S36RG : 7" Tape & Reel ; Pb- Free ; Halogen- Free



Absolute Maximum Ratings

($T_A=25^\circ\text{C}$ Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V_{DSS}	30	V
Gate –Source Voltage	V_{GSS}	± 12	V
Continuous Drain Current($T_J=150^\circ\text{C}$)	I_D	1.8	A
$T_A=70^\circ\text{C}$		1.0	
Pulsed Drain Current	I_{DM}	6	A
Continuous Source Current(Diode Conduction)	I_S	1	A
Power Dissipation	P_D	0.3	W
$T_A=70^\circ\text{C}$		0.2	
Operating Junction Temperature	T_J	-55/150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55/150	$^\circ\text{C}$

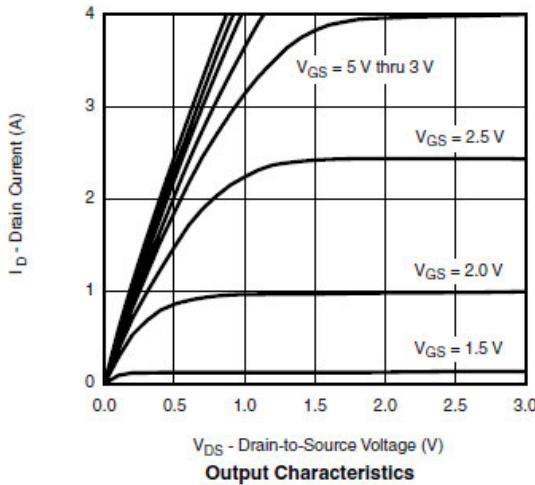
Electrical Characteristics

($T_A=25^\circ\text{C}$ Unless otherwise noted)

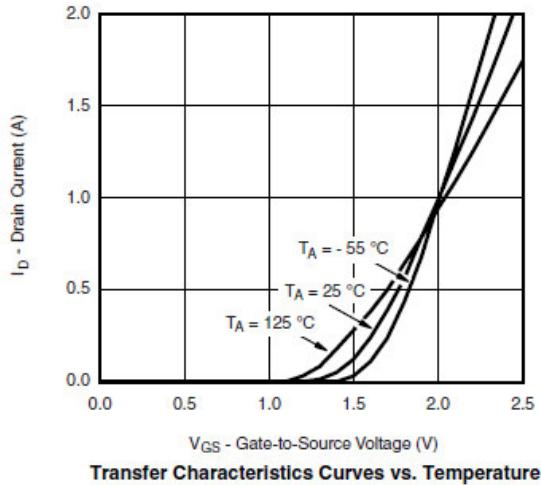
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	30			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	0.5		1.0	
Gate Leakage Current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 12\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=24\text{V}, V_{GS}=0\text{V}$			1	uA
		$V_{DS}=24\text{V}, V_{GS}=0\text{V}$ $T_J=85^\circ\text{C}$			5	
On-State Drain Current	$I_{D(\text{on})}$	$V_{DS}\geq 5\text{V}, V_{GS}=4.5\text{V}$	1.8			A
Drain-Source On-Resistance	$R_{DS(\text{on})}$	$V_{GS}=4.5\text{V}, I_D=1.5\text{A}$		380	430	mΩ
		$V_{GS}=2.5\text{V}, I_D=1.2\text{A}$		480	580	
		$V_{GS}=1.8\text{V}, I_D=0.6\text{A}$		700	860	
Forward Transconductance	g_{FS}	$V_{DS}=10\text{V}, I_D=1.0\text{A}$		1		S
Diode Forward Voltage	V_{SD}	$I_S=1.0\text{A}, V_{GS}=0\text{V}$		0.65	1.2	V
Dynamic						
Input Capacitance	C_{iss}	$V_{DS}=15\text{V}, V_{GS}=0\text{V}$ $f=1\text{MHz}$		85		pF
Output Capacitance	C_{oss}			25		
Reverse Transfer Capacitance	C_{rss}			15		
Total Gate Charge	Q_g	$V_{DS}=15\text{V}, V_{GS}=4.5\text{V}$ $I_D=1.2\text{A}$		1.4	1.8	nC
Gate-Source Charge	Q_{gs}			0.3		
Gate-Drain Charge	Q_{gd}			0.6		
Turn-On Time	$t_{d(on)}$	$V_{DD}=15\text{V}, R_L=20\Omega$ $I_D=1.2\text{A}, V_{GEN}=4.5\text{V}$		15	25	ns
	t_r			25	45	
Turn-Off Time	$t_{d(off)}$			15	25	
	t_f			10	20	



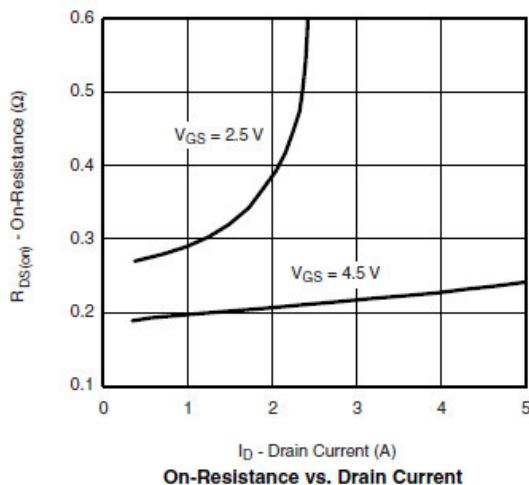
Typical Characteristics



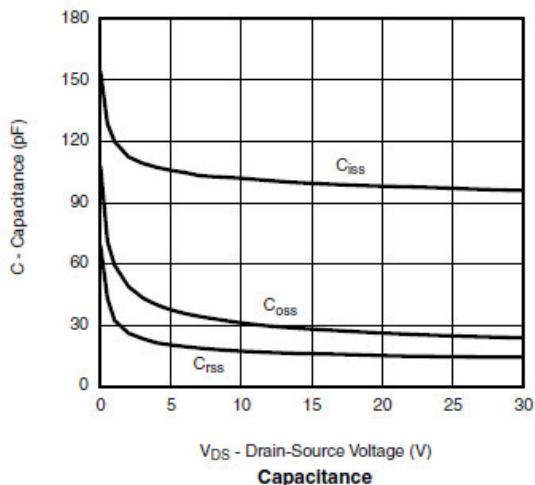
Output Characteristics



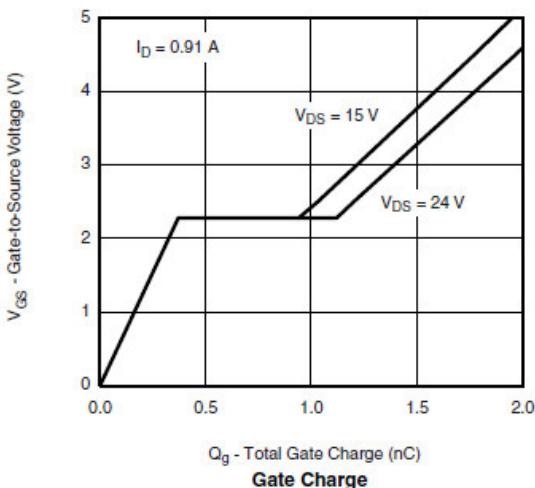
Transfer Characteristics Curves vs. Temperature



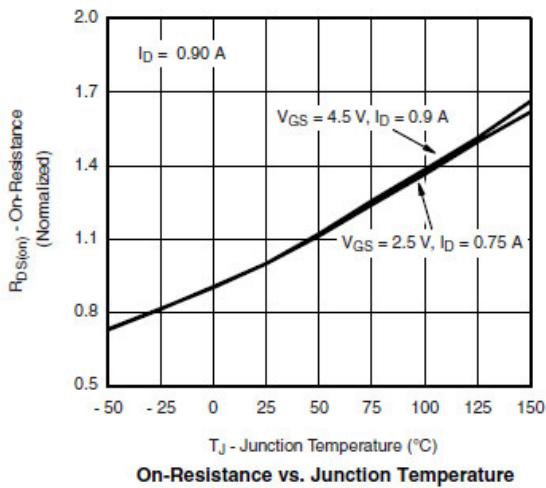
On-Resistance vs. Drain Current



Capacitance



Gate Charge



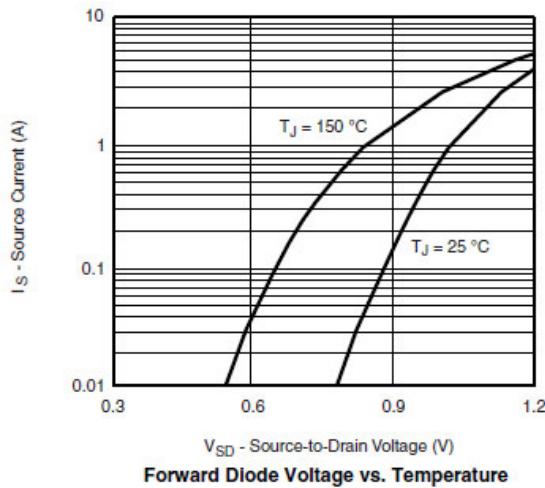
On-Resistance vs. Junction Temperature



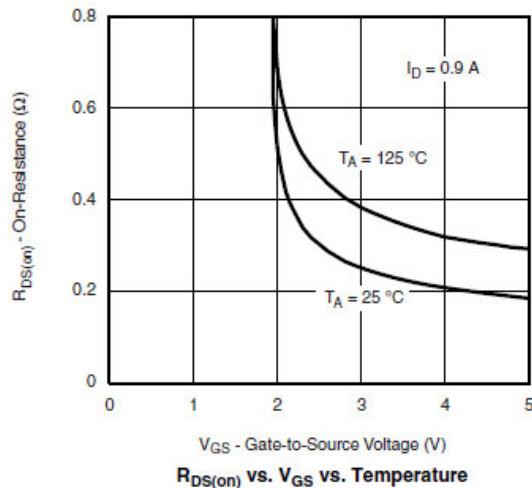
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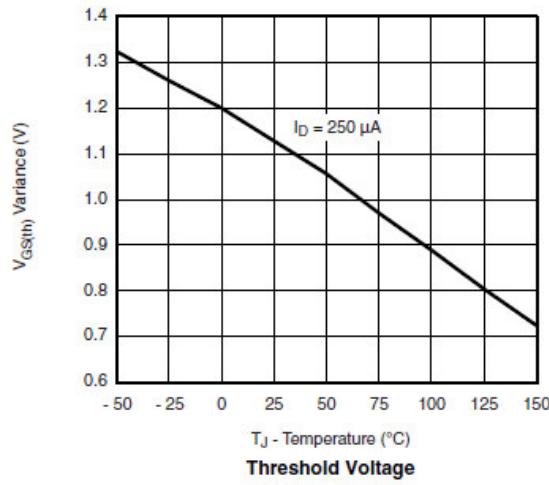
Typical Characteristics



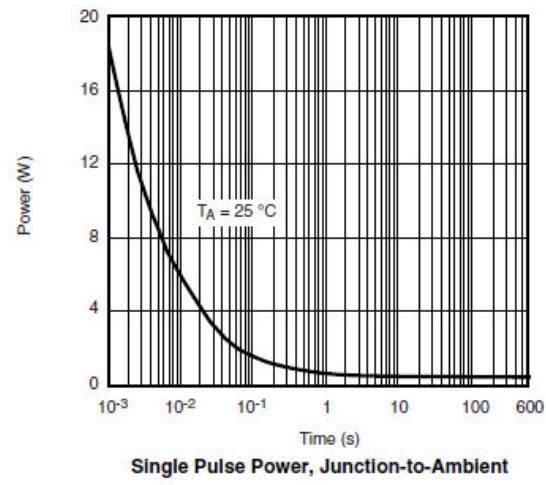
Forward Diode Voltage vs. Temperature



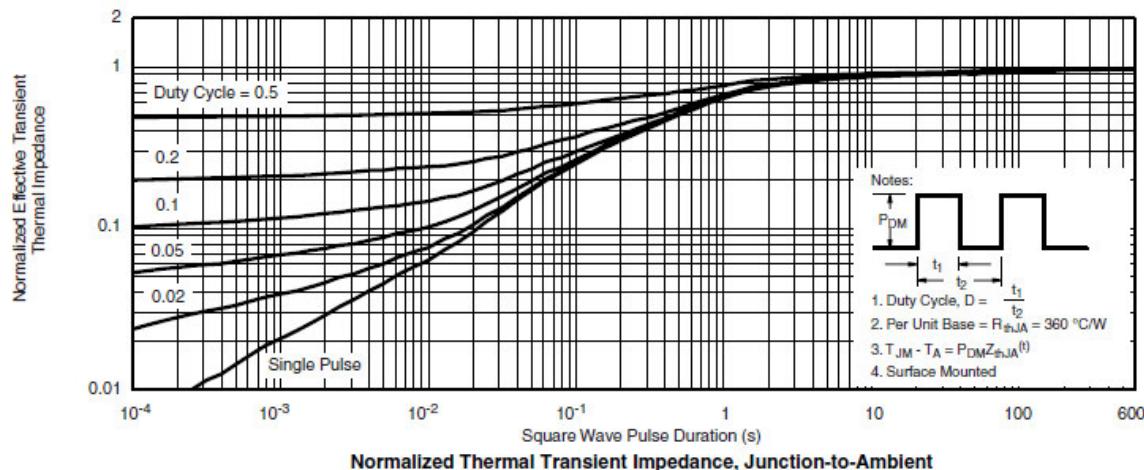
$R_{DS(on)}$ vs. V_{GS} vs. Temperature



Threshold Voltage



Single Pulse Power, Junction-to-Ambient

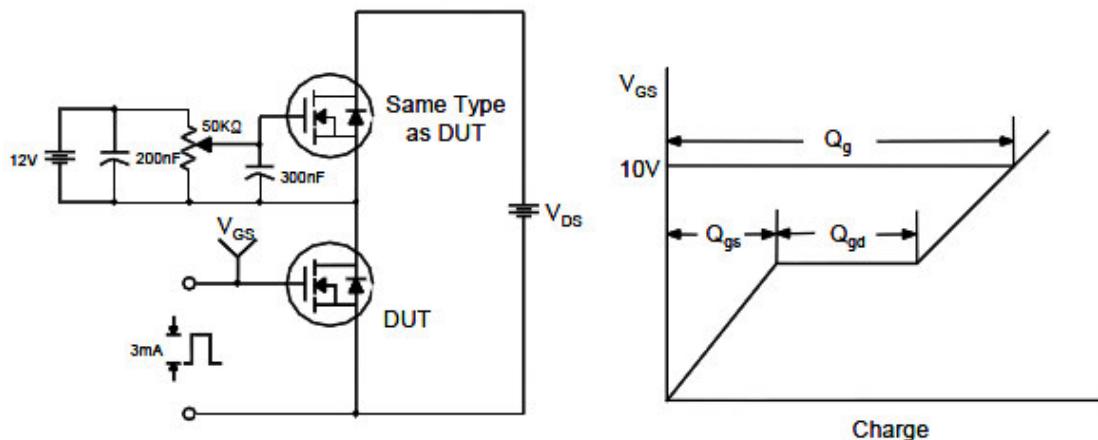


Normalized Thermal Transient Impedance, Junction-to-Ambient

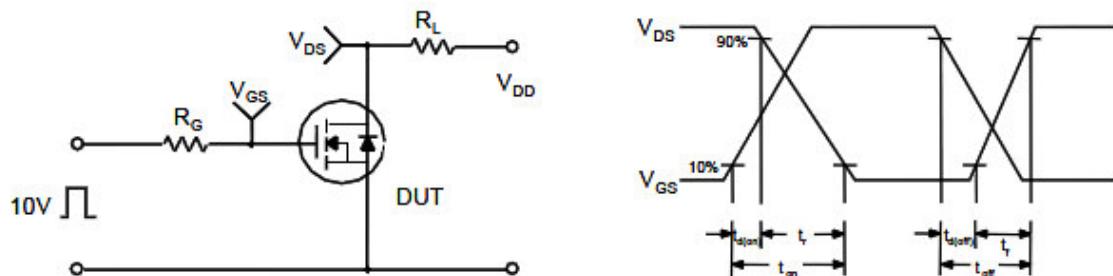


Typical Characteristics

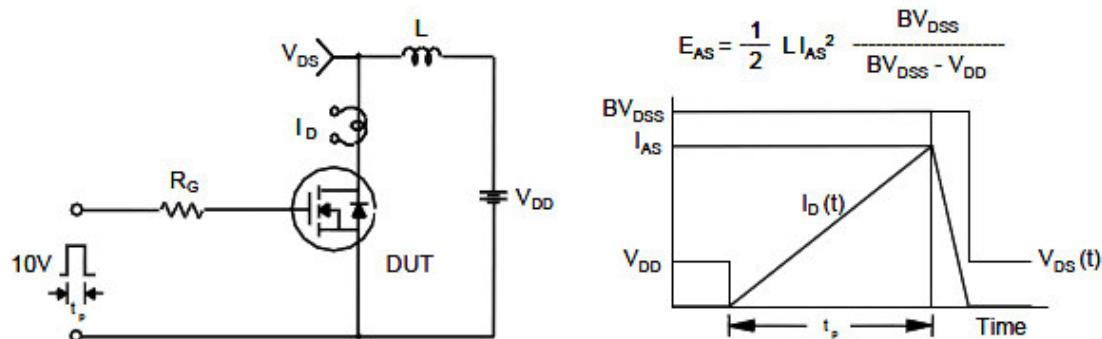
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

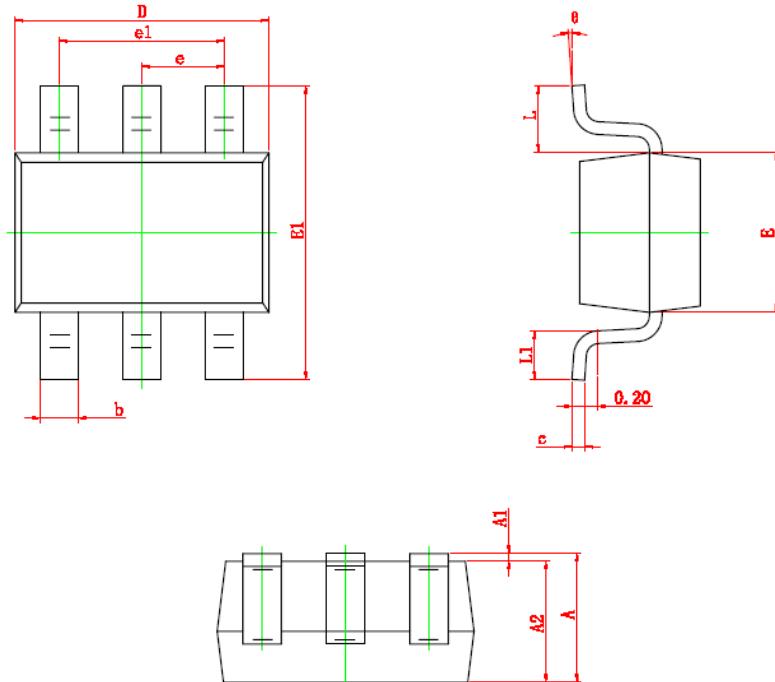




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Package Information (SOT-363)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650 TYP		0.026 TYP	
e1	1.200	1.400	0.047	0.055
L	0.525 REF		0.021 REF	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°

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