



## General Description

AFC3366W, N & P Pair enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent  $R_{DS(ON)}$ , low gate charge.

These devices are particularly suited for low voltage power management, and low in-line power loss are needed in commercial industrial surface mount applications.

## Features

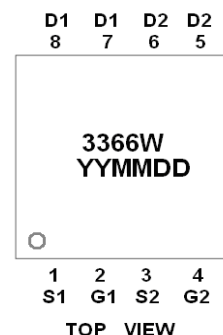
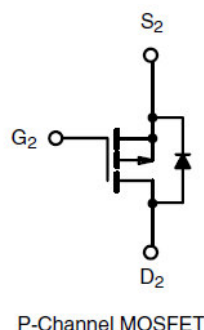
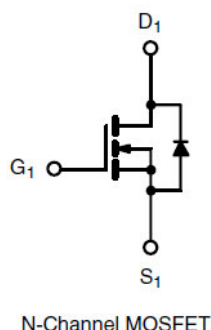
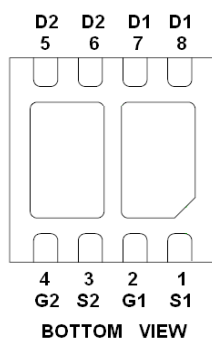
N-Channel

- 60V/12A,  $R_{DS(ON)}=48m\Omega@V_{GS}=10V$
- 60V/10A,  $R_{DS(ON)}=54m\Omega@V_{GS}=4.5V$

P-Channel

- -60V/-8A,  $R_{DS(ON)}=105m\Omega@V_{GS}=-10V$
- -60V/-6A,  $R_{DS(ON)}=115m\Omega@V_{GS}=-4.5V$

## Pin Description ( DFN3X3-8L )



## Application

- DC/DC Conversion
- Load Switch
- DC FAN

## Pin Define

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	S2	Source 2
4	G2	Gate 2
5	D2	Drain 2
6	D2	Drain 2
7	D1	Drain 1
8	D1	Drain 1

## Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFC3366WFN338RG	3366W YYMMDD	DFN3X3-8L	Tape & Reel	5000 EA

※ YY year code

※ MM month code

※ DD date code

※ AFC3366WFN338RG : 13" Tape & Reel ; Pb- Free ; Halogen- Free



## Absolute Maximum Ratings ( N-Channel )

(T<sub>A</sub>=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V <sub>DSS</sub>	60	V
Gate –Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current(T <sub>J</sub> =150°C)	I <sub>D</sub>	T <sub>A</sub> =25°C	12
		T <sub>A</sub> =70°C	10
Pulsed Drain Current	I <sub>DM</sub>	30	A
Continuous Source Current(Diode Conduction)	I <sub>S</sub>	10	A
Power Dissipation	P <sub>D</sub>	T <sub>A</sub> =25°C	2
		T <sub>A</sub> =70°C	1.5
Operating Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature Range	T <sub>STG</sub>	-55/150	°C
Thermal Resistance-Junction to Ambient	R <sub>θJA</sub>	56	°C/W

## Electrical Characteristics ( N-Channel )

(T<sub>A</sub>=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	60			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	0.8		2.5	
Gate Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =48V, V <sub>GS</sub> =0V			1	uA
		V <sub>DS</sub> =48V, V <sub>GS</sub> =0V T <sub>J</sub> =85°C			5	
On-State Drain Current	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 5V, V <sub>GS</sub> =4.5V	30			A
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =12A		40	48	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A		44	54	
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =15V, I <sub>D</sub> =5.3A		24		S
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =2.0A, V <sub>GS</sub> =0V		0.8	1.2	V
<b>Dynamic</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =5V I <sub>D</sub> ≅5.6A		10	15	nC
Gate-Source Charge	Q <sub>gs</sub>			3.5		
Gate-Drain Charge	Q <sub>gd</sub>			3.6		
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V f=1MHz		890		pF
Output Capacitance	C <sub>oss</sub>			85		
Reverse Transfer Capacitance	C <sub>rss</sub>			48		
Turn-On Time	t <sub>d(on)</sub>	V <sub>DD</sub> =30V, R <sub>L</sub> =6.8Ω I <sub>D</sub> ≅5.0A, V <sub>GEN</sub> =4.5V R <sub>G</sub> =6Ω		10	15	ns
	t <sub>r</sub>			12	20	
Turn-Off Time	t <sub>d(off)</sub>			25	35	
	t <sub>f</sub>			10	15	



### Absolute Maximum Ratings ( P-Channel )

(T<sub>A</sub>=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V <sub>DSS</sub>	-60	V
Gate -Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current(T <sub>J</sub> =150°C)	I <sub>D</sub>	T <sub>A</sub> =25°C	-8.0
		T <sub>A</sub> =70°C	-6.0
Pulsed Drain Current	I <sub>DM</sub>	-30	A
Continuous Source Current(Diode Conduction)	I <sub>S</sub>	-10	A
Power Dissipation	P <sub>D</sub>	T <sub>A</sub> =25°C	1.8
		T <sub>A</sub> =70°C	1.2
Operating Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature Range	T <sub>STG</sub>	-55/150	°C
Thermal Resistance-Junction to Ambient	R <sub>θJA</sub>	62.5	°C/W

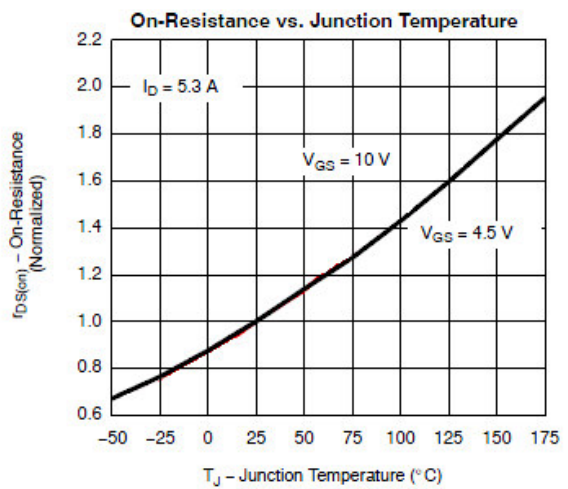
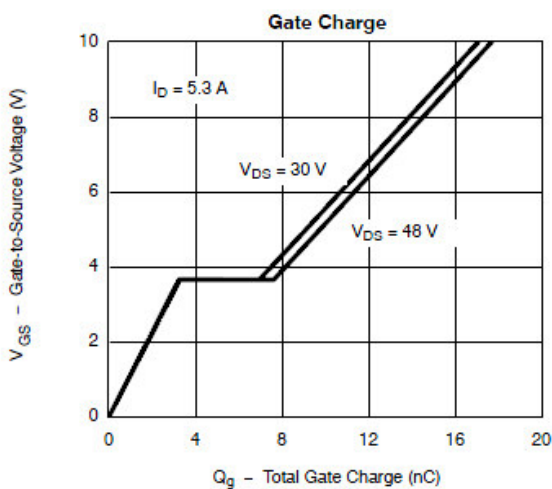
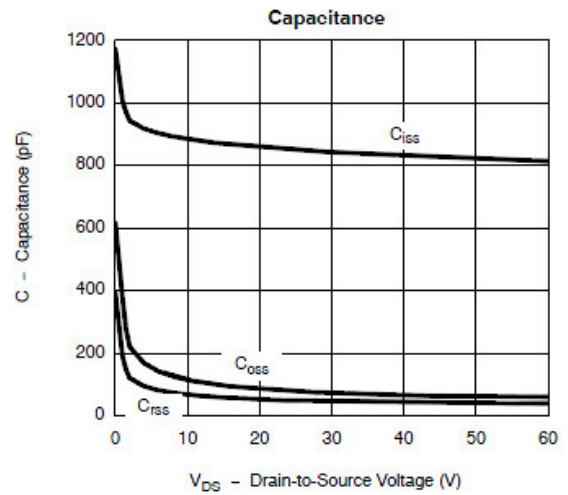
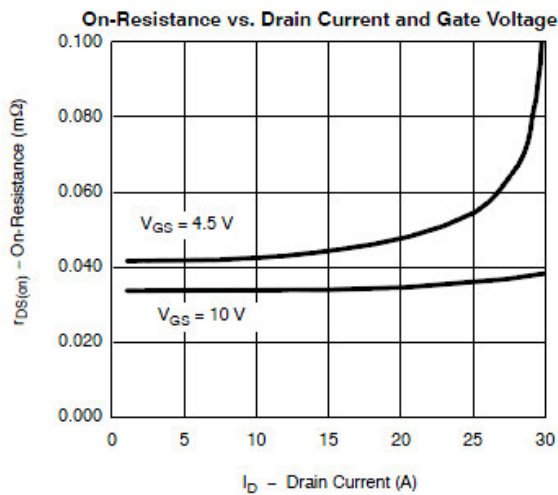
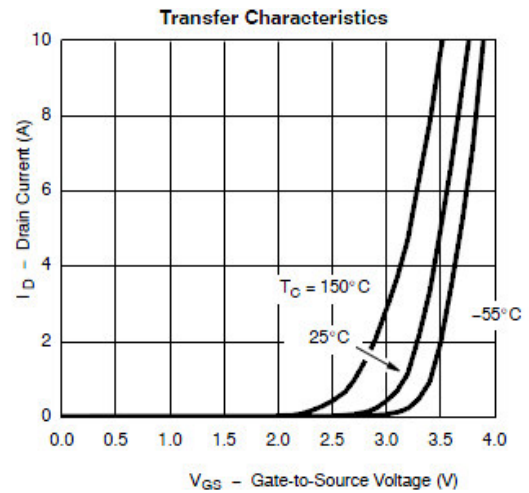
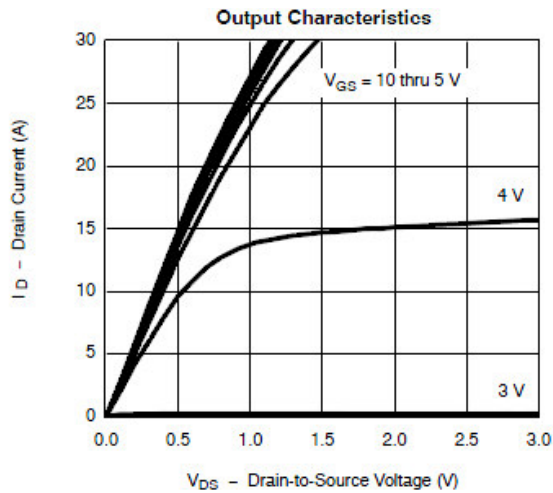
### Electrical Characteristics ( P-Channel )

(T<sub>A</sub>=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> = -250uA	-60			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> = -250uA	-0.8		-2.5	V
Gate Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±20V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -48V, V <sub>GS</sub> =0V			-1	uA
		V <sub>DS</sub> = -48V, V <sub>GS</sub> =0V T <sub>J</sub> =85°C			-20	
On-State Drain Current	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ -5V, V <sub>GS</sub> = -10V	-20			A
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = -10V, I <sub>D</sub> =-8.0A		96	105	mΩ
		V <sub>GS</sub> = -4.5V, I <sub>D</sub> =-6.0A		104	115	
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> = -15V, I <sub>D</sub> = -3.2A		12		S
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = -2A, V <sub>GS</sub> =0V		-0.8	-1.2	V
<b>Dynamic</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =-10V I <sub>D</sub> = -4.0A		12	20	nC
Gate-Source Charge	Q <sub>gs</sub>			2.5		
Gate-Drain Charge	Q <sub>gd</sub>			3.5		
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V f=1MHz		900		pF
Output Capacitance	C <sub>oss</sub>			90		
Reverse Transfer Capacitance	C <sub>rss</sub>			40		
Turn-On Time	t <sub>d(on)</sub>	V <sub>DD</sub> =-30V, R <sub>L</sub> =7.5Ω I <sub>D</sub> ≡-3.0A, V <sub>GEN</sub> =-10V R <sub>G</sub> =3Ω		10	20	ns
	t <sub>r</sub>			6	10	
Turn-Off Time	t <sub>d(off)</sub>			30	45	
	t <sub>f</sub>			12	25	

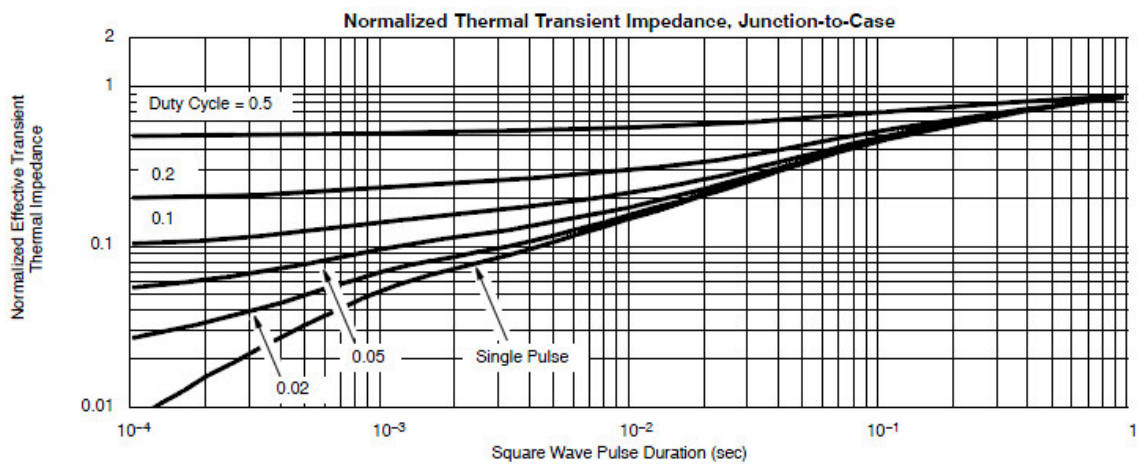
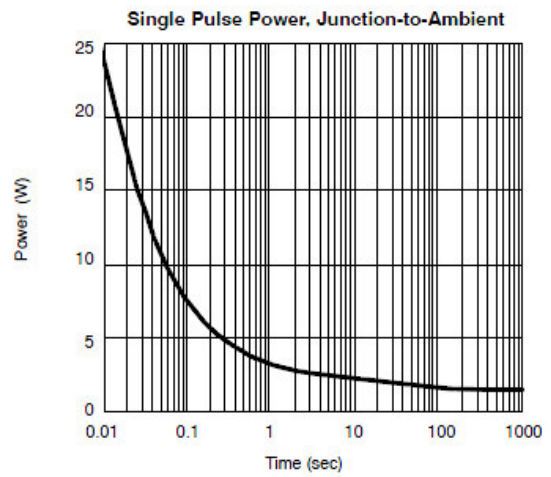
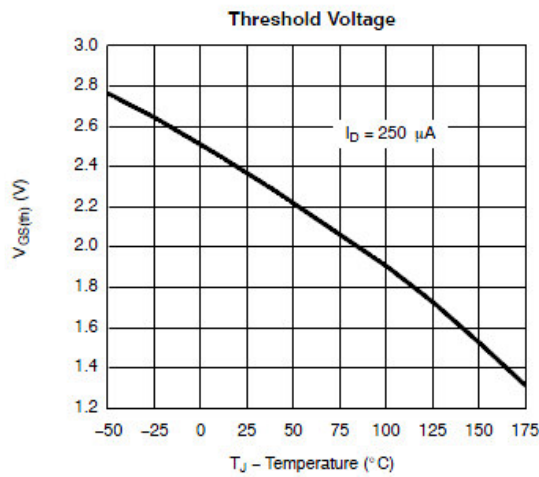
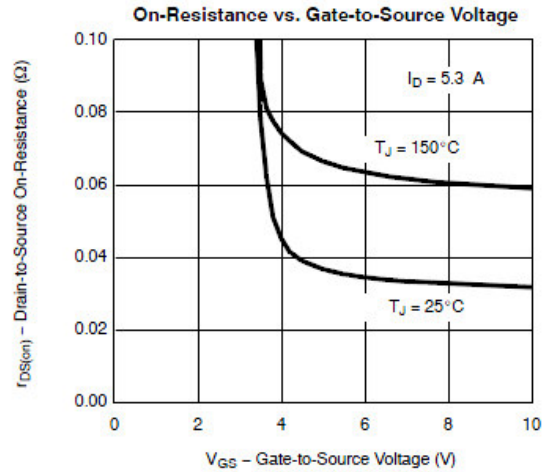
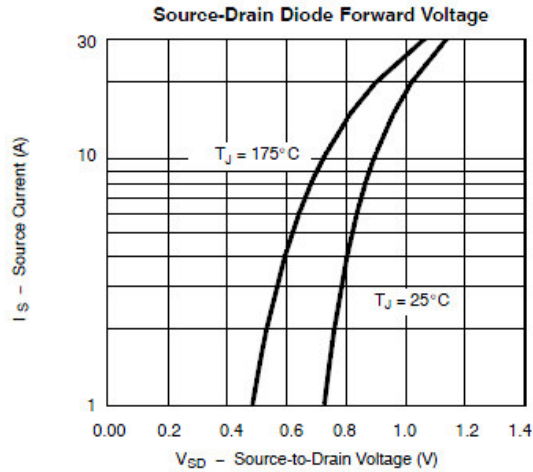


## Typical Characteristics ( N-Channel )



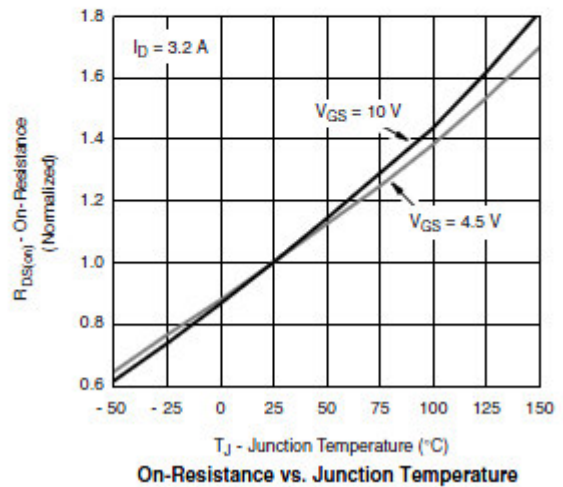
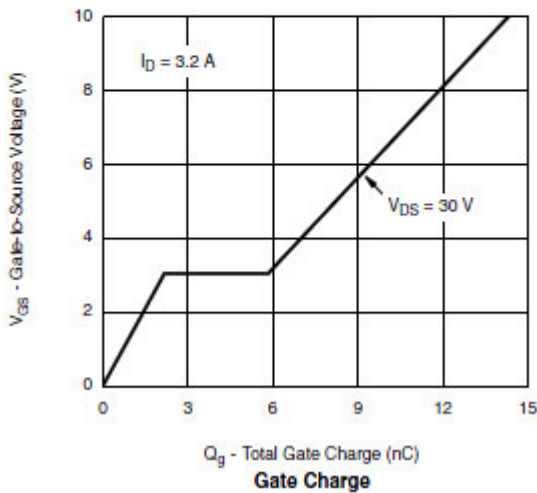
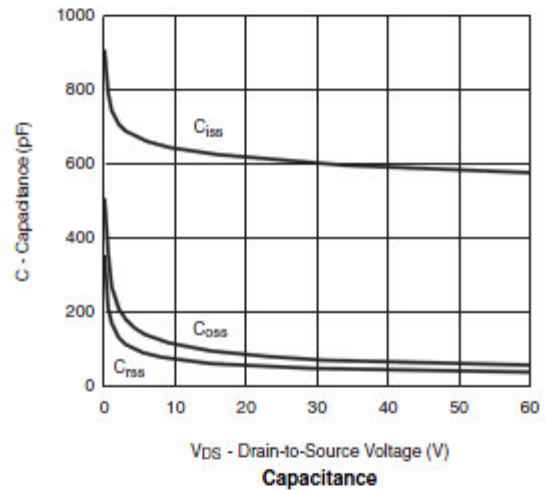
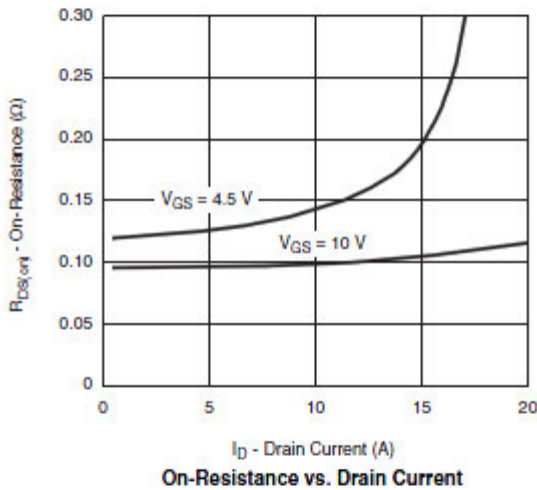
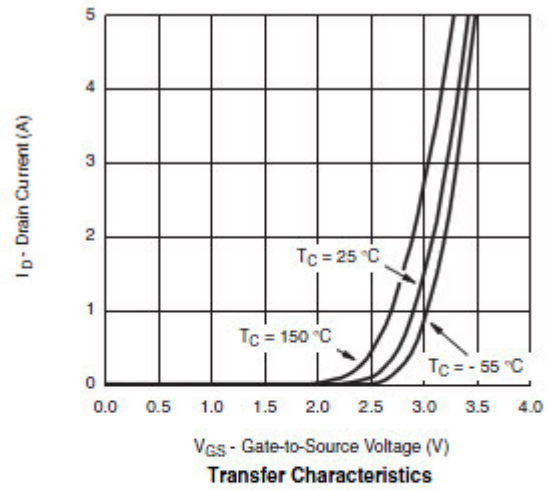
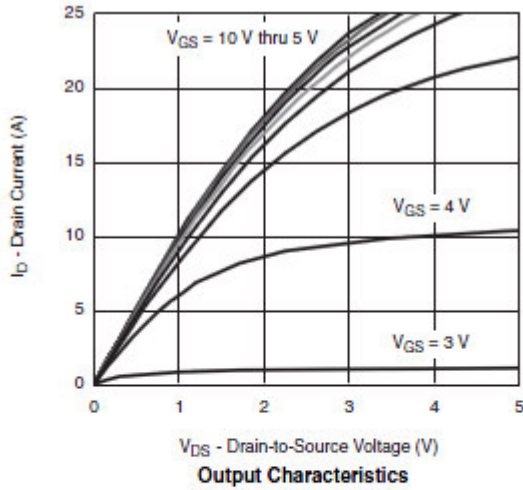


## Typical Characteristics ( N-Channel )



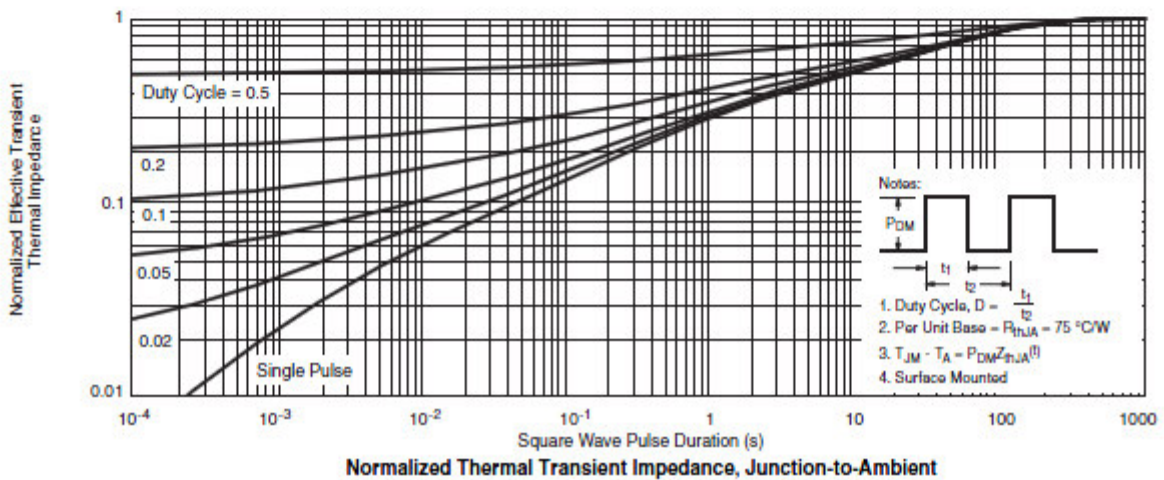
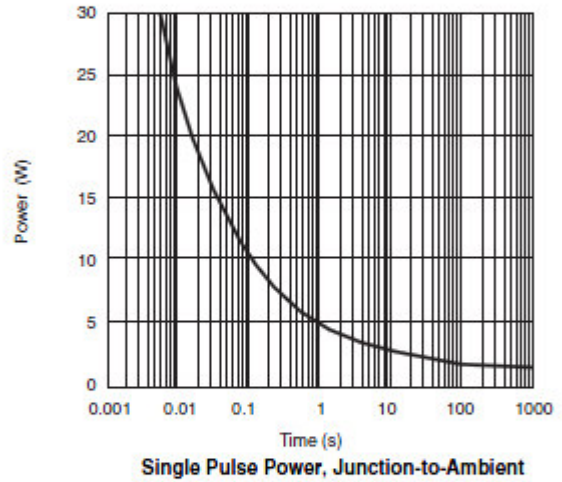
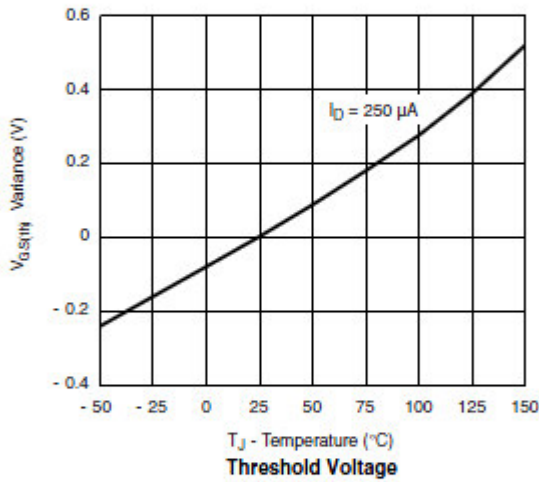
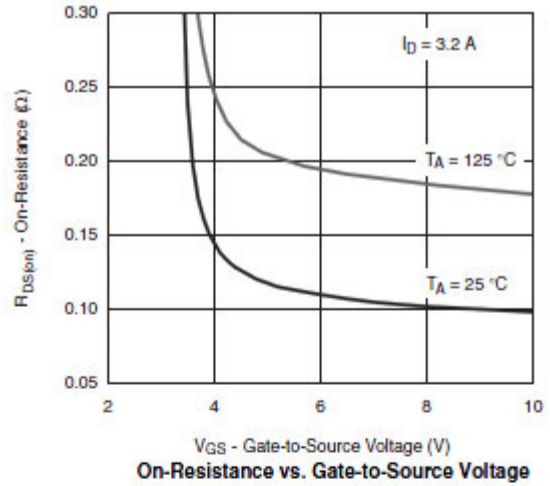
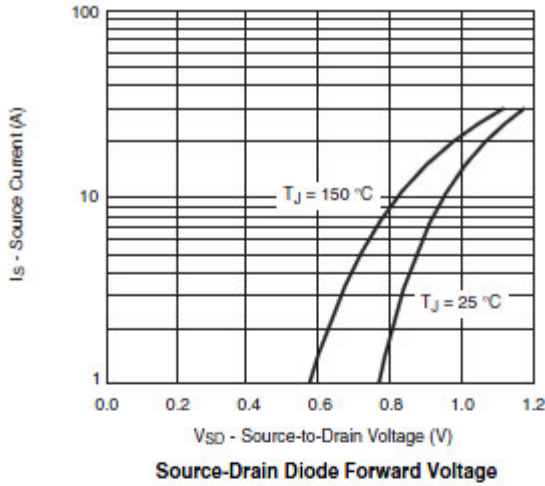


## Typical Characteristics ( P-Channel )





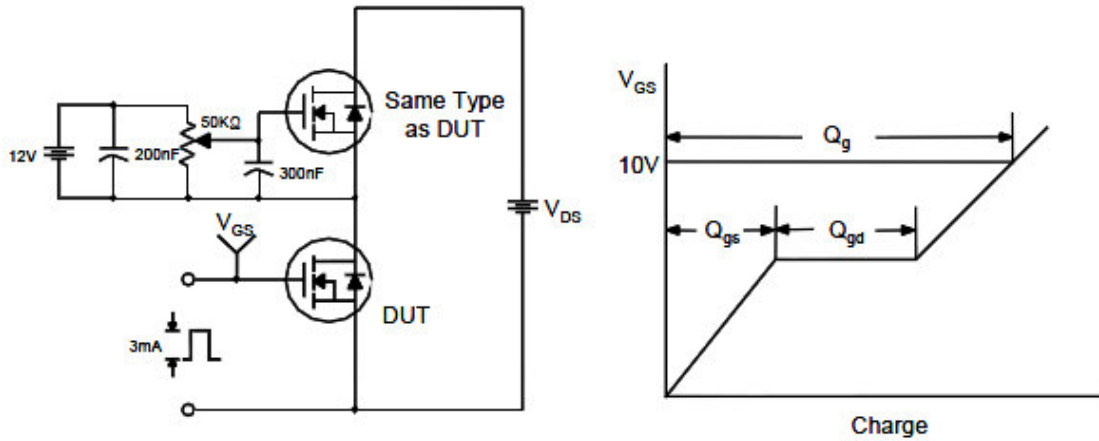
**Typical Characteristics ( P-Channel )**



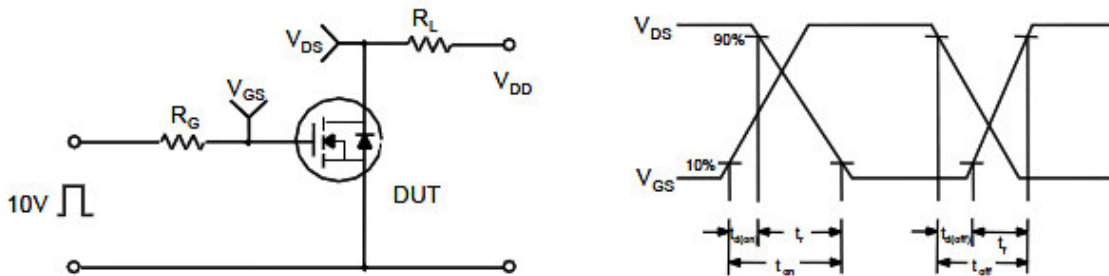


**Typical Characteristics**

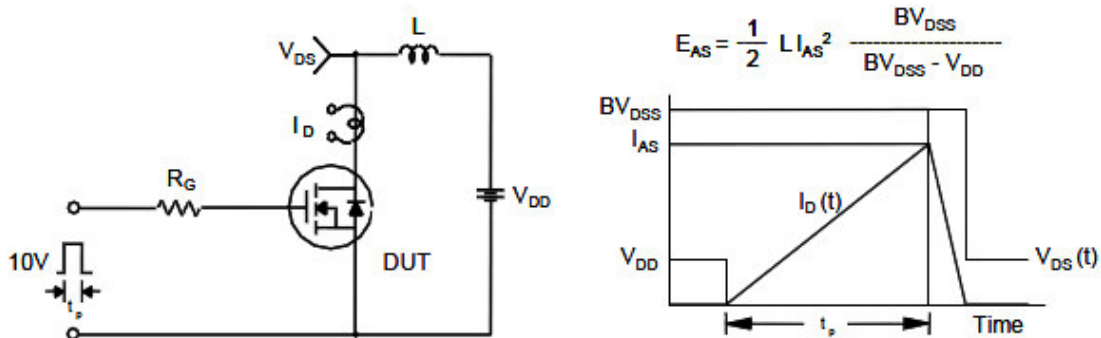
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



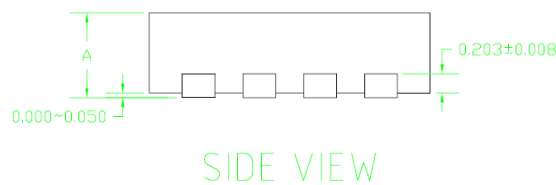
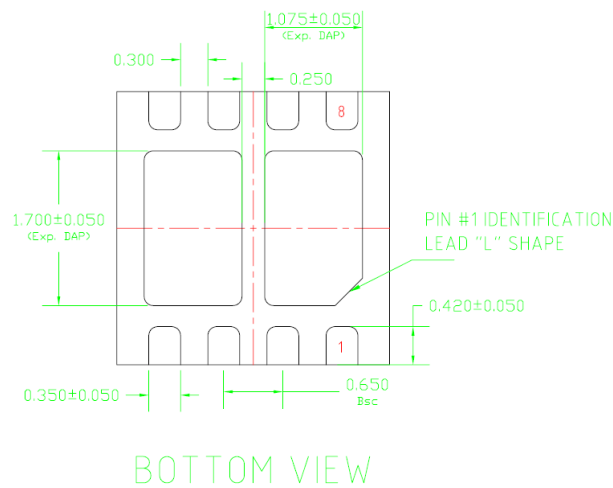
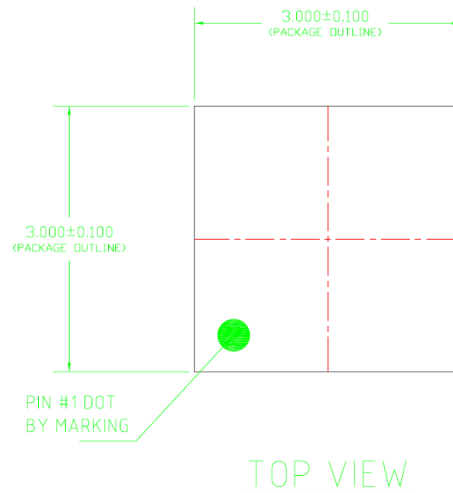
Unclamped Inductive Switching Test Circuit & Waveforms







**Package Information ( DFN3X3-8L )**



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