



## 600V N-Channel MOSFET

FQP1N60

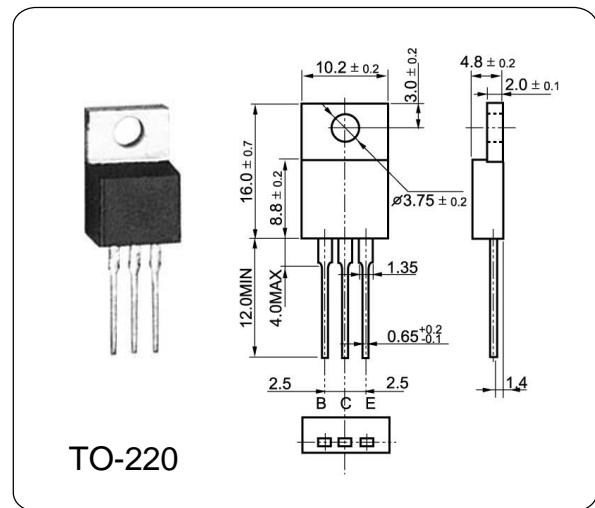
## DESCRIPTION

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switch mode power supplies.

## ABSOLUTE MAXIMUM RATINGS ( Ta = 25 °C)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	600	V
Drain Current - Continuous	I <sub>D</sub>	1.0	A
Drain Current - Pulsed	I <sub>DM</sub>	3.0	A
Gate-Source Voltage	V <sub>GSS</sub>	±30	V
Power Dissipation	P <sub>D</sub>	34	W
Max. Operating Junction Temperature	T <sub>j</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55~150	°C



## ELECTRICAL CHARACTERISTICS ( Ta = 25 °C)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250 μA	600	—	—	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V	—	—	1.0	uA
Gate-Body Leakage Current, Forward	I <sub>GSSF</sub>	V <sub>GS</sub> = 30V, V <sub>DS</sub> = 0V	—	—	100	uA
Gate-Body Leakage Current, Reverse	I <sub>GSSR</sub>	V <sub>GS</sub> = -30V, V <sub>DS</sub> = 0V	—	—	-100	uA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	2.0	—	4.0	V
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.5 A	—	9.7	12	W
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 1.0 A	—	—	1.4	V