

500mA / 50V Digital transistors (with built-in resistor)

DTB123YU / DTB123YK

勝 特 力 材 料 886-3-5753170 胜特力电子(上海) 86-21-34970699 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw

Applications

Inverter, Interface, Driver

Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on / off conditions need to be set for operation, making the device design easy.

Structure

PNP epitaxial planar silicon transistor (Resistor built-in type)

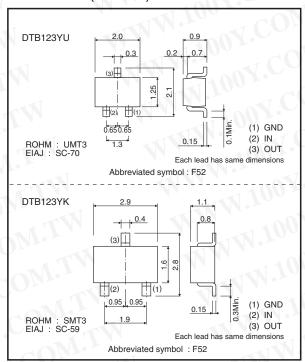
Packaging specifications

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	Package	UMT3	SMT3	
	Packaging type	Taping	Taping	
	Code	T106	T146	
Part No.	Basic ordering unit (pieces)	3000	3000	
DTB123YU		0	-	
DTB123YK		-	0	

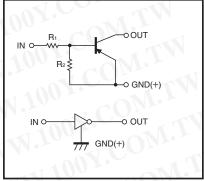
Absolute maximum ratings (Ta=25°C)

Parameter	Cumbal	Limits	Unit	
Farameter	Symbol	DTB123YU DTB123YK	Offic	
Supply voltage	Vcc	-50	V	
Input voltage	VIN	-12 to +5	V	
Output current	lc	-500	mA	
Power dissipation	Po	200	mW	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Dimensions (Unit : mm)



•Inner circuit



R₁=2.2kΩ, R₂=10kΩ

• Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	VI(off)	7		-0.3	V	Vcc=-5V, Io=-100μA
	VI(on)	-2	-	A T		Vo=-0.3V, Io=-20mA
Output voltage	VO(on)	-	-0.1	-0.3	V	Io/I:=-50mA/-2.5mA
Input current	li (Y .	U	-3.0	mA	V=-5V
Output current	IO(off)	<i>y</i> _	-	-0.5	μА	Vcc=-50V, VI=0V
DC current gain	Gı	56		V-	-	Vo=-5V, Io=-50mA
Input resistance	Rı	1.54	2.2	2.86	kΩ	_
Resistance ratio	R ₂ /R ₁	3.6	4.5	5.5	_	- 1
Transition frequency	fr *	16//	200	-	MHz	Vc==-10V, I==50mA, f=100MHz

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Electrical characteristic curves

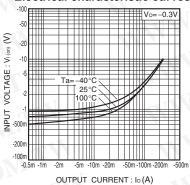


Fig.1 Input voltage vs. output current (ON characteristics)

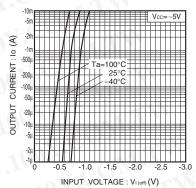


Fig.2 Output current vs. input voltage (OFF characteristics)

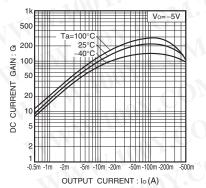


Fig.3 DC current gain vs. output current

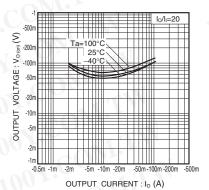


Fig.4 Output voltage vs. output current

^{*} Characteristics of built-in transistor

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