

500mA / 50V Digital transistors (with built-in resistors)

DTD113EK

Applications

Inverter, Interface, Driver

Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2)The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on / off conditions need to be set for operation, making the device design easy.

Structure

NPN epitaxial planar silicon transistor (Resistor built-in type)

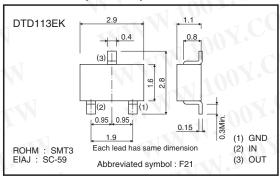
Packaging specifications

	Package	SMT3
CO_{D}	Packaging type	Taping
	Code	T146
Part No.	Basic ordering unit (pieces)	3000
DTD113E	0	

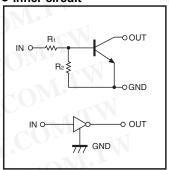
Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Farameter	Symbol	DTD113EK	Offic	
Supply voltage	Vcc	50	V	
Input voltage	Vin	-10 to +10	V	
Output current	lc	500	mA	
Power dissipation	Pd	200	mW	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	င	
		~~~		

#### Dimensions (Unit : mm)



#### Inner circuit



 $R_1=R_2=1.0k\Omega$ 

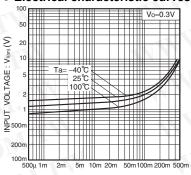
勝 特 力 材 料 886-3-5753170 胜特力电子(上海) 86-21-34970699 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw DTD113EK Data Sheet

# • Electrical characteristics (Ta=25°C)

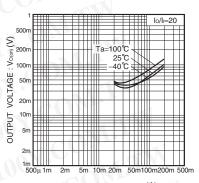
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	VI(off)	, Chr	_	0.5	V	Vcc=5V, Io=100μA
	V _I (on)	3	A.F.	7		Vo=0.3V, Io=20mA
Output voltage	VO(on)	GU	0.1	0.3	V	lo/l≔50mA/2.5mA
Input current	1 li		17	7.2	mA	V⊫5V
Output current	IO(off)	1 C	DF.	0.5	μА	Vcc=50V, Vi=0V
DC current gain	G	33	-1	1-7	_	Vo=5V, Io=50mA
Input resistance	R ₁	0.7	1	1.3	kΩ	4
Resistance ratio	R2/R1	0.8	1	1.2	7 7	-
Transition frequency	f⊤ *	-1	200		MHz	VcE=10V, IE= -50mA, f=100MHz

^{*} Characteristics of built-in transisto

#### Electrical characteristic curves



OUTPUT CURRENT: Io(A)
Fig.1 Input voltage vs. output current
(ON characteristics)



OUTPUT CURRENT : Io (A)
Fig.4 Output voltage vs. output current

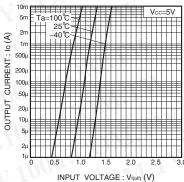
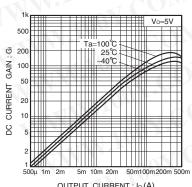


Fig.2 Output current vs. input voltage (OFF characteristics)





OUTPUT CURRENT: lo (A)
Fig.3 DC current gain
vs. output current

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