

Digital transistors (built-in resistors)

DTD123YK

Applications

Inverter, Interface, Driver

Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also
- have the advantage of almost completely eliminating parasitic effects.
- Only the on / off conditions need to be set for operation, making the device design easy.

Structure

NPN epitaxial planar silicon transistor (Resistor built-in type)

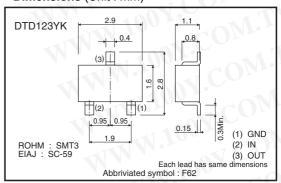
Package specifications

71/	Package	SMT3
	Packaging type	Taping
	Code	T146
Part No.	Basic ordering unit (pieces)	3000
DTD123YK		0

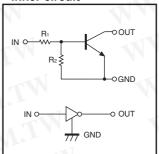
◆Absolute maximum ratings (Ta=25°C)

- 13 1 111 / 1		/		
Parameter	Symbol	Limits	Unit	
i arameter	Symbol	DTD123YK	Offic	
Supply voltage	Vcc	50	V	
Input voltage	Vin	-5 to +12	V	
Output current	lc	500	mA	
Power dissipation	Po	200	mW	
Junction temperature	Tj	150	င	
Storage temperature	Tstg	-55 to +150	ာ	

●Dimensions (Unit: mm)



●Inner circuit



 $R_1=2.2k\Omega$, $R_2=10k\Omega$

勝 特 力 材 料 886-3-5753170 胜特力电子(上海) 86-21-34970699 胜特力电子(深圳) 86-755-83298787 Http://www.100y.com.tw DTD123YK Data Sheet

●Electrical characteristics (Ta=25°C)

Symbol	Min.	Тур.	Max.	Unit	Conditions
V _{I(off)}	, Or	-	0.3	V	Vcc=5V, Io=100μA
V _{I(on)}	2	A.	7		Vo=0.3V, Io=20mA
VO(on)	GU	0.1	0.3	V	lo/l≔50mA/2.5mA
lı		18	3.6	mA	V=5V
IO(off)	1 C	OF.	0.5	μА	Vcc=50V, Vi=0V
Gı	56	-1	7-7	_	Vo=5V, Io=50mA
R ₁	1.54	2.2	2.86	kΩ	4
R ₂ /R ₁	3.6	4.5	5.5	<u> </u>	-
f⊤ *		200	_	MHz	VcE=10V, IE= -50mA, f=100MHz
	VI(off) VI(on) VO(on) II IO(off) GI R1 R2/R1	VI(off) - VI(on) 2 VO(on) - II - IO(off) - GI 56 R1 1.54 R2/R1 3.6	VI(off) - - VI(on) 2 - VO(on) - 0.1 II - - IO(off) - - GI 56 - R1 1.54 2.2 R2/R1 3.6 4.5	VI(off) - - 0.3 VI(on) 2 - - VO(on) - 0.1 0.3 II - - 3.6 IO(off) - - 0.5 GI 56 - - R1 1.54 2.2 2.86 R2/R1 3.6 4.5 5.5	V(off) - - 0.3 V V(on) 2 - - - VO(on) - 0.1 0.3 V II - - 3.6 mA Io(off) - - 0.5 μA GI 56 - - - R1 1.54 2.2 2.86 kΩ R2/R1 3.6 4.5 5.5 -

^{*} Characteristics of built-in transistor

Electrical characteristics curves

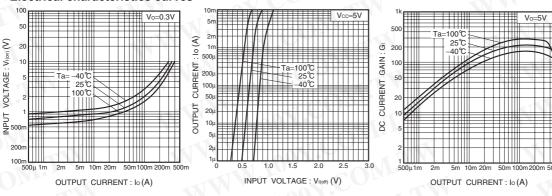


Fig.1 Input voltage vs. output current (ON characteristics)

Fig.2 Output current vs. input voltage Fig.3 DC current gain vs. output current (OFF characteristics)

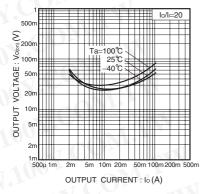


Fig.4 Output voltage vs. output current

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