## 200mA / 30V Low VcE (sat) Digital transistors (with built-in resistors)

### DTD723YE / DTD723YM

#### Applications

Inverter, Interface, Driver

#### ● Feature

- 1) VCE (sat) is lower than conventional products.
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 4) Only the on / off conditions need to be set for operation, making the device design easy.

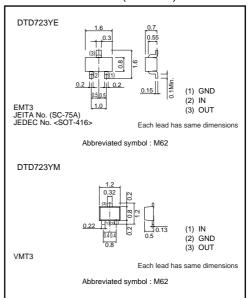
#### Structure

NPN epitaxial plannar silicon transistor (Resistor built-in type)

#### ◆Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits DTD723YE DTD723YM	Unit
Supply voltage	Vcc	30	V
Input voltage	Vin	−5 to +15	V
Collector current *1	IC (max)	200	mA
Power dissipation *2	Po	150	mW
Junction temperature	Tj	150	ဗ
Storage temperature	Tstg	-55 to +150	ဗ

#### External dimensions (Unit : mm)



#### Packaging specifications

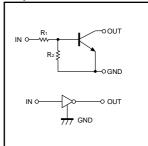
	Package	EMT3	VMT3
Packaging type		Taping	Taping
	Code	TL	T2L
Part No.	Basic ordering unit (pieces)	3000	8000
DTD723YE		0	-
DTD723YM		-	0

#### ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Input voltage	VI(off)	-	-	0.3	V	Vcc=5V, Io=100μA	
	VI(on)	2.5	-	-		Vo=0.3V, Io= 20mA	
Output voltage	Vo(on)	-	70	300	mV	Io/I:= 50mA / 2.5mA	
Input current	lı	-	-	3.0	mA	Vi= 5V	
Output current	IO(off)	-	-	500	nA	Vcc= 30V, Vi=0V	
DC current gain	Gı	140	-	-	-	Vo= 2V, Io= 100mA	
Transition frequency *	fτ	-	260	-	MHz	VcE=10V, IE= -5mA, f=100MHz	
Input resistance	R <sub>1</sub>	1.54	2.2	2.86	kΩ	-	
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	3.6	4.5	5.5	-	-	

<sup>\*</sup> Characteristics of built-in transistor

#### ●Equivalent circuit



 $R_1=2.2k\Omega / R_2=10k\Omega$ 

<sup>\*1</sup> Characteristics of built-in transistor. \*2 Each terminal mounted on a recom

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