

# **Complementary Silicon Power Transistors**

... designed for use as high frequency drivers in Audio Amplifiers.

- High Gain Complementary Silicon Power Transistors
- Safe Operating Area 100% Tested 50 V, 3.0 A, 1.0 Sec.
- Excellent Frequency Response  $f_T = 20 \text{ MHz min.}$

# **MAXIMUM RATINGS**

Rating	Symbol	MJ15018 MJ15019	MJ15020 MJ15021	Unit
Collector–Emitter Voltage	V <sub>CEO</sub>	200	250	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	200	250	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	7.0		Vdc
Collector Current — Continuous	I <sub>C</sub>	4.0		Adc
Base Current — Continuous	I <sub>B</sub>	2.0		Adc
Emitter Current — Continuous	Ι <sub>Ε</sub>	6.0		Adc
Total Power Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	150 0.86		Watts W/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +200		°C

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{ heta JC}$	1.17	°C/W

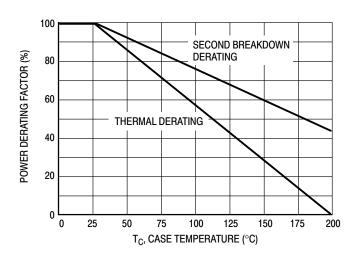
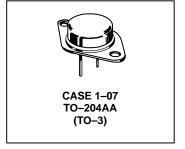


Figure 1. Power Derating

MJ15018 MJ15020\* MJ15019 MJ15021\*

\*ON Semiconductor Preferred Device

4.0 AMPERES
COMPLEMENTARY
SILICON
POWER TRANSISTORS
200 AND 250 VOLTS
150 WATTS



Preferred devices are ON Semiconductor recommended choices for future use and best overall value.

# MJ15018 MJ15020 MJ15019 MJ15021

# **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Sustaining Voltage (1) (I <sub>C</sub> = 100 mAdc, I <sub>B</sub> = 0)	MJ15018, MJ15019 MJ15020, MJ15021	V <sub>CEO(sus)</sub>	200 250	_	Vdc
Collector Cutoff Current $(V_{CE} = 150 \text{ Vdc}, I_B = 0)$ $(V_{CE} = 200 \text{ Vdc}, I_B = 0)$	MJ15018, MJ15019 MJ15020, MJ15021	I <sub>CEO</sub>		500 500	μAdc
Emitter Cutoff Current ( $V_{EB} = 7.0 \text{ Vdc}, I_{C} = 0$ )		I <sub>EBO</sub>	_	500	μAdc
SECOND BREAKDOWN					
Second Breakdown Collector Current with Base Forward–Biased (V <sub>CE</sub> = 50 Vdc, t = 0.5 s (non–repetitive)		I <sub>S/b</sub>	3.0	_	Adc
ON CHARACTERISTICS (1)					
DC Current Gain $ \begin{aligned} &(I_C = 1.0 \text{ Adc, } V_{CE} = 4.0 \text{ V}) \\ &(I_C = 3.0 \text{ Adc, } V_{CE} = 4.0 \text{ V}) \end{aligned} $		h <sub>FE</sub>	30 10	_	
Collector–Emitter Saturation Voltage (I <sub>C</sub> = 1.0 Adc, I <sub>B</sub> = 0.1 Adc)		V <sub>CE(sat)</sub>	_	1.0	Vdc
Base–Emitter on Voltage (I <sub>C</sub> = 1.0 Adc, V <sub>CE</sub> = 4.0 Vdc)		V <sub>BE(on)</sub>	_	2.0	Vdc
DYNAMIC CHARACTERISTICS					_
Current–Gain — Bandwidth Product $(I_C = 0.5 \text{ Adc}, V_{CE} = 10 \text{ Vdc}, f_{test} = 1.0 \text{ MHz})$		f <sub>T</sub>	20	_	MHz
Output Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, F <sub>test</sub> = 1.0 MHz)		C <sub>ob</sub>	_	500	pF

<sup>(1)</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu s,$  Duty Cycle  $\leq$  2%

# TYPICAL DYNAMIC CHARACTERISTICS

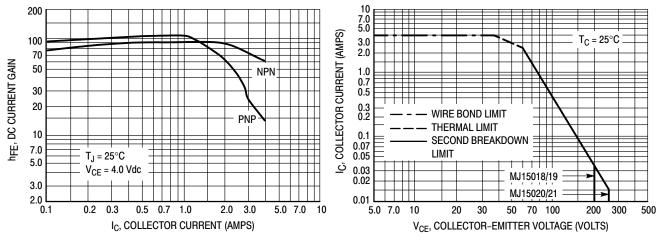


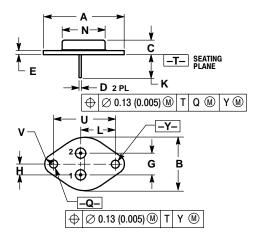
Figure 2. DC Current Gain

Figure 3. Maximum Rated Forward Biased Safe Operating Area

# MJ15018 MJ15020 MJ15019 MJ15021

# **PACKAGE DIMENSIONS**

**CASE 1-07** TO-204AA (TO-3) ISSUE Z



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. ALL RULES AND NOTES ASSOCIATED WITH REFERENCED TO-204AA OUTLINE SHALL APPLY.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	1.550 REF		39.37 REF		
В		1.050		26.67	
С	0.250	0.335	6.35	8.51	
D	0.038	0.043	0.97	1.09	
E	0.055	0.070	1.40	1.77	
G	0.430 BSC		10.92 BSC		
Н	0.215 BSC		5.46 BSC		
K	0.440	0.480	11.18	12.19	
L	0.665 BSC		16.89 BSC		
N		0.830		21.08	
Q	0.151	0.165	3.84	4.19	
U	1.187 BSC		30.15 BSC		
٧	0.131	0.188	3.33	4.77	

STYLE 1: PIN 1. BASE 2. EMITTER CASE: COLLECTOR

### MJ15018 MJ15020 MJ15019 MJ15021

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