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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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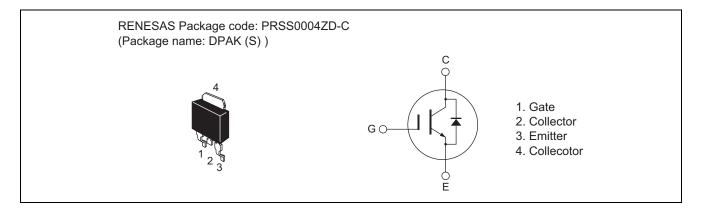
RJH60C9DPD

Silicon N Channel IGBT Application: Inverter

REJ03G1838-0100 Rev.1.00 Oct 14, 2009

Features

- High breakdown-voltage
- Low on-voltage
- Built-in diode



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Iten	1	Symbol	Ratings	Unit
Collector to emitter voltage	/ diode reverse voltage	V _{CES} / V _R	600	V
Gate to emitter voltage		V_{GES}	±30	V
Collector current	Tc = 25°C	lc	10	Α
	Tc = 100°C	lc	5	Α
Collector peak current		Ic(peak) Note1	20	Α
Collector to emitter diode fo	rward current	İ _{DF}	5	Α
Collector to emitter diode fo	rward peak current	i _{DF} (peak) Note1	20	Α
Collector dissipation		P _C Note2	45	W
Junction to case thermal im	pedance	θj-c Note2	2.78	°C/ W
Junction temperature		Tj	150	°C
Storage temperature		Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at Tc = 25°C

RJH60C9DPD Preliminary

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

ltem	Symbol	Min	Тур	Max	Unit	Test Conditions
Zero gate voltage collector current / diode reverse current	I _{CES} / I _R	_	_	1.0	μА	V _{CE} = 600 V, V _{GE} = 0V
Gate to emitter leak current	I _{GES}	_	_	±100	nA	V _{GE} = ±30 V, V _{CE} = 0V
Gate to emitter cutoff voltage	$V_{GE(off)}$	4.0	6.0	8.0	V	V _{CE} = 10 V, I _C = 1 mA
Collector to emitter saturation voltage	V _{CE(sat)}	_	1.9	2.5	V	Tc = 25°C I _C = 5 A, V _{GE} = 15 V Note3
	V _{CE(sat)}	_	2.0		V	Tc = 100°C I _C = 5 A, V _{GE} = 15 V ^{Note3}
Input capacitance	Cies	_	180	_	pF	V _{CE} = 25 V
Output capacitance	Coes	_	19	_	pF	$V_{GE} = 0V$
Reveres transfer capacitance	Cres	_	7	_	pF	f = 1 MHz
Total gate charge	Qg	_	8.0	_	nC	V _{GE} = 15 V
Gate to emitter charge	Qge	_	5.0	_	nC	V _{CE} = 300 V
Gate to collector charge	Qgc	_	2.5	_	nC	I _C = 5 A
Switching time	t _{d(on)}	_	25	_	ns	I _C = 5 A
	t _r	_	50	_	ns	$R_L = 37.5 \Omega$
	t _{d(off)}	_	40	_	ns	V _{GE} = 15 V
	t _f	_	250		ns	Rg = 5 Ω
EDD Forward voltage	\/-		1.0	2.3	\/	L ₋ - 5 Λ Note3

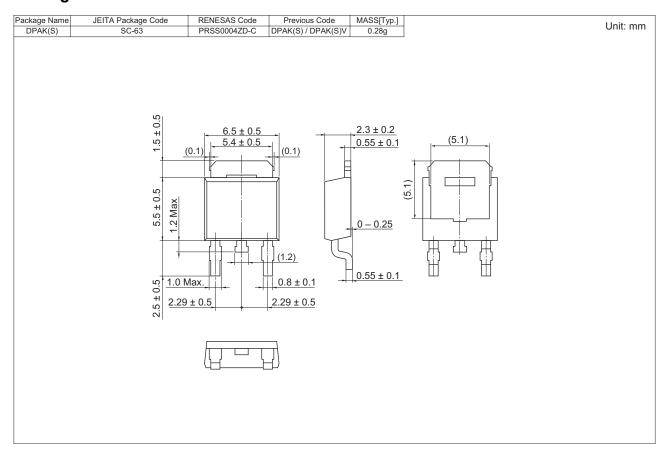
FRD Forward voltage	V_{F}	_	1.8	2.3	V	$I_F = 5 A^{\text{Note3}}$
FRD reverse recovery time	t _{rr}	_	100	_	ns	$I_F = 5 \text{ A}, \text{ di}_F/\text{dt} = 100 \text{ A}/\mu\text{s}$

Notes: 3. Pulse test.

^{4.} Under development. —The specifications potentially be changed without notice.

RJH60C9DPD Preliminary

Package Dimension



Ordering Information

Part No. Q		Quantity	Shipping Container		
	RJH60C9DPD-00-J2	3000 pcs	Taping		

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