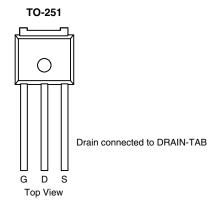


SUU10P10-195

Vishay Siliconix

P-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω) Max.	I _D (A)	Q _g (Typ.)	
	0.195 at V _{GS} = - 10 V	- 8.8		
- 100	0.200 at V _{GS} = - 7.5 V	- 8.7	12	
	0.207 at V _{GS} = - 6 V	- 8.6		

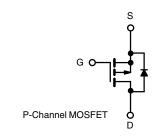


FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_a and UIS Tested
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- DC/DC Converters
- Motor Control



Ordering Information:

SUU10P10-195-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	(T _C = 25 °C, unless otl	nerwise noted)			
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	- 100	V	
Gate-Source Voltage		V _{GS}	± 20	- V	
Continuous Drain Current (T ₁ = 150 °C)	T _C = 25 °C	1-	- 8.8		
Continuous Drain Current $(1) = 150^{\circ}$ C)	T _C = 70 °C	I _D	- 7.1	А	
Pulsed Drain Current		I _{DM}	- 15	A	
Avalanche Current		I _{AS}	- 18		
Single Avalanche Energy ^a	L = 0.1 mH	E _{AS}	16.2	mJ	
	T _C = 25 °C	D	32.1 ^b		
Maximum Power Dissipation ^a	T _A = 25 °C ^c	– P _D –	2.5	W	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Limit	Unit		
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	50	°C/W		
Junction-to-Case (Drain)	R _{thJC}	3.9	0/11		

Notes:

a. Duty cycle \leq 1 %.

b. See SOA curve for voltage derating.

c. When mounted on 1" square PCB (FR-4 material).

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{DS} = 0 V, I_D = -250 \mu A$	- 100			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1.5		- 3.5	V
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 250	nA
Zero Gate Voltage Drain Current		V _{DS} = - 100 V, V _{GS} = 0 V			- 1	
	I _{DSS}	V_{DS} = - 100 V, V_{GS} = 0 V, T_{J} = 125 °C			- 50	μA
		$V_{DS} = -100 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 150 \text{ °C}$			- 250	1
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le$ - 10 V, V_{GS} = - 10 V	- 15			Α
		V _{GS} = - 10 V, I _D = - 3.6 A		0.162	0.195	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 7.5 V, I _D = - 3.5 A		0.166	0.200	Ω
		V _{GS} = - 6 V, I _D = - 3.5 A		0.172	0.207	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 20 V, I _D = - 3.6 A		12		S
Dynamic ^b		· · · · · · · · · · · · · · · · · · ·				
Input Capacitance	C _{iss}			1110		pF
Output Capacitance	C _{oss}	V _{GS} = 0 V, V _{DS} = - 50 V, f = 1 MHz		64		
Reverse Transfer Capacitance	C _{rss}			40		
		$V_{DS} = -50 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -3.6 \text{ A}$		23.5 35.3		
Total Gate Charge ^c	Qg			12	18	nC
Gate-Source Charge ^c	Q _{gs}	V_{DS} = - 50 V, V_{GS} = - 4.5 V, I_{D} = - 3.6 A		4		
Gate-Drain Charge ^c	Q _{gd}			5.3		
Gate Resistance	Rg	f = 1 MHz	1.3	6.5	13	Ω
Turn-On Delay Time ^c	t _{d(on)}			6	12	
Rise Time ^c	t _r	V_{DD} = - 50 V, R_L = 17.2 Ω		9	18	- ns
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong$ - 2.9 A, V_{GEN} = - 10 V, R_g = 1 Ω		35	53	
Fall Time ^c	t _f			10	20	
Drain-Source Body Diode Ratings an	d Characteri	stics T _C = 25 °C ^b				
Continuous Current	۱ _S				- 8.8	^
Pulsed Current	I _{SM}				- 15	A
Forward Voltage ^a	V _{SD}	I _F = - 2.9 A, V _{GS} = 0 V		- 0.83	- 1.5	V
Reverse Recovery Time	t _{rr}			46	69	ns
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = - 2.9 A, dl/dt = 100 A/μs		- 4.5	- 6.8	Α
Reverse Recovery Charge	Q _{rr}	1 1		98	147	nC

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



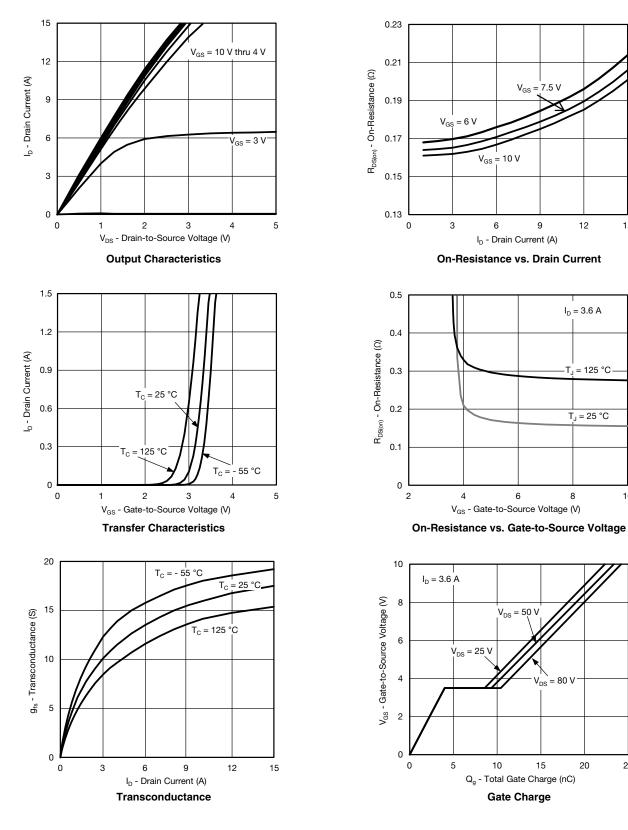
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



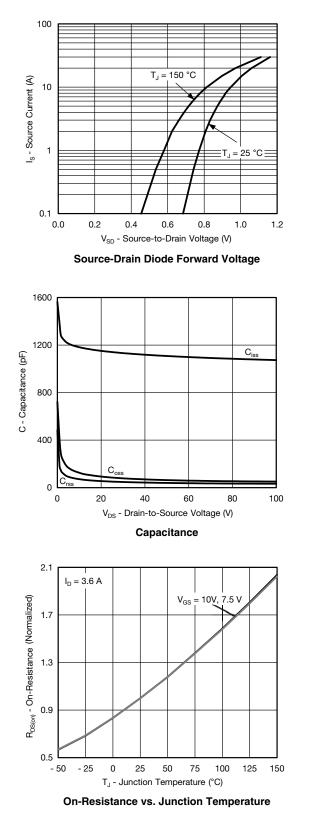
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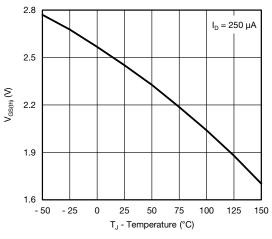
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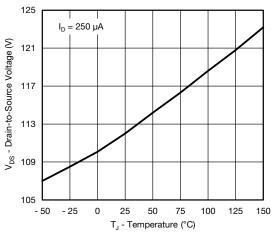
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

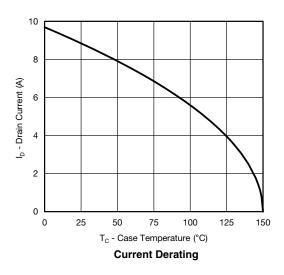




Threshold Voltage



Drain Source Breakdown vs. Junction Temperature



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0.2

0.1 0.05

0.1 10-4 0.02 111

10⁻³

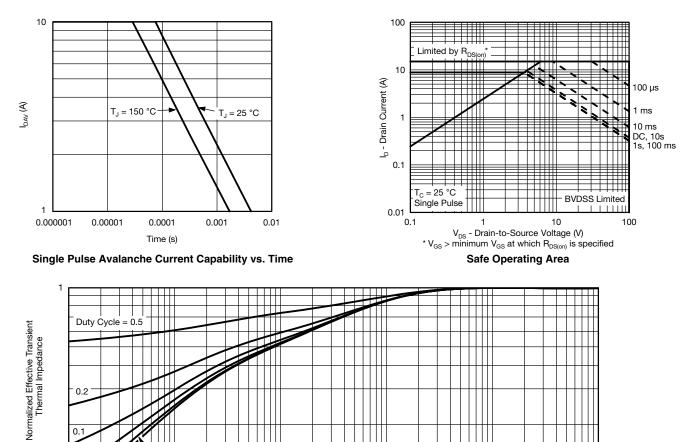
10-2

S nale Pulse

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



10-1

Square Wave Pulse Duration (s) Normalized Thermal Transient Impedance, Junction-to-Case

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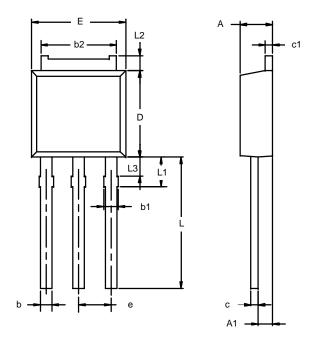
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Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?63455.



Package Information Vishay Siliconix

TO-251AA (DPAK)



Note: Dimension L3 is for reference only.

	MILLIN	IETERS	INC	IES
Dim	Min	Max	Min	Max
Α	2.21	2.38	0.087	0.094
A1	0.89	1.14	0.035	0.045
b	0.71	0.89	0.028	0.035
b1	0.76	1.14	0.030	0.045
b2	5.23	5.43	0.206	0.214
С	0.46	0.58	0.018	0.023
c1	0.46	0.58	0.018	0.023
D	5.97	6.22	0.235	0.245
Е	6.48	6.73	0.255	0.265
е	2.28 BSC		0.090 BSC	
L	8.89	9.53	0.350	0.375
L1	1.91	2.28	0.075	0.090
L2	0.89	1.27	0.035	0.050
L3	1.15	1.52	0.045	0.060
ECN: S-0 DWG: 53	3946—Rev. E 46	, 09-Jul-01	•	•



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