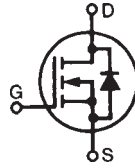


**Polar™**  
**Power MOSFET**

**IXTA06N120P**  
**IXTP06N120P**

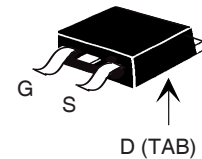
$V_{DSS} = 1200V$   
 $I_{D25} = 0.6A$   
 $R_{DS(on)} \leq 34\Omega$

N-Channel Enhancement Mode  
Avalanche Rated

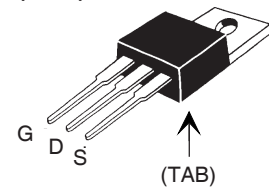


Symbol	Test Conditions	Maximum Ratings	
$V_{DSS}$	$T_J = 25^\circ C$ to $150^\circ C$	1200	V
$V_{DGR}$	$T_J = 25^\circ C$ to $150^\circ C$ , $R_{GS} = 1M\Omega$	1200	V
$V_{GSS}$	Continuous	$\pm 20$	V
$V_{GSM}$	Transient	$\pm 30$	V
$I_{D25}$	$T_C = 25^\circ C$	0.6	A
$I_{DM}$	$T_C = 25^\circ C$ , Pulse Width Limited by $T_{JM}$	1.2	A
$I_A$	$T_C = 25^\circ C$	0.6	A
$E_{AS}$	$T_C = 25^\circ C$	50	mJ
$dV/dt$	$I_S \leq I_{DM}$ , $V_{DD} \leq V_{DSS}$ , $T_J \leq 150^\circ C$	10	V/ns
$P_D$	$T_C = 25^\circ C$	42	W
$T_J$		-55 ... +150	$^\circ C$
$T_{JM}$		150	$^\circ C$
$T_{stg}$		-55 ... +150	$^\circ C$
$T_L$	1.6mm (0.062) from Case for 10s	300	$^\circ C$
$T_{SOLD}$	Plastic Body for 10s	260	$^\circ C$
$M_d$	Mounting Torque (TO-220)	1.13 / 10	Nm/lb.in.
<b>Weight</b>	TO-263	2.5	g
	TO-220	3.0	g

TO-263 (IXTA)



TO-220 (IXTP)



G = Gate      D = Drain  
S = Source      TAB = Drain

**Features**

- International Standard Packages
- Avalanche Rated
- Low Package Inductance

**Advantages**

- Easy to Mount
- Space Savings
- High Power Density

**Applications**

- High Voltage Switched-Mode and Resonant-Mode Power Supplies
- High Voltage Pulse Power Applications
- High Voltage Discharge Circuits in Lasers Pulsers, Spark Igniters, RF Generators
- High Voltage DC-DC Converters
- High Voltage DC-AC Inverters

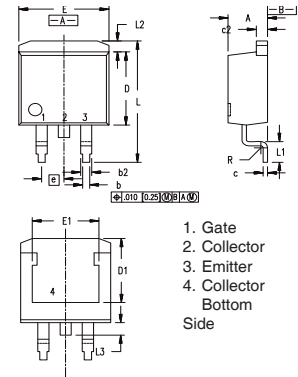
Symbol	Test Conditions ( $T_J = 25^\circ C$ , Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
$BV_{DSS}$	$V_{GS} = 0V$ , $I_D = 250\mu A$	1200		V
$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 50\mu A$	2.0		V
$I_{GSS}$	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$			$\pm 50$ nA
$I_{DSS}$	$V_{DS} = V_{DSS}$ , $V_{GS} = 0V$ $T_J = 125^\circ C$			3 $\mu A$ 125 $\mu A$
$R_{DS(on)}$	$V_{GS} = 10V$ , $I_D = 0.5 \cdot I_{D25}$ , Note 1	27	34	$\Omega$

Symbol	Test Conditions ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
$g_{fs}$	$V_{DS} = 30\text{V}$ , $I_D = 0.5 \cdot I_{D25}$ , Note 1	0.28	0.45	S
$C_{iss}$ $C_{oss}$ $C_{rss}$	$V_{GS} = 0\text{V}$ , $V_{DS} = 25\text{V}$ , $f = 1\text{MHz}$		236	pF
			15	pF
			3.2	pF
$t_{d(on)}$ $t_r$ $t_{d(off)}$ $t_f$	<b>Resistive Switching Times</b> $V_{GS} = 10\text{V}$ , $V_{DS} = 0.5 \cdot V_{DSS}$ , $I_D = 0.5 \cdot I_{D25}$ , $R_G = 50\Omega$ (External)		19	ns
			37	ns
			35	ns
			34	ns
$Q_{g(on)}$ $Q_{gs}$ $Q_{gd}$	$V_{GS} = 10\text{V}$ , $V_{DS} = 0.5 \cdot V_{DSS}$ , $I_D = 0.5 \cdot I_{D25}$		13.3	nC
			2.4	nC
			7.8	nC
$R_{thJC}$ $R_{thCS}$	(TO-220)	0.50	3.0	$^\circ\text{C/W}$ $^\circ\text{C/W}$

**Source-Drain Diode**

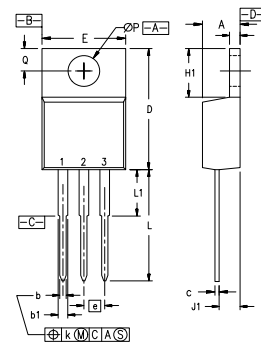
Symbol	Test Conditions ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
$I_S$	$V_{GS} = 0\text{V}$			0.6 A
$I_{SM}$	Repetitive, Pulse Width Limited by $T_{JM}$			1.8 A
$V_{SD}$	$I_F = I_S$ , $V_{GS} = 0\text{V}$ , Note 1			1.5 V
$t_{rr}$	$I_F = 0.6\text{A}$ , $-di/dt = 100\text{A}/\mu\text{s}$ $V_R = 100\text{V}$ , $V_{GS} = 0\text{V}$		900	ns

Note 1. Pulse test,  $t \leq 300 \mu\text{s}$ ; duty cycle,  $d \leq 2\%$ .

**TO-263 (IXTA) Outline**


1. Gate
2. Collector
3. Emitter
4. Collector Bottom Side

Dim.	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	4.06	4.83	.160	.190
b	0.51	0.99	.020	.039
b2	1.14	1.40	.045	.055
c	0.40	0.74	.016	.029
c2	1.14	1.40	.045	.055
D	8.64	9.65	.340	.380
D1	8.00	8.89	.280	.320
E	9.65	10.41	.380	.405
E1	6.22	8.13	.270	.320
e	2.54	BSC	.100	BSC
L	14.61	15.88	.575	.625
L1	2.29	2.79	.090	.110
L2	1.02	1.40	.040	.055
L3	1.27	1.78	.050	.070
L4	0	0.13	0	.005

**TO-220 (IXTP) Outline**


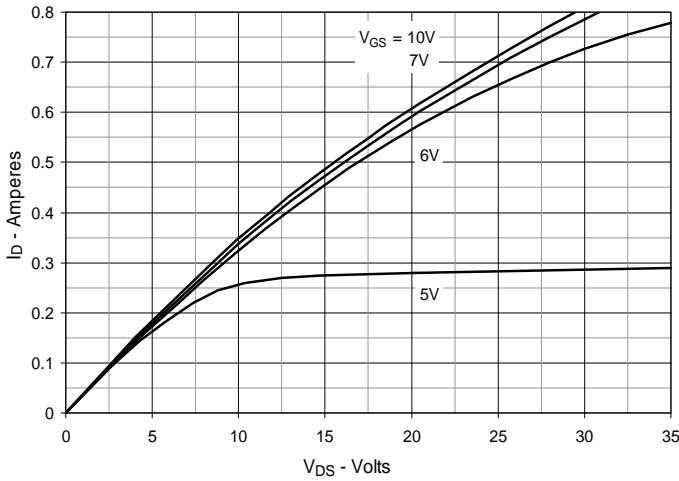
Pins: 1 - Gate 2 - Drain

SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.170	.190	4.32	4.83
b	.025	.040	0.64	1.02
b1	.045	.065	1.15	1.65
c	.014	.022	0.35	0.56
D	.580	.630	14.73	16.00
E	.390	.420	9.91	10.66
e	.100 BSC		2.54 BSC	
F	.045	.055	1.14	1.40
H1	.230	.270	5.85	6.85
J1	.090	.110	2.29	2.79
k	0	.015	0	0.38
L	.500	.550	12.70	13.97
L1	.110	.230	2.79	5.84
ØP	.139	.161	3.53	4.08
Q	.100	.125	2.54	3.18

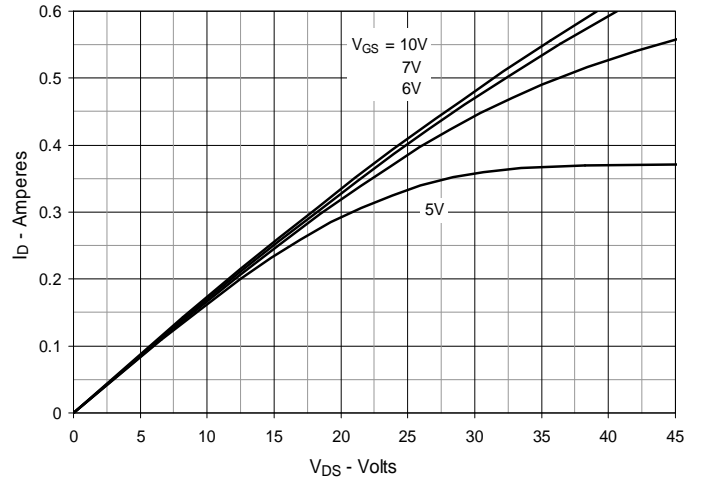
IXYS Reserves The Right to Change Limits, Test Conditions, and Dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents:	4,835,592	4,931,844	5,049,961	5,237,481	6,162,665	6,404,065 B1	6,683,344	6,727,585	7,005,734 B2	7,157,338B2
	4,850,072	5,017,508	5,063,307	5,381,025	6,259,123 B1	6,534,343	6,710,405 B2	6,759,692	7,063,975 B2	
	4,881,106	5,034,796	5,187,117	5,486,715	6,306,728 B1	6,583,505	6,710,463	6,771,478 B2	7,071,537	

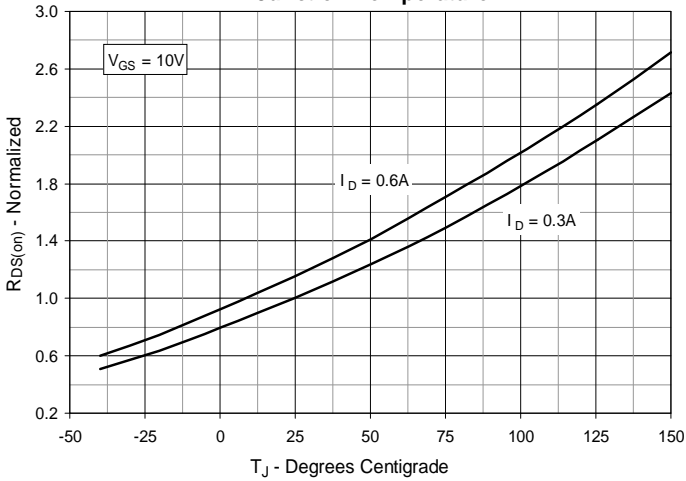
**Fig. 1. Output Characteristics**  
@ 25°C



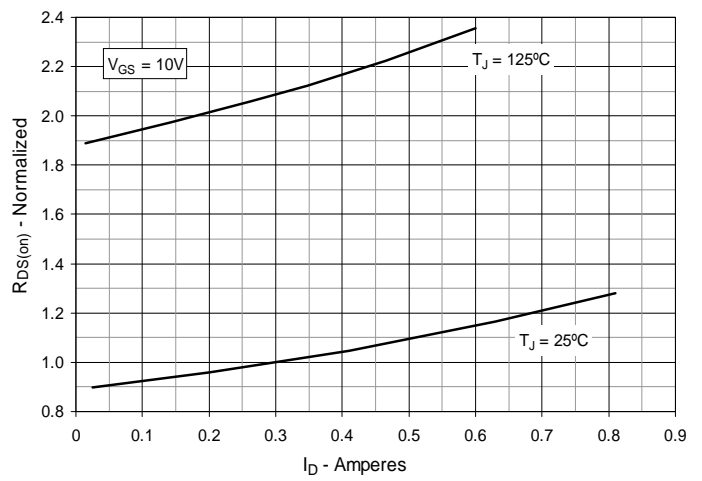
**Fig. 2. Output Characteristics**  
@ 125°C



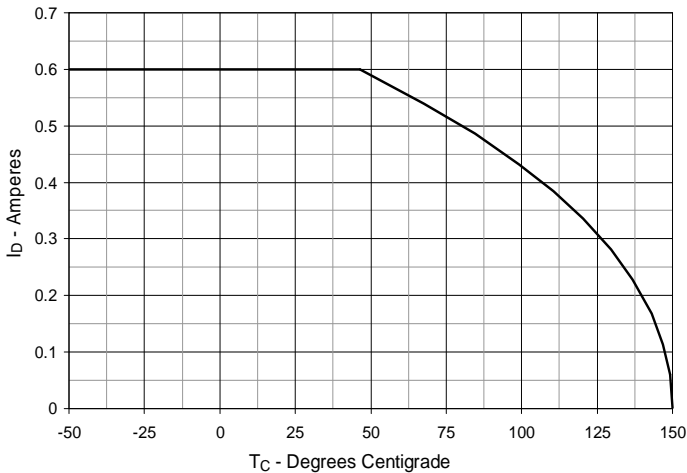
**Fig. 3. R<sub>DS(on)</sub> Normalized to I<sub>D</sub> = 0.3A Value vs. Junction Temperature**



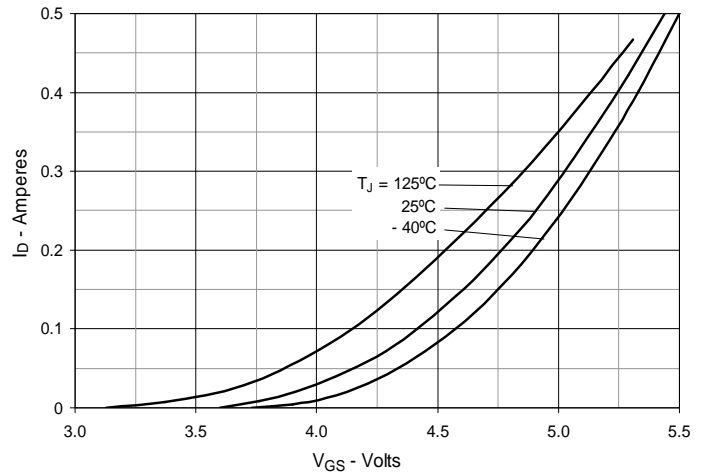
**Fig. 4. R<sub>DS(on)</sub> Normalized to I<sub>D</sub> = 0.3A Value vs. Drain Current**



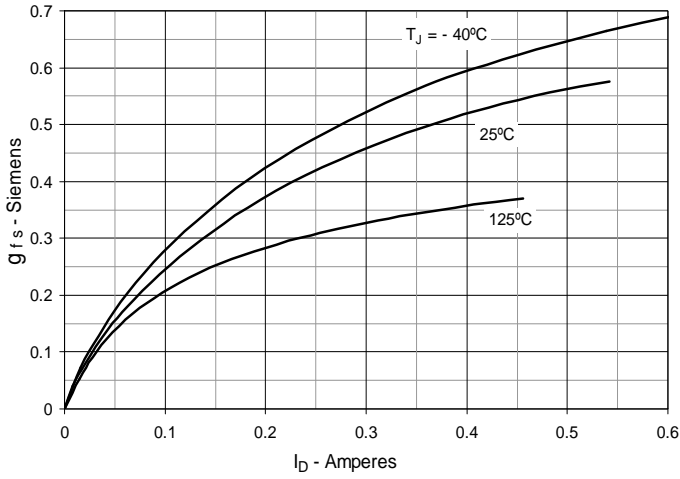
**Fig. 5. Maximum Drain Current vs. Case Temperature**



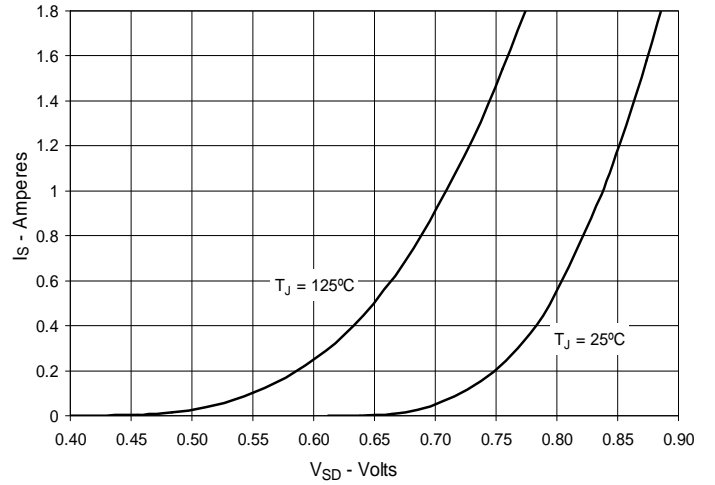
**Fig. 6. Input Admittance**



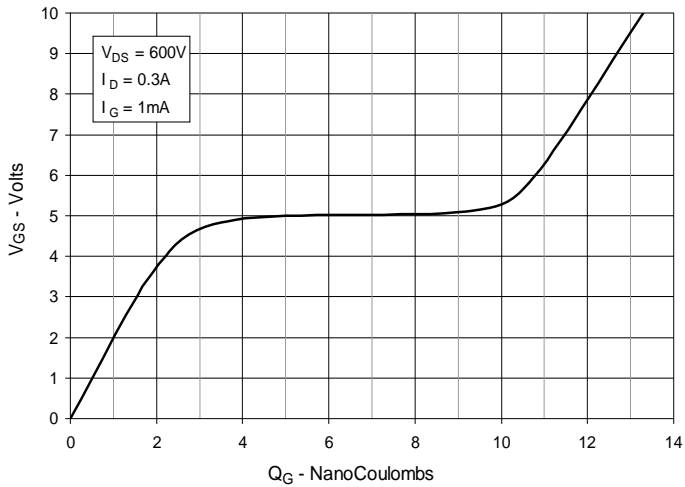
**Fig. 7. Transconductance**



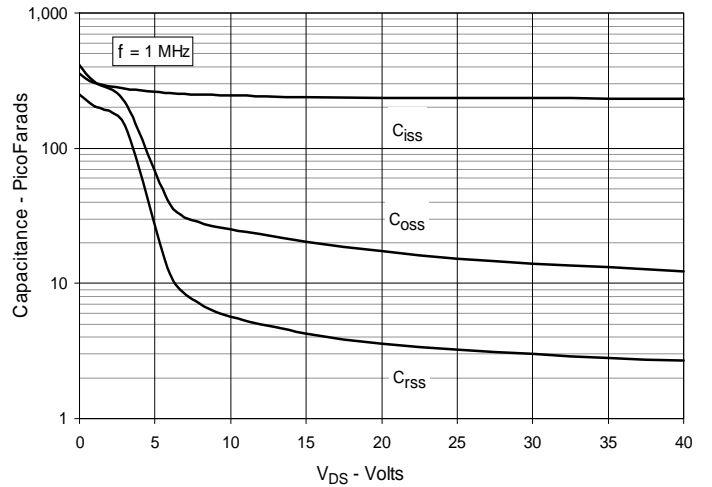
**Fig. 8. Forward Voltage Drop of Intrinsic Diode**



**Fig. 9. Gate Charge**



**Fig. 10. Capacitance**



**Fig. 11. Maximum Transient Thermal Impedance**

