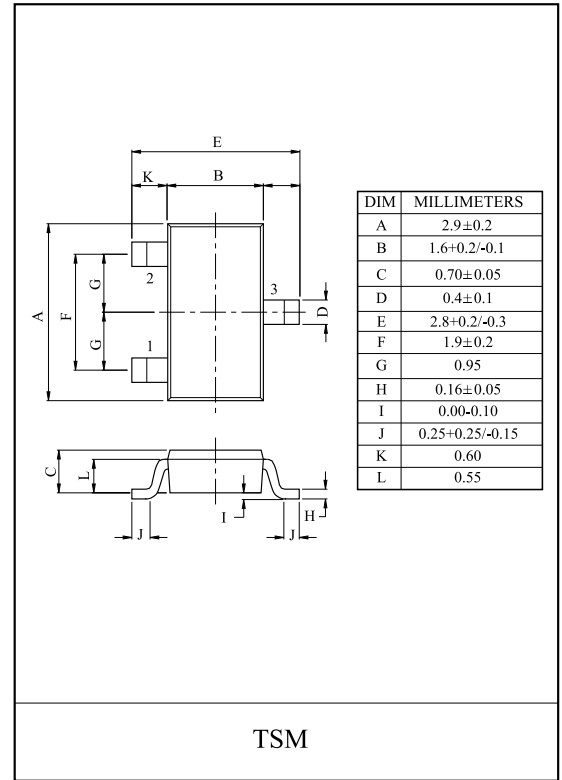


General Description

This Trench MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for Load switch and Back-Light Inverter.

FEATURES

- $V_{DSS}=40V$, $I_D=3.9A$
- Drain-Source ON Resistance
 $R_{DS(ON)}=45m$ (Max.) @ $V_{GS}=10V$
 $R_{DS(ON)}=58m$ (Max.) @ $V_{GS}=4.5V$
- Super High Dense Cell Design

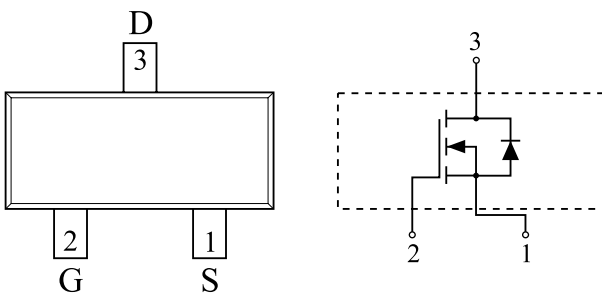


MAXIMUM RATING (Ta=25 °C)

CHARACTERISTIC	SYMBOL	N-Ch	UNIT
Drain-Source Voltage	V_{DSS}	40	V
Gate-Source Voltage	V_{GSS}	± 20	V
Drain Current	DC@Ta=25	3.9	A
	DC@Ta=70	3.1	
	Pulsed	I_{DP}	
Drain-Source-Diode Forward Current	I_S	0.8	A
Drain Power Dissipation	Ta=25	1.25	W
	Ta=70	0.8	
Maximum Junction Temperature	T_j	150	
Storage Temperature Range	T_{stg}	-55 150	
Thermal Resistance, Junction to Ambient	R_{thJA}	100	/W

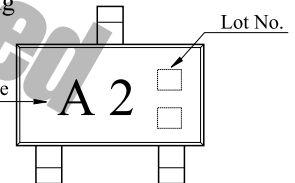
Note > *Surface Mounted on 1 × 1 FR4 Board, t = 5sec

PIN CONNECTION (TOP VIEW)



Marking

Type Name



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ELECTRICAL CHARACTERISTICS (Ta=25)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_{DS}=250\mu A$	40	-	-	V
Drain Cut-off Current	I_{DSS}	$V_{GS}=0V, V_{DS}=32V$	-	-	0.5	μA
		$V_{GS}=0V, V_{DS}=32V, T_j=55$	-	-	10	
Gate Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
Gate Threshold Voltage	V_{th}^*	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	-	3.0	V
Drain-Source ON Resistance	$R_{DS(ON)}^*$	$V_{GS}=10V, I_D=3.9A$	-	29	45	m
		$V_{GS}=4.5V, I_D=3.5A$	-	42	58	
Forward Transconductance	g_{fs}^*	$V_{DS}=10V, I_D=3.9A$	-	11	-	S
Dynamic						
Input Capacitance	C_{iss}	$V_{DS}=20V, f=1MHz, V_{GS}=0V$	-	446	-	pF
Output Capacitance	C_{oss}		-	78	-	
Reverse Transfer Capacitance	C_{rss}		-	40	-	
Total Gate Charge	Q_g^*	$V_{DS}=20V, V_{GS}=10V, I_D=3.9A$	-	9.3	-	nC
Gate-Source Charge	Q_{gs}^*		-	1.8	-	
Gate-Drain Charge	Q_{gd}^*		-	2.0	-	
Turn-On Delay Time	$t_{d(on)}^*$	$V_{DD}=20V, V_{GS}=10V$ $I_D=1A, R_G=6$	-	10.3	-	ns
Turn-On Rise Time	t_r^*		-	5.4	-	
Turn-Off Delay Time	$t_{d(off)}^*$		-	28.2	-	
Turn-Off Fall Time	t_f^*		-	4.0	-	
Source-Drain Diode Ratings						
Source-Drain Forward Voltage	V_{SDF}^*	$V_{GS}=0V, I_S=1A$	-	0.8	1.2	V
Note > *Pulse Test : Pulse width <300 μs , Duty cycle < 2%						

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Fig1. $I_D - V_{DS}$

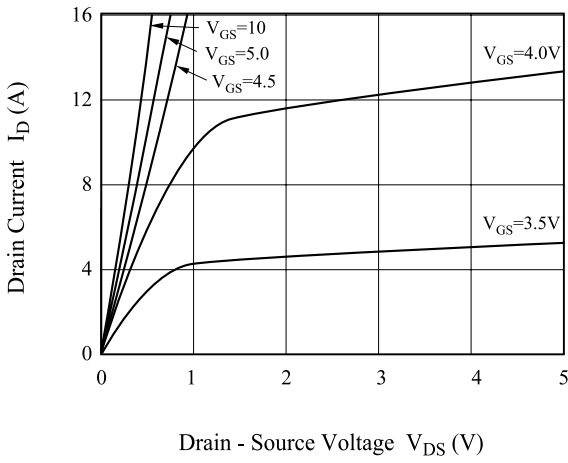


Fig2. $R_{DS(ON)} - I_D$

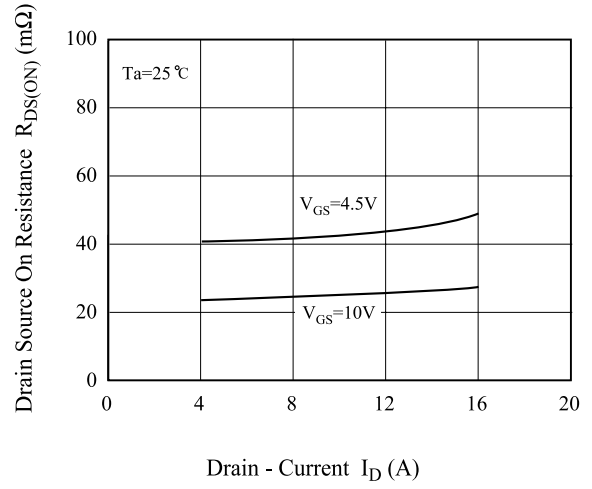


Fig3. $I_D - V_{GS}$

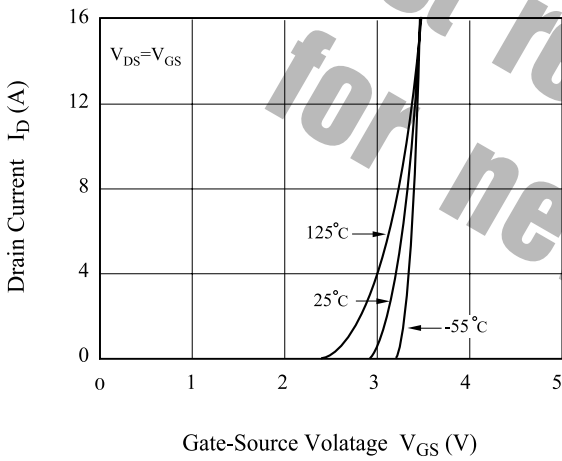


Fig4. $R_{DS(ON)} - T_j$

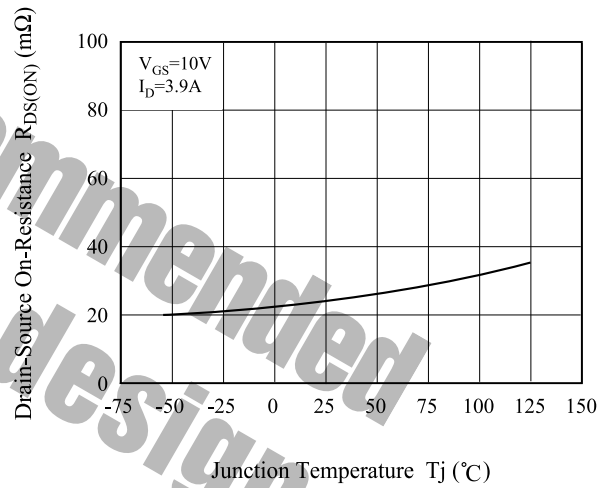


Fig5. $V_{th} - T_j$

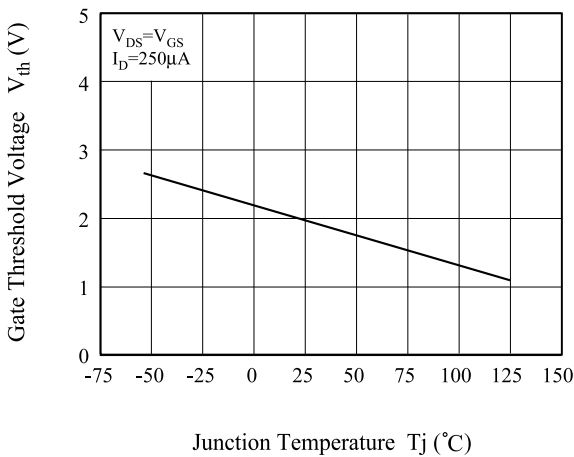
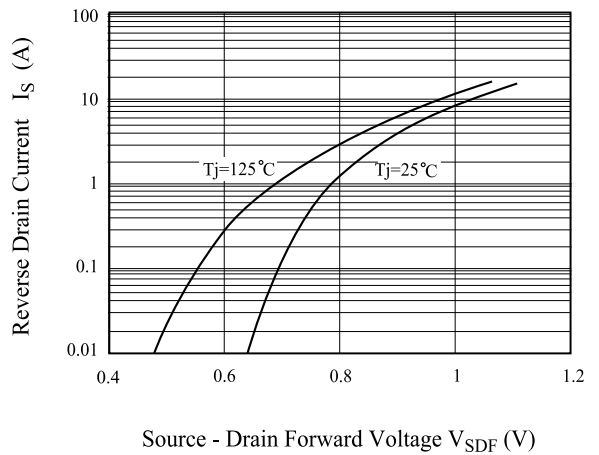


Fig6. $I_S - V_{SDF}$



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Fig7. $V_{GS} - Q_g$

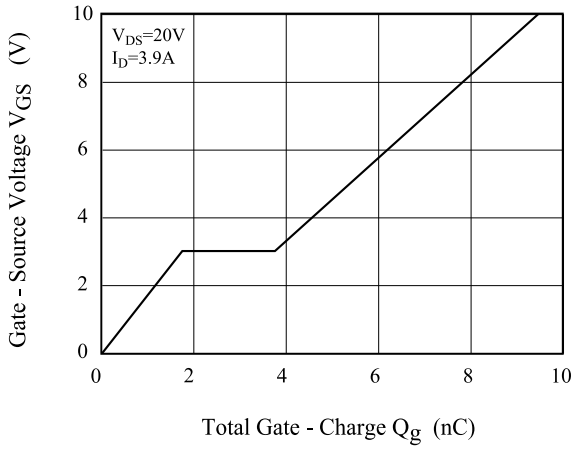


Fig8. $C - V_{DS}$

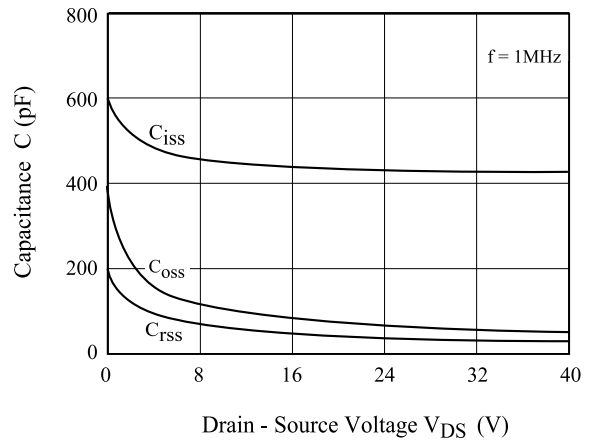


Fig9. Safe Operation Area

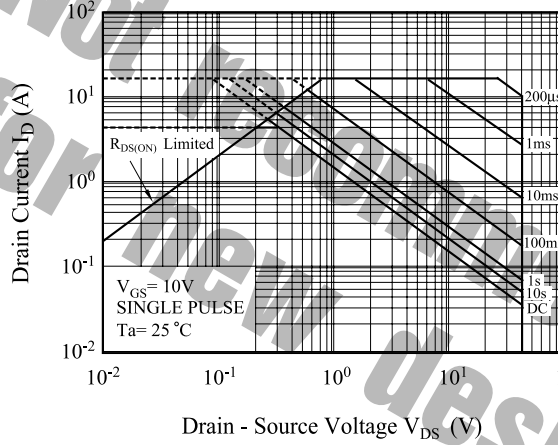
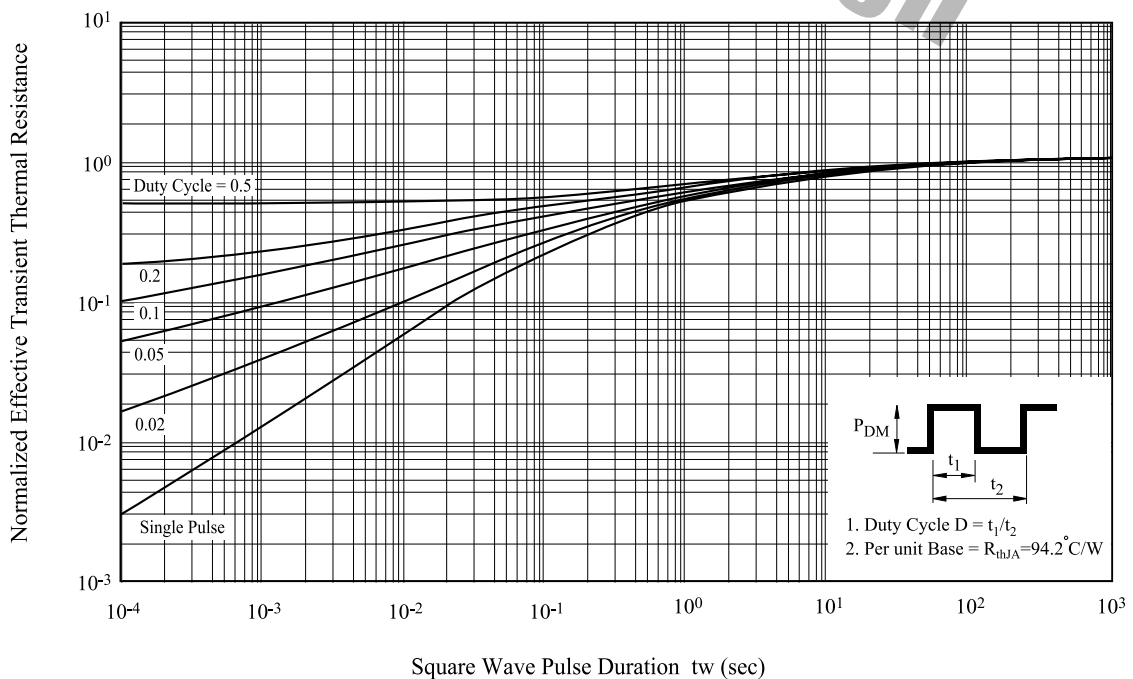


Fig10. Transient Thermal Response Curve



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Fig11. Gate Charge Circuit and Wave Form

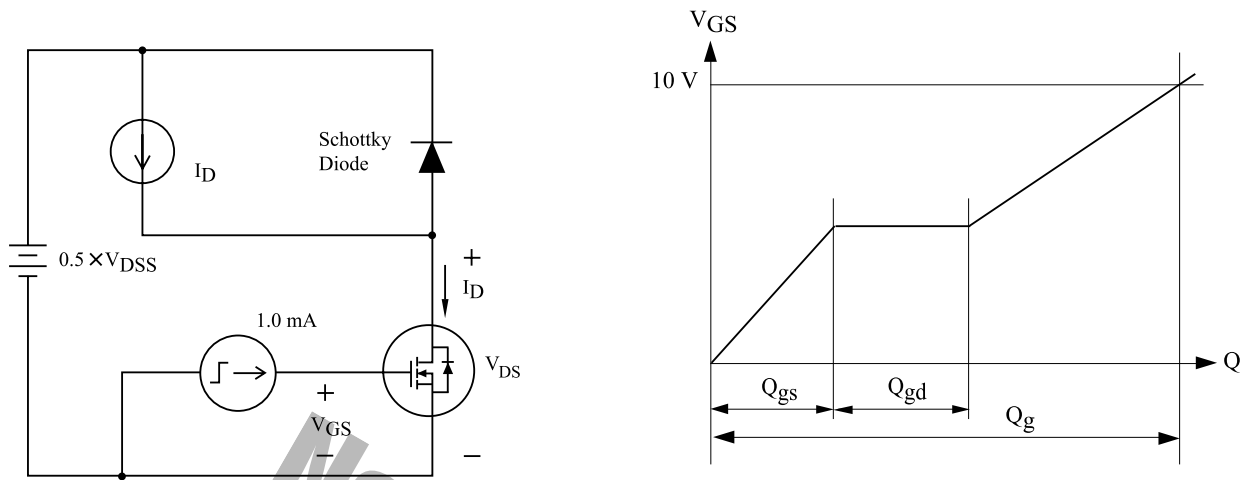


Fig12. Resistive Load Switching

