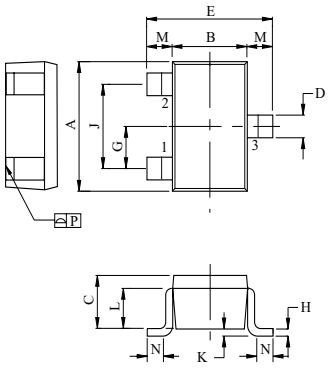
KTK5132U

N CHANNEL MOS FIELD EFFECT TRANSISTOR

ULTRA-HIGH SPEED SWITCHING APPLICATIONS
ANALOG SWITCH APPLICATIONS

FEATURES

- 2.5 Gate Drive.
- Low Threshold Voltage : V_{th}=0.5 1.5V.
- High Speed.
- Small Package.
- Enhancement-Mode.



DIM	MILLIMETERS
A	2.00±0.20
B	1.25±0.15
C	0.90±0.10
D	0.3+0.10/-0.05
E	2.10±0.20
G	0.65
H	0.15+0.1/-0.06
J	1.30
K	0.00~0.10
L	0.70
M	0.42±0.10
N	0.10 MIN
P	0.1 MAX

1. SOURCE
2. GATE
3. DRAIN

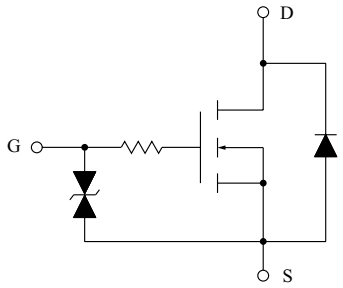
USM

MAXIMUM RATING (Ta=25℃)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GSS}	±20	V
DC Drain Current	I_D	100	mA
Drain Power Dissipation	P_D*	200	mW
Channel Temperature	T_{ch}	150	℃
Storage Temperature Range	T_{stg}	-55 150	℃

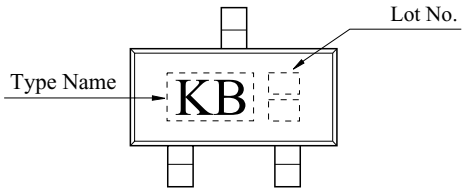
Note) * Package Mounted On 99.5% Alumina 10×8×0.6㎜)

EQUIVALENT CIRCUIT



THIS TRANSISTOR IS ELECTROSTATIC SENSITIVE DEVICE.
PLEASE HANDLE WITH CAUTION.

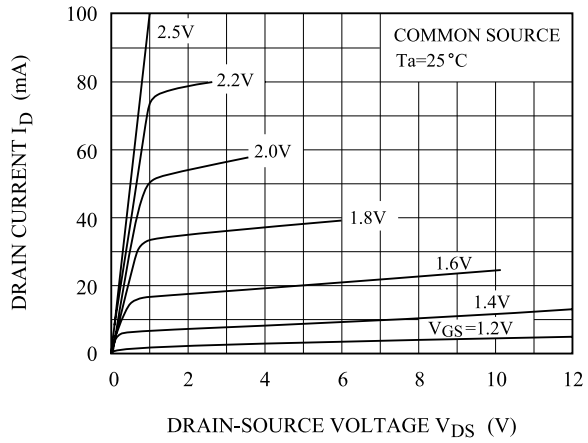
Marking



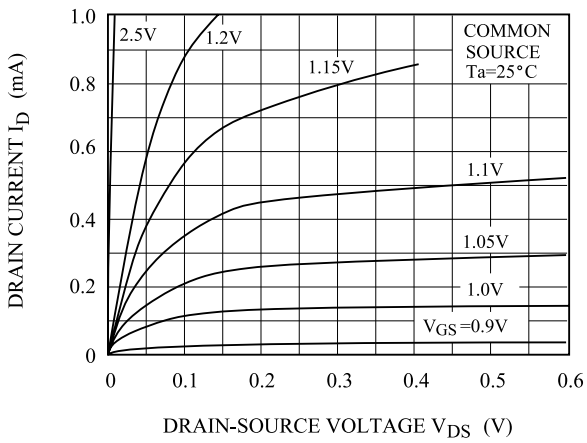
ELECTRICAL CHARACTERISTICS (Ta=25℃)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage Current		I_{GSS}	V_{GS}=±16V, V_{DS}=0V	-	-	±1	μA
Drain-Source Breakdown Voltage		$V_{(BR)DSS}$	I_D=100 μA, V_{GS}=0V	30	-	-	V
Drain Cut-off Current		I_{DSS}	V_{DS}=30V, V_{GS}=0V	-	-	1	μA
Gate Threshold Voltage		V_{th}	V_{DS}=3V, I_D=0.1mA	0.5	-	1.5	V
Forward Transfer Admittance		$\|Y_{fs}\|$	V_{DS}=3V, I_D=10mA	25	-	-	mS
Drain-Source ON Resistance		$R_{DS(ON)}$	I_D=10mA, V_{GS}=2.5V	-	4	7	Ω
Input Capacitance		C_{iss}	V_{DS}=3V, V_{GS}=0V, f=1MHz	-	8.5	-	pF
Reverse Transfer Capacitance		C_{rss}	V_{DS}=3V, V_{GS}=0V, f=1MHz	-	3.3	-	pF
Output Capacitance		C_{oss}	V_{DS}=3V, V_{GS}=0V, f=1MHz	-	9.3	-	pF
Switching Time	Turn-on Time	t_{on}	V_{DD}=5V, I_D=10mA, V_{GS}=0 5V	-	50	-	nS
	Turn-off Time	t_{off}		-	180	-	nS

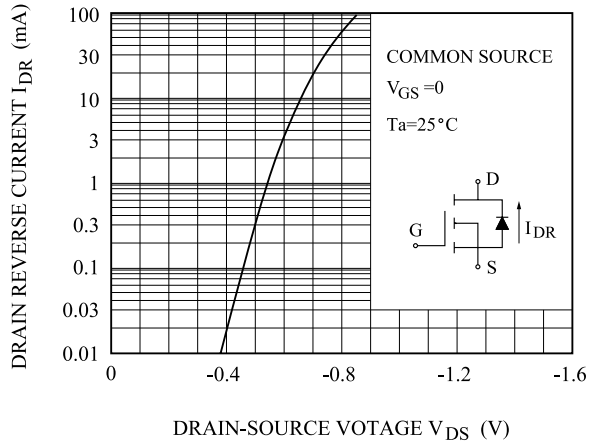
I_D - V_{DS}



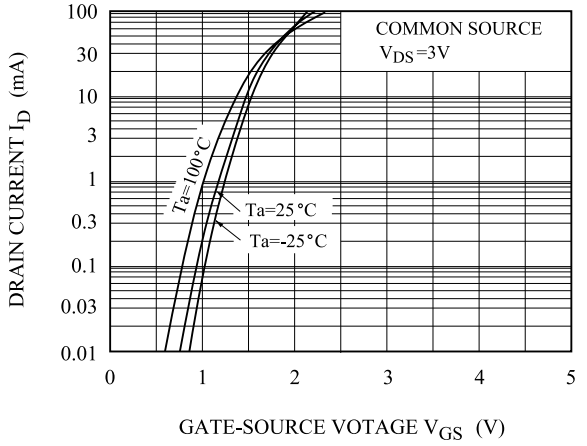
I_D - V_{DS}
(LOW VOLTAGE REGION)



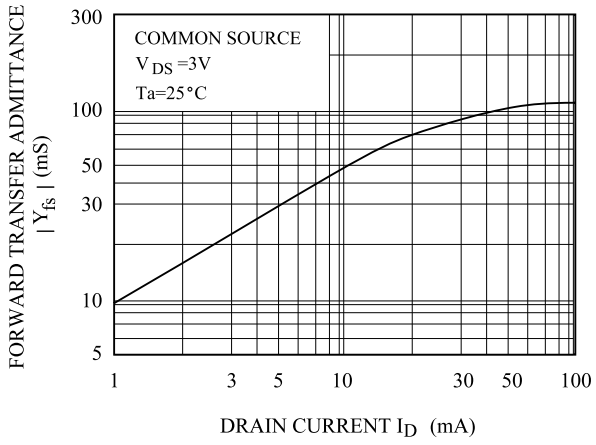
I_{DR} - V_{DS}



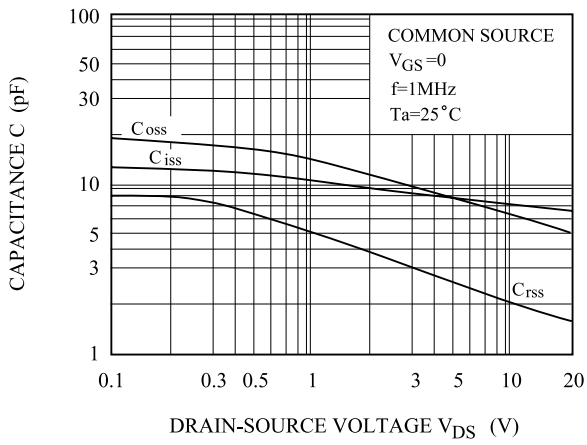
I_D - V_{GS}



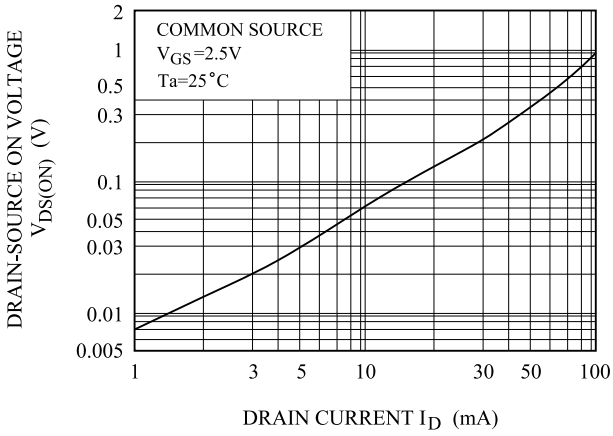
$|Y_{fs}|$ - I_D



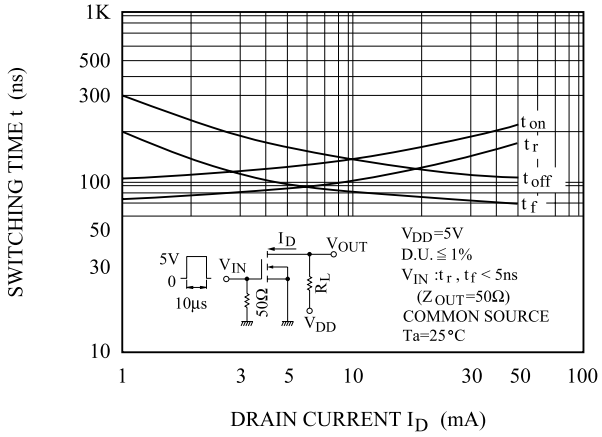
C - V_{DS}



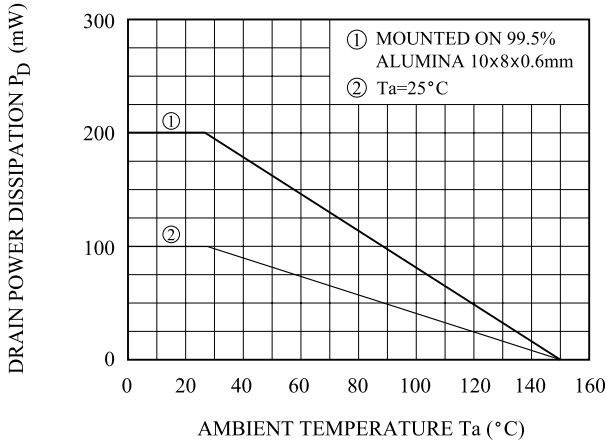
$V_{DS(ON)}$ - I_D



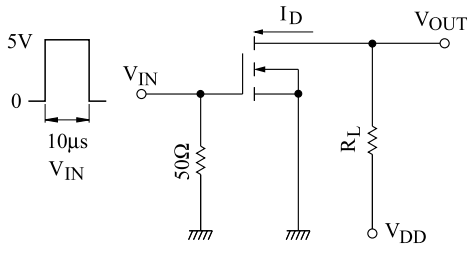
t - I_D



P_D - Ta



SWITCHING TIME TEST CIRCUIT



V_{DD} =5V
D.U. ≦ 1%
V_{IN} :t_r , t_f < 5ns
(Z_{OUT} =50Ω)
COMMON SOURCE
Ta=25°C