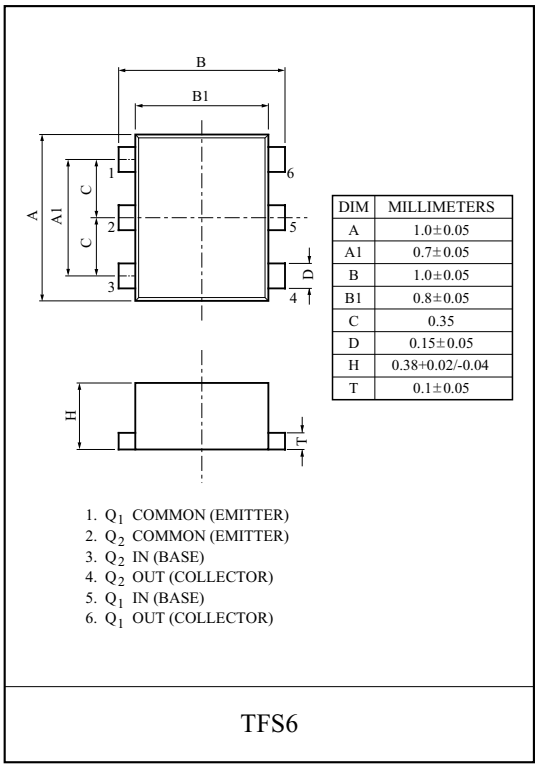
KRA721F~KRA724F

EPITAXIAL PLANAR PNP TRANSISTOR

SWITCHING APPLICATION.
INTERFACE CIRCUIT AND DRIVER CIRCUIT APPLICATION.

FEATURES

- With Built-in Bias Resistors.
- Simplify Circuit Design.
- Reduce a Quantity of Parts and Manufacturing Process.
- High Packing Density.
- Thin Fine Pitch Super mini 6 pin Package.

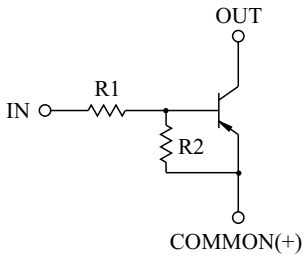


DIM	MILLIMETERS
A	1.0±0.05
A1	0.7±0.05
B	1.0±0.05
B1	0.8±0.05
C	0.35
D	0.15±0.05
H	0.38+0.02/-0.04
T	0.1±0.05

1. Q_1 COMMON (EMITTER)
2. Q_2 COMMON (EMITTER)
3. Q_2 IN (BASE)
4. Q_2 OUT (COLLECTOR)
5. Q_1 IN (BASE)
6. Q_1 OUT (COLLECTOR)

TFS6

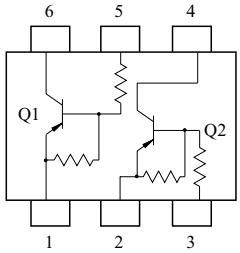
EQUIVALENT CIRCUIT



BIAS RESISTOR VALUES

TYPE NO.	R1(kΩ)	R2(kΩ)
KRA721F	4.7	4.7
KRA722F	10	10
KRA723F	22	22
KRA724F	47	47

EQUIVALENT CIRCUIT (TOP VIEW)



MAXIMUM RATING (Ta=25℃)

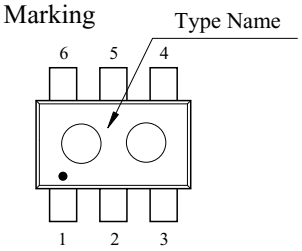
CHARACTERISTIC		SYMBOL	RATING	UNIT
Output Voltage	KRA721F~724F	V_O	-20	V
Input Voltage		V_I	-10/10	V
Output Current		I_O	-50	mA
Power Dissipation		P_D*	50	mW
Junction Temperature		T_j	150	℃
Storage Temperature Range		T_{stg}	55～150	℃

* Total Rating.

MARK SPEC

TYPE	KRA721F	KRA722F	KRA723F	KRA724F
MARK	MA	MB	MC	MD

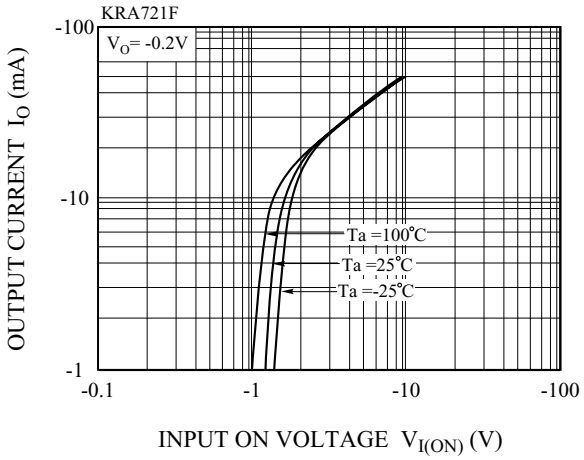
Marking



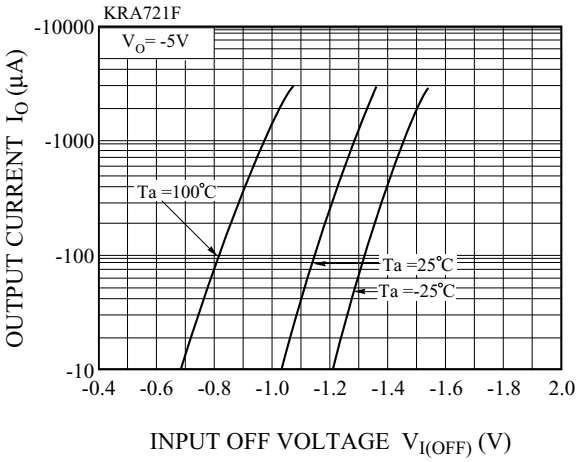
ELECTRICAL CHARACTERISTICS (Ta=25℃)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Cut-off Current	KRA721F~724F	$I_{O(OFF)}$	V_O=-20V, V_I=0	-	-	-500	nA
DC Current Gain	KRA721F	G_I	V_O=-5V, I_O=-10mA	30	-	-	
	KRA722F			60	-	-	
	KRA723F			100	-	-	
	KRA724F			120	-	-	
Output Voltage	KRA721F~724F	$V_{O(ON)}$	I_O=-5mA, I_I=-0.25mA	-	-	-0.15	V
Input Voltage (ON)	KRA721F	$V_{I(ON)}$	V_O=-0.2V, I_O=-5mA	-	-	-2.0	V
	KRA722F			-	-	-2.2	
	KRA723F			-	-	-2.7	
	KRA724F			-	-	-3.6	
Input Voltage (OFF)	KRA721F~724F	$V_{I(OFF)}$	V_O=-5V, I_O=-0.1mA	-0.8	-	-1.5	V
Input Current	KRA721F	I_I	V_I=-5V	-	-	-1.8	mA
	KRA722F			-	-	-0.88	
	KRA723F			-	-	-0.36	
	KRA724F			-	-	-0.18	

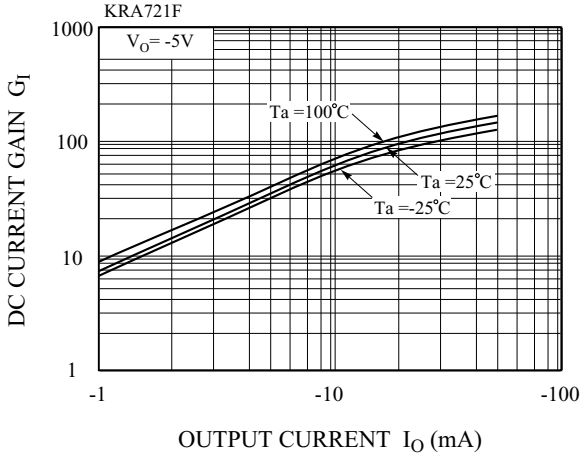
I_O - $V_{I(ON)}$



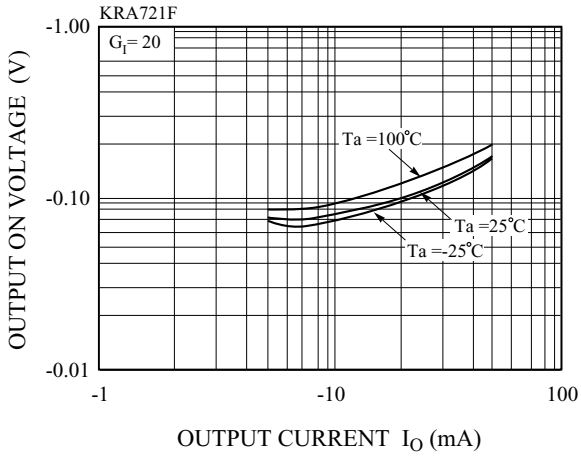
I_O - $V_{I(OFF)}$



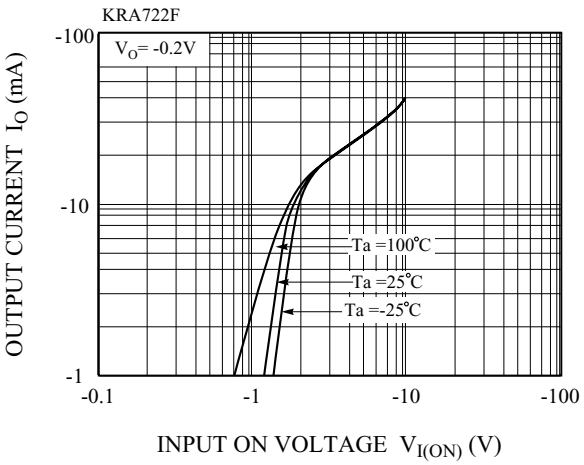
G_I - I_O



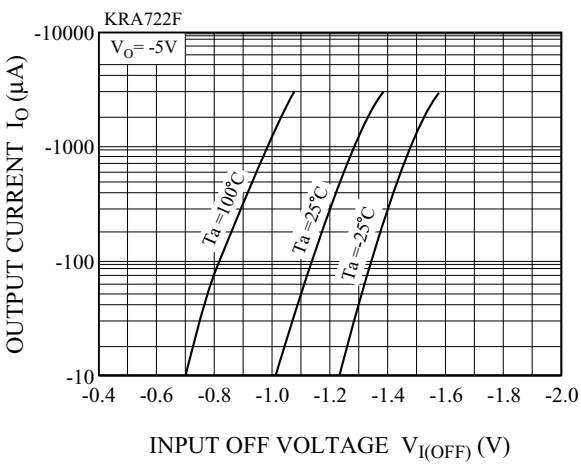
$V_{O(ON)}$ - I_O



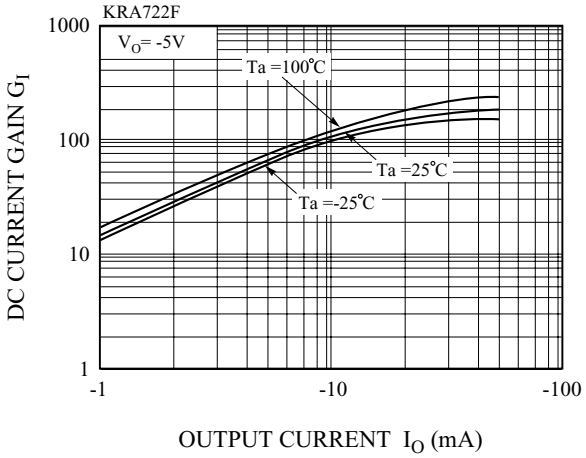
I_O - $V_{I(ON)}$



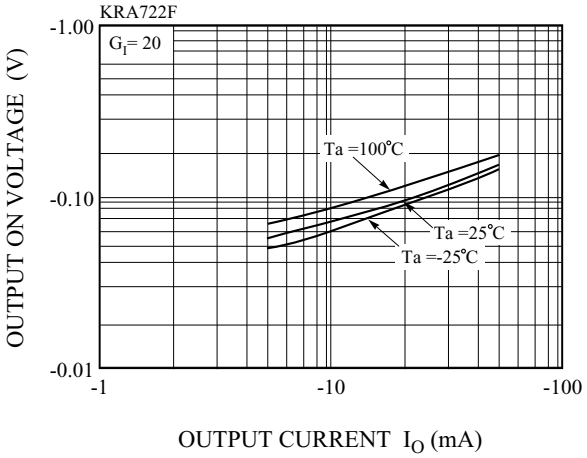
I_O - $V_{I(OFF)}$



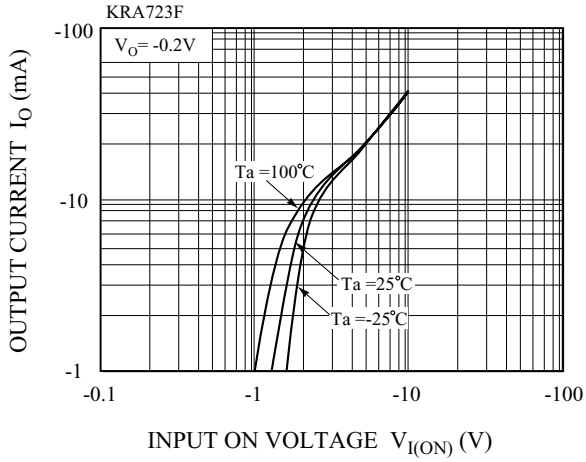
G_I - I_O



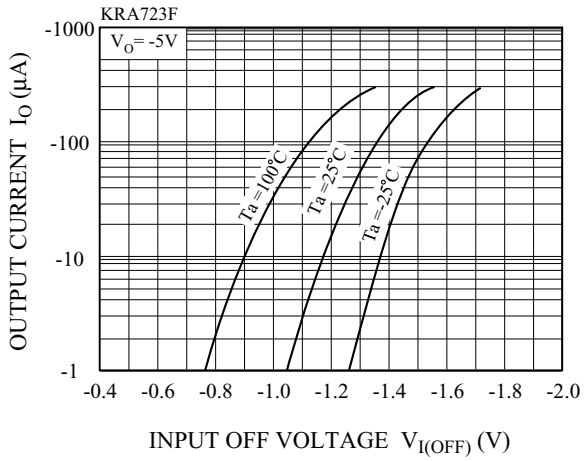
$V_{O(ON)}$ - I_O



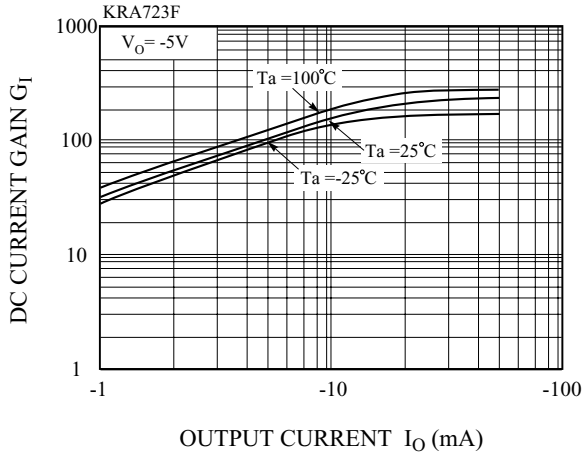
I_O - $V_{I(ON)}$



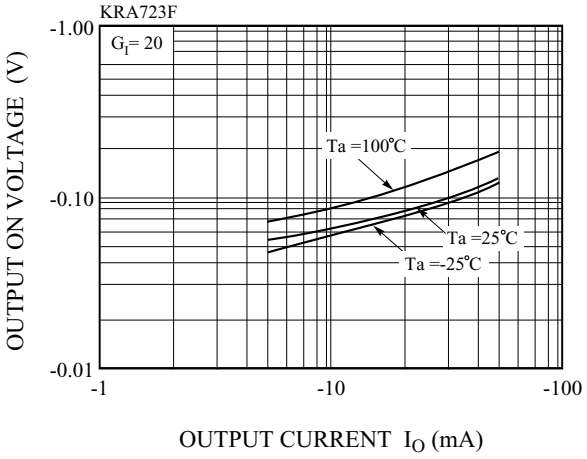
I_O - $V_{I(OFF)}$



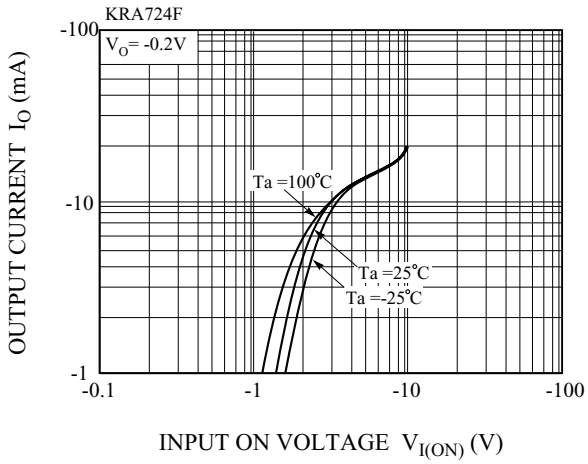
G_I - I_O



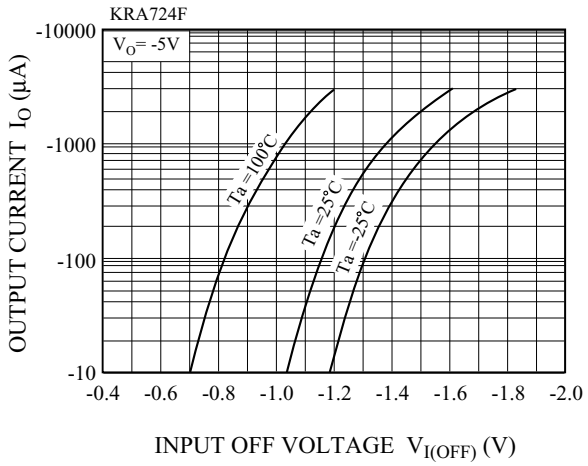
$V_{O(ON)}$ - I_O



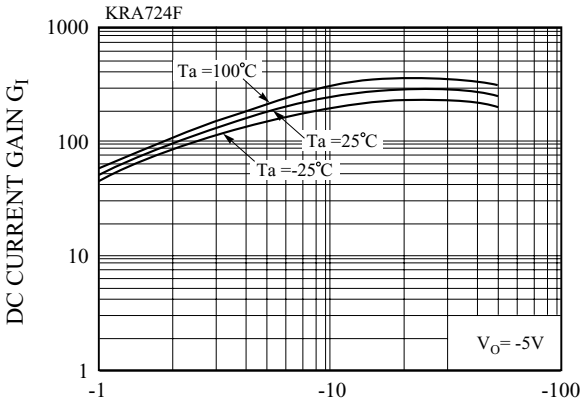
I_O - $V_{I(ON)}$



I_O - $V_{I(OFF)}$



G_I - I_O



$V_{O(ON)}$ - I_O