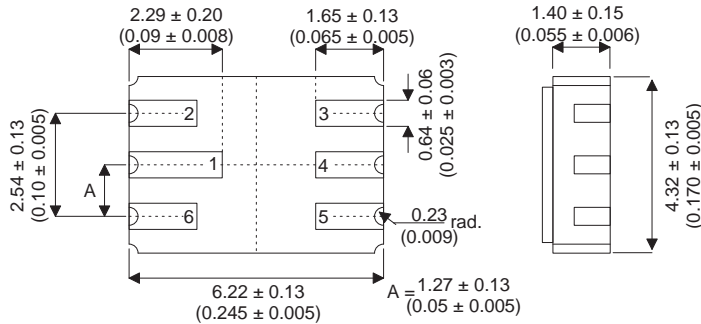


## MECHANICAL DATA

Dimensions in mm (inches)



## DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

$V_{DSS}$  90V  
 $I_D$  0.9A  
 $R_{DS(on)}$  4.0Ω

## FEATURES

- Faster switching
- Low Ciss
- Integral Source-Drain Diode
- High Input Impedance and High Gain

## CERAMIC LCC2 PACKAGE

(Underside View)

PAD 1 – DRAIN 1      PAD 4 – DRAIN 2  
PAD 2 – GATE 1      PAD 5 – SOURCE 2  
PAD 3 – GATE 2      PAD 6 – SOURCE 1

## DESCRIPTION

These Dual enhancement-mode (normally-off) vertical DMOS FETs are ideally suited to a wide range of switching and amplifying applications where high breakdown voltage, high input impedance, low input capacitance, and fast switching speeds are desired.

High Reliability Screening options are available.

## ABSOLUTE MAXIMUM RATINGS

 Each Side -  $T_{CASE} = 25^{\circ}C$  unless otherwise stated

$V_{DS}$	Drain - Source Voltage	90V
$I_D$	Drain Current - Continuous ( $T_C = 25^{\circ}C$ )	0.9A
	- Continuous ( $T_C = 100^{\circ}C$ )	0.7A
$I_{DM}$	Drain Current - Pulsed (Note 1)	3A
$V_{GS}$	Gate - Source Voltage	±20V
$P_{tot(1)}$	Total Power Dissipation at $T_{mb} \leq 25^{\circ}C$	6.25W
	De-rate Linearly above $25^{\circ}C$	0.050W/ $^{\circ}C$
$P_{tot(2)}$	Total Power Dissipation at $T_{amb} \leq 25^{\circ}C$	0.5W
$T_j, T_{stg}$	Operating and Storage Junction Temperature Range	-55 to +150 $^{\circ}C$

## THERMAL DATA

$R_{thj-mb}$	Thermal Resistance Junction – Mounting base	Max	20	$^{\circ}C/W$
$R_{thj-amb}$	Thermal Resistance Junction - Ambient	Max	250	$^{\circ}C/W$

- NOTES:
- 1) Repetitive Rating: Pulse Width limited by maximum junction temperature.
  - 2) Starting  $T_J = 25^{\circ}C$ ,  $L = 1.46mH$ ,  $I_{AS} = 48A$ ,  $V_{DD} = 50V$ ,  $R_G = 25\Omega$ ,
  - 3) Pulse Test: Pulse Width  $\leq 380\mu S$ , Duty Cycle,  $\delta \leq 2\%$

Semelab Plc reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.

## STATIC ELECTRICAL RATINGS (Each Side - $T_{case}=25^{\circ}C$ unless otherwise stated)

Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{(BR)DSS}$ Drain – Source Breakdown Voltage	$V_{GS} = 0V$ $I_D = 1.0\mu A$	90	-	-	V
$V_{GS(th)}$ Gate – Source threshold Voltage	$V_{DS} = V_{GS}$ $I_D = 1.0mA$	0.8	-	2	
	$T_C = 125^{\circ}C$	0.3	-	-	
$I_{GSS}$ Gate – Source Leakage Current	$V_{GS} = \pm 20V$ $V_{DS} = 0V$	-	-	$\pm 100$	nA
	$T_C = 125^{\circ}C$	-	-	$\pm 500$	
$I_{DSS}$ Zero Gate Voltage Drain Current	$V_{DS} = 72V$ $V_{GS} = 0V$	-	-	1.0	$\mu A$
	$T_C = 125^{\circ}C$	-	-	100	
$I_{D(on)}$ On – State Drain Current (note 3)	$V_{DS} = 15V$ $V_{GS} = 10V$	1.5	-	-	A
$R_{DS(on)}$ Drain – Source On Resistance (note 3)	$V_{GS} = 5V$ $I_D = 0.3A$	-	-	5.3	$\Omega$
	$V_{GS} = 10V$ $I_D = 1.0A$	-	-	4	
$T_C = 125^{\circ}C$	-	-	7.5		
$V_{DS(on)}$ Drain – Source On Voltage (note 3)	$V_{GS} = 5V$ $I_D = 0.3A$	-	-	1.6	V
	$V_{GS} = 10V$ $I_D = 1.0A$	-	-	4	
	$T_C = 125^{\circ}C$	-	-	7.5	
$g_{FS}$ Forward Transconductance (Note 3)	$V_{DS} = 7.5V$ $I_D = 0.475A$	170	-	-	ms
$V_{SD}$ Diode Forward Voltage (Note 3)	$V_{GS} = 0V$ $I_s = 0.86A$	0.7	-	1.4	V

## DYNAMIC CHARACTERISTICS

$C_{iss}$ Input Capacitance	$V_{DS} = 25V$ $f = 1.0MHz$	$V_{GS} = 0V$	-	50	-	pF
$C_{oss}$ Output Capacitance			-	40	-	
$C_{rss}$ Reverse Transfer Capacitance			-	10	-	
$T_{d(on)}$ Turn-On Delay	$V_{DD} = 25V$ $R_{GS} = 50\Omega$	$I_D = 1A$ (Note 3)	-	10	-	ns
$T_{d(off)}$ Turn-Off Delay Time			-	10	-	

## MATCHING CHARACTERISTICS FET1 to FET2

$V_{GS(th)M}$ Gate – Source threshold Voltage Matching	$V_{DS} = V_{GS}$	$I_D = 1mA$	-	-	$\pm 25$	mV
$g_{FSM}$ Forward Transconductance Matching (Note 3)	$V_{DS} = 7.5V$	$I_D = 0.475A$	-	-	50	ms

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