

### SINGLE P-CHANNEL ENHANCEMENT MODE MOSFET

### **Features**

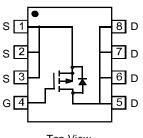
- Low On-Resistance
  - 14mΩ @ V<sub>GS</sub> = -10V
  - 25mΩ @ V<sub>GS</sub> = -4.5V
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208
- Weight: 0.072g (approximate)



Top View



Top View Internal Schematic

## **Ordering Information (Note 3)**

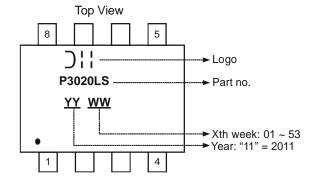
Part Number	Case	Packaging
DMP3020LSS-13	SO-8	2500/Tape & Reel

**SO-8** 

Notes: 1. N

- 1. No purposefully added lead.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com.
- 3. For packaging details, go to our website at http://www.diodes.com.

## **Marking Information**





## **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Chara	cteristic		Symbol	Value	Units
Drain-Source Voltage			$V_{DSS}$	-30	V
Gate-Source Voltage			$V_{GSS}$	±25	V
Drain Current (Note 4)	Steady State	T <sub>A</sub> = 25°C T <sub>A</sub> = 70°C	I <sub>D</sub>	-12 -6	А
Pulsed Drain Current (Note 5)			I <sub>DM</sub>	-40	Α

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 4)	$P_{D}$	2.5	W
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	50	°C/W
Operating and Storage Temperature Range	$T_{J}, T_{STG}$	-55 to +150	°C

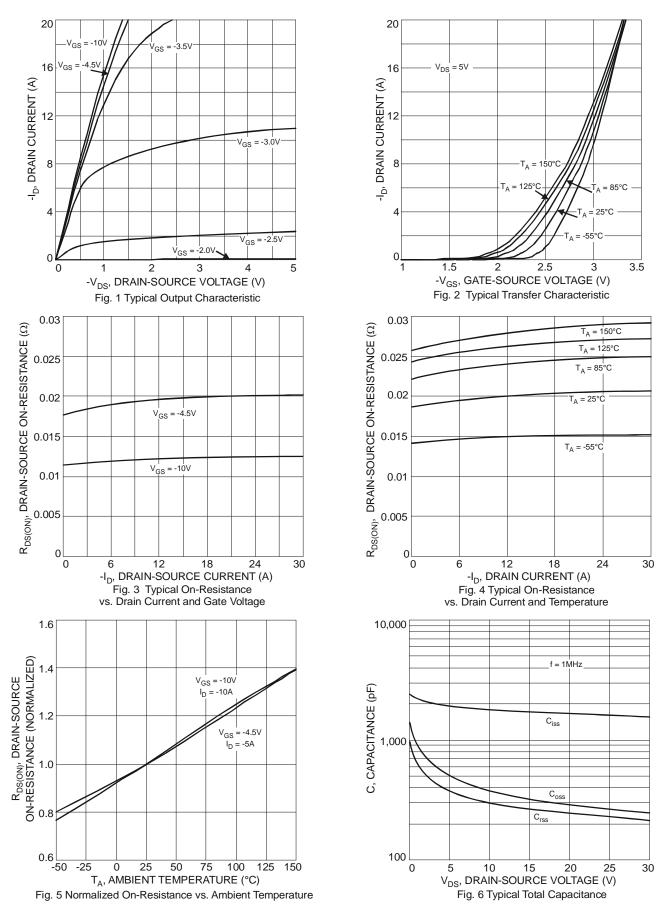
# Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30		_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	-1	μΑ	$V_{DS} = -30V, V_{GS} = 0V$
Cata Sauraa Laakaga	_	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±800	ΠA	$V_{GS} = \pm 25V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	-1		-2	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance		_	11.6	14	mΩ	$V_{GS} = -10V, I_D = -8A$
Static Drain-Source On-Resistance	R <sub>DS</sub> (ON)	l	18.6	25	1112.2	$V_{GS} = -4.5V, I_D = -5A$
Forward Transconductance	g <sub>fs</sub>		12	_	S	$V_{DS} = -10V, I_{D} = -12A$
Diode Forward Voltage (Note 6)	$V_{SD}$	-0.5		-1.1	V	$V_{GS} = 0V$ , $I_S = -2A$
DYNAMIC CHARACTERISTICS						
Input Capacitance	Ciss		1802	_	pF	V <sub>DS</sub> = -15V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	Coss		415	_	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	295	_	pF	
Gate Resistance	R <sub>G</sub>		2.3	_	Ω	$V_{GS} = 0V$ , $V_{DS} = 0V$ , $f = MHz$
SWITCHING CHARACTERISTICS						
Total Gate Charge	Qg		15.3			$V_{DS} = -15V$ , $V_{GS} = -4.5V$ , $I_{D} = -8A$
Total Gate Charge	Qg		30.7		nC	$V_{DS} = -15V$ , $V_{GS} = -10V$ , $I_{D} = -8A$
Gate-Source Charge	$Q_{gs}$	_	3.5		nC	$V_{DS} = -15V$ , $V_{GS} = -10V$ , $I_{D} = -8A$
Gate-Drain Charge	$Q_{gd}$	_	7.9	_		$V_{DS} = -15V$ , $V_{GS} = -10V$ , $I_{D} = -8A$
Turn-On Delay Time	t <sub>d(on)</sub>	_	5.1	_		
Rise Time	t <sub>r</sub>		8	_		$V_{GS} = -10V, V_{DS} = -15V,$
Turn-Off Delay Time	t <sub>d(off)</sub>	_	46	_	ns	$R_D = 15\Omega$ , $R_G = 6\Omega$
Fall Time	t <sub>f</sub>		30	_		

Notes:

- 4. Device mounted on 2 oz. Copper pads on FR-4 PCB with  $R_{0JA}$  = 50°C/W. 5. Pulse width  $\leq$ 10 $\mu$ S, Duty Cycle  $\leq$ 1%. 6. Short duration pulse test used to minimize self-heating effect.

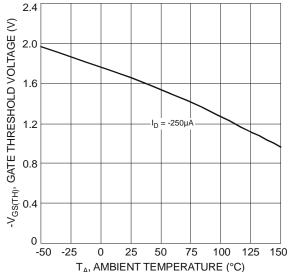


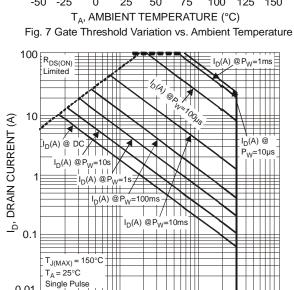


 $T_A = 25^{\circ}C$ 

1.2

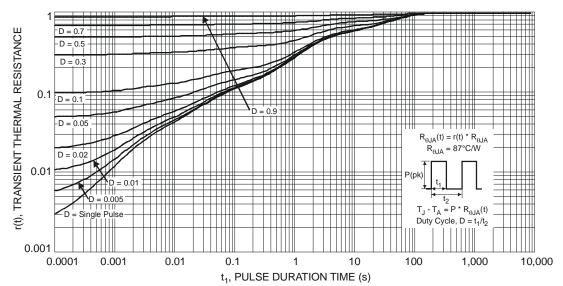






 ${\rm V_{DS}},$  DRAIN-SOURCE VOLTAGE (V) Fig. 9 SOA, Safe Operation Area

10



100

20

اع، SOURCE CURRENT (A)

0

0

0.4

0.6

-V<sub>SD</sub>, SOURCE-DRAIN VOLTAGE (V)

Fig. 8 Diode Forward Voltage vs. Current

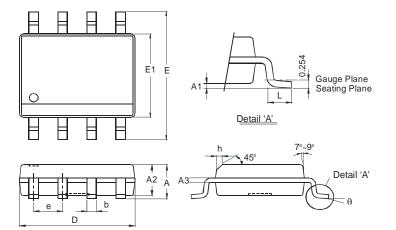
8.0

Fig. 10 Transient Thermal Response

0.01

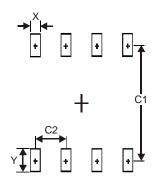


# **Package Outline Dimensions**



SO-8					
Dim	Min	Max			
Α	-	1.75			
A1	0.10	0.20			
A2	1.30	1.50			
А3	0.15	0.25			
b	0.3	0.5			
D	4.85	4.95			
Е	5.90	6.10			
E1	3.85	3.95			
е	<b>e</b> 1.27 Typ				
h	-	0.35			
L	0.62	0.82			
θ	0°	8°			
All Dimensions in mm					

# **Suggested Pad Layout**



Dimensions	Value (in mm)
Х	0.60
Y	1.55
C1	5.4
C2	1.27



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