





SINGLE P-CHANNEL ENHANCEMENT MODE MOSFET

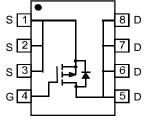
Features

- Low On-Resistance
 - $11m\Omega$ @ $V_{GS} = -10V$
 - 17mΩ @ V_{GS} = -4.5V
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 2)
- "Green" Device (Note 4)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.072g (approximate)

SO-8



TOP VIEW Internal Schematic

Sign

Maximum Ratings @T_A = 25°C unless otherwise specified

| Characteristic | | | Symbol | Value | Units |
|-------------------------------|-----------------|--|------------------|--------------|-------|
| Drain-Source Voltage | | | V _{DSS} | -30 | V |
| Gate-Source Voltage | | | V _{GSS} | ±20 | V |
| Drain Current (Note 1) | Steady State | T _A = 25°C T _A = 70°C | ID | -13 -9.75 | А |
| Pulsed Drain Current (Note 3) | | | I _{DM} | -45 | A |

Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
|---|----------------------------------|-------------|------|
| Total Power Dissipation (Note 1) | PD | 2.5 | W |
| Thermal Resistance, Junction to Ambient | $R_{	heta JA}$ | 50 | °C/W |
| Operating and Storage Temperature Range | T _{J,} T _{STG} | -55 to +150 | °C |

Notes:

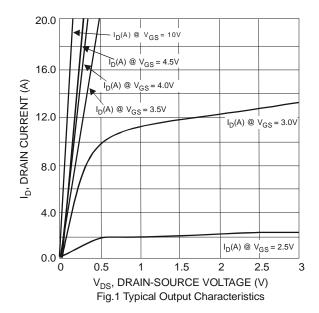
- 1. Device mounted on 2 oz. Copper pads on FR-4 PCB with $R_{\theta JA}$ = 50°C/W.
- 2. No purposefully added lead.
- 3. Pulse width ≤10μS, Duty Cycle ≤1%.
- 4. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

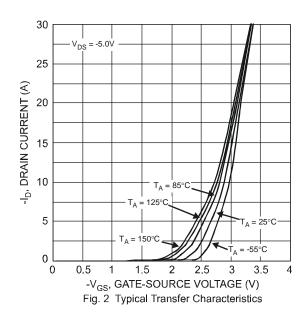


Electrical Characteristics @T_A = 25°C unless otherwise specified

| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition | |
|-----------------------------------|----------------------|------|--------------|----------|------|---|--|
| OFF CHARACTERISTICS (Note 5) | | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | -30 | | l | V | $V_{GS} = 0V, I_D = -250\mu A$ | |
| Zero Gate Voltage Drain Current | I _{DSS} | _ | | -1 | μΑ | $V_{DS} = -30V, V_{GS} = 0V$ | |
| Gate-Source Leakage | I _{GSS} | _ | | ±100 | nA | $V_{GS} = \pm 20V, V_{DS} = 0V$ | |
| ON CHARACTERISTICS (Note 5) | | | | | | | |
| Gate Threshold Voltage | $V_{GS(th)}$ | -1 | | -2 | V | $V_{DS} = V_{GS}, I_{D} = -250 \mu A$ | |
| Static Drain-Source On-Resistance | R _{DS} (ON) | _ | 9 14 | 11 17 | mΩ | $V_{GS} = -10V, I_D = -13A$ $V_{GS} = -4.5V, I_D = -10A$ | |
| Forward Transconductance | g _{fs} | _ | 15 | _ | S | V _{DS} = -15V, I _D = -8A | |
| Diode Forward Voltage (Note 5) | V _{SD} | -0.5 | | -1.1 | V | $V_{GS} = 0V, I_S = -2.1A$ | |
| DYNAMIC CHARACTERISTICS | | | | | | | |
| Input Capacitance | C _{iss} | _ | 2748 | | pF | | |
| Output Capacitance | Coss | _ | 357 | | pF | $V_{DS} = -20V, V_{GS} = 0V$ f = 1.0MHz | |
| Reverse Transfer Capacitance | C _{rss} | _ | 356 | | pF | 11 = 1.0WHZ | |
| Gate Resistance | R_{G} | _ | 2.0 | _ | Ω | $V_{DS} = 0V$, $V_{GS} = 0V$ f = 1.0MHz | |
| SWITCHING CHARACTERISTICS | | | | | | | |
| Total Gate Charge | Q_{g} | _ | 30.0 60.4 | _ | | $V_{DS} = -10V$, $V_{GS} = -4.5V$, $I_{D} = -13A$ $V_{DS} = -10V$, $V_{GS} = -10V$, $I_{D} = -13A$ | |
| Gate-Source Charge | Q _{gs} | _ | 7.2 | _ | nC | $V_{DS} = -10V, V_{GS} = -10V, I_{D} = -13A$ | |
| Gate-Drain Charge | Q _{gd} | _ | 16.4 | _ | | $V_{DS} = -10V, V_{GS} = -10V, I_{D} = -13A$ | |
| Turn-On Delay Time | t _{d(on)} | _ | 11.2 | | | | |
| Rise Time | t _r | | 12.4 | - | nc | $V_{DS} = -15V, V_{GS} = -10V,$ | |
| Turn-Off Delay Time | t _{d(off)} | | 104.9 | _ | ns | $I_D = -1A, R_G = 6.0\Omega$ | |
| Fall Time | t _f | | 61.7 | | | | |

Notes: 5. Short duration pulse test used to minimize self-heating effect.







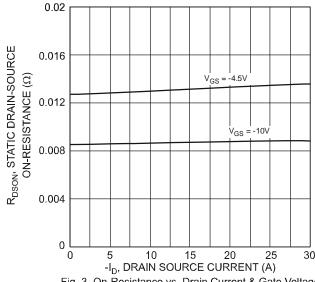


Fig. 3 On-Resistance vs. Drain Current & Gate Voltage

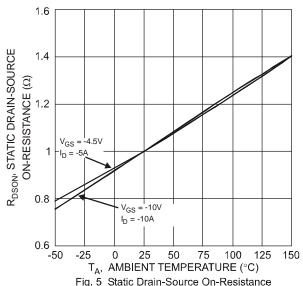


Fig. 5 Static Drain-Source On-Resistance vs. Ambient Temperature

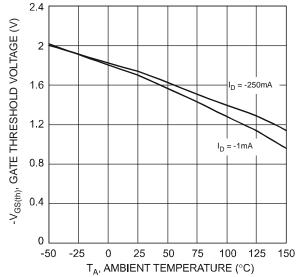


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

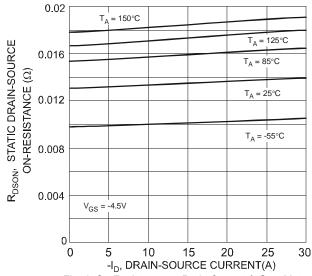
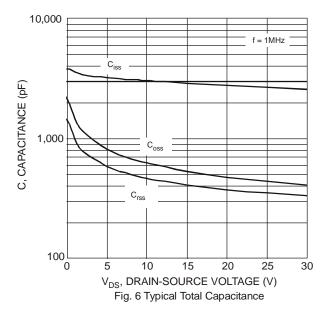


Fig. 4 On-Resistance vs.Drain Current & Gate Voltage



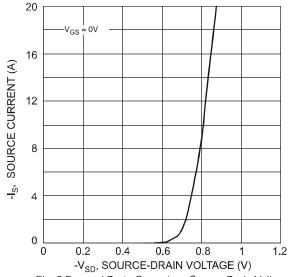


Fig. 8 Forward Drain Current vs. Source-Drain Voltage



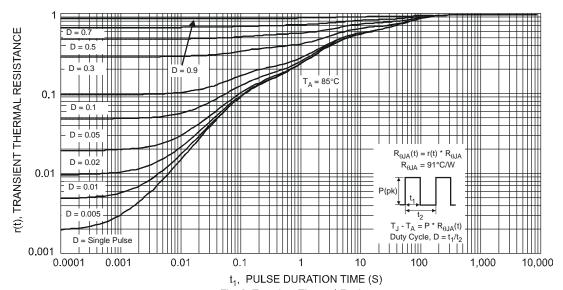


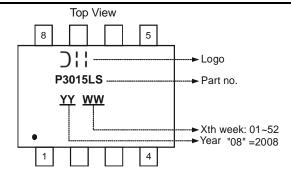
Fig. 9 Transient Thermal Resistance

Ordering Information (Note 6)

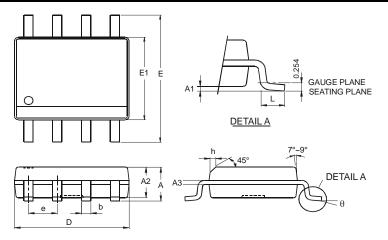
| Part Number | Case | Packaging |
|---------------|---------------------|-----------|
| DMP3015LSS-13 | SO-8 2500/Tape & Re | |

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



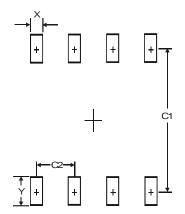
Package Outline Dimensions



| SO-8 | | | | |
|----------------------|-----------|------|--|--|
| Dim | Min | Max | | |
| Α | | 1.75 | | |
| A1 | 0.08 | 0.25 | | |
| A2 | 1.30 | 1.50 | | |
| A3 | 0.20 Typ. | | | |
| b | 0.3 | 0.5 | | |
| D | 4.80 | 5.30 | | |
| Е | 5.79 | 6.20 | | |
| E1 | 3.70 | 4.10 | | |
| е | 1.27 Typ. | | | |
| h | _ | 0.35 | | |
| L | 0.38 | 1.27 | | |
| θ | 0° | 8° | | |
| All Dimensions in mm | | | | |



Suggested Pad Layout



| Dimensions | Value (in mm) |
|------------|---------------|
| Х | 0.60 |
| Υ | 1.55 |
| C1 | 5.4 |
| C2 | 1.27 |

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