



A Product Line of Diodes Incorporated



ZXM61P02F

20V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on)}	I _D T _A = 25°C
201/	600mΩ @ V _{GS} = -4.5V	-0.92A
-20V	900m Ω @ V _{GS} = -2.7V	-0.75A

Description and Applications

This MOSFET utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed, making it ideal for high-efficiency power management applications.

- DC DC converters
- Power management functions
- Disconnect switches
- Motor control

Features and Benefits

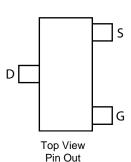
- Fast switching speed
- Low on-resistance
- Low threshold
- Low gate drive
- "Lead Free", RoHS Compliant (Note 1)
- Halogen and Antimony Free. "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

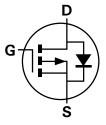
Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish; Solderable per MIL-STD-202, Method 208
- Weight: 0.008 grams (approximate)



SOT23





Equivalent Circuit

Ordering Information (Note 3)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXM61P02FTA	P02	7	8	3000 Units

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. No purposely added lead. Halogen and Antimony free.

2. Diodes Inc's "Green" Policy can be found on our website at http://www.diodes.com

3. For packaging details, go to our website at http://www.diodes.com

Marking Information

Notes:







ZXM61P02F

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	-20	V
Gate-Source Voltage			V _{GS}	±12	V
Continuous Drain Current	V _{GS} = 4.5V	$T_A = 25^{\circ}C$ (Note 5) $T_A = 70^{\circ}C$ (Note 5)	ID	-0.9 -0.7	А
Pulsed Drain Current (Note 6)			I _{DM}	-4.9	A
Continuous Source Current (Body Diode) (Note 5)			ls	-0.9	A
Pulsed Source Current (Body Diode) (Note 6)			I _{SM}	-4.9	A

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 4)	6	625	mW
Linear Derating Factor	PD	5	mW/°C
Power Dissipation (Note 5)	6	806	mW
Linear Derating Factor	PD	6.4	mW/°C
Thermal Resistance, Junction to Ambient (Note 4)	R _{0JA}	200	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	R _{0JA}	155	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C

Notes:

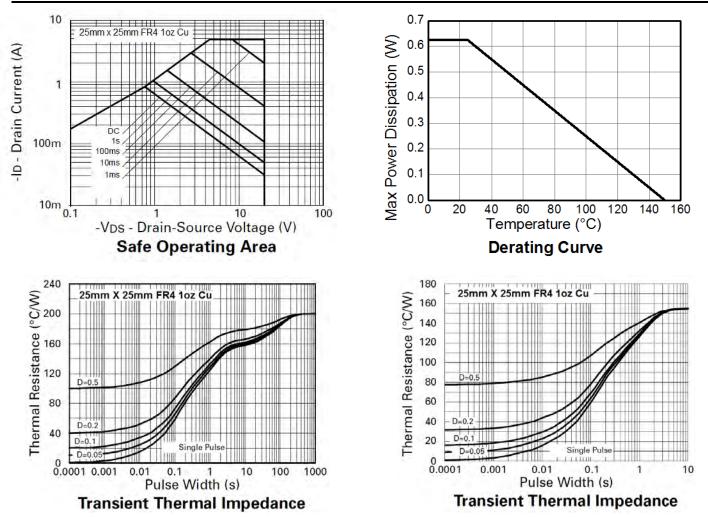
4. For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions 5. For a device surface mounted on FR4 PCB measured at t ≤5 secs.

6. Repetitive rating 25mm x 25mm FR4 PCB, D=0.05 pulse width=10µs - pulse current limited by maximum junction temperature.





Thermal Characteristics







ZXM61P02F

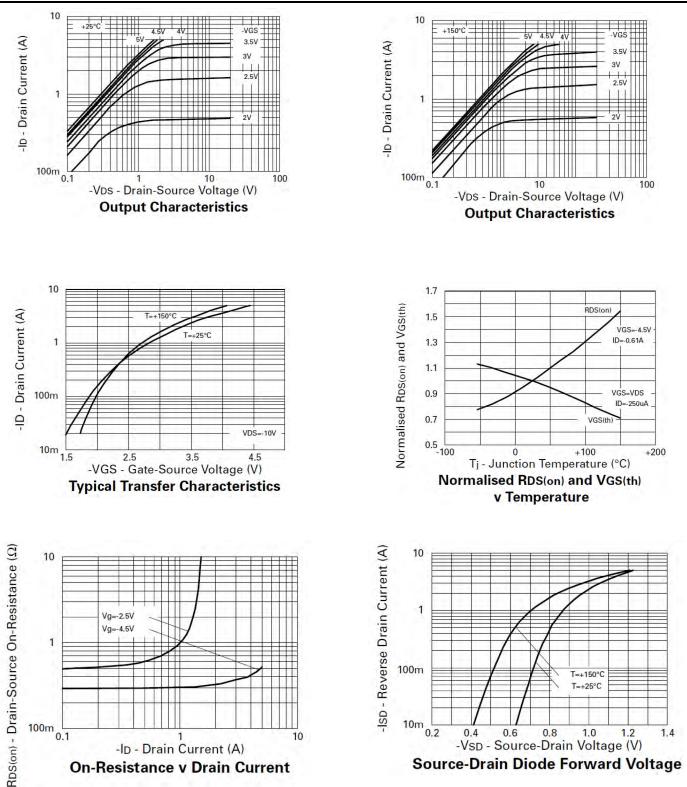
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$I_D = -250 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-0.1	μΑ	$V_{DS} = -20V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 12V$, $V_{DS} = 0V$	
ON CHARACTERISTICS							
Gate Threshold Voltage	V _{GS(th)}	-0.7		—	V	$I_D = -250 \mu A$, $V_{DS} = V_{GS}$	
Static Drain-Source On-Resistance (Note 7)	D			0.6	Ω	$V_{GS} = -4.5V, I_D = -0.61A$	
	R _{DS (ON)}			0.9		$V_{GS} = -2.7V, I_D = -0.31A$	
Forward Transconductance (Notes 7 and 9)	g fs	0.56	_	_	S	$V_{DS} = -10V, I_D = -0.31A$	
Diode Forward Voltage (Note 7)	V _{SD}	_	_	-0.95	V	$T_J = 25^{\circ}C, I_S = -0.61A, V_{GS} = 0V$	
Reverse Recovery Time (Note 9)	t _{rr}	_	14.9	_	ns	T _J = 25°C, I _F = -0.61A,	
Reverse Recovery Charge (Note 9)	Qrr	_	5.6	_	nC	di/dt = 100A/µs	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	_	150	_		V _{DS} = -15V, V _{GS} = 0V f = 1.0MHz	
Output Capacitance	Coss	_	70		pF		
Reverse Transfer Capacitance	C _{rss}	_	30	_			
Turn-On Delay Time (Note 8)	t _{d(on)}	_	2.9	_			
Turn-On Rise Time (Note 8)	tr	_	6.7	_		$\label{eq:VDD} \begin{split} V_{DD} &= -110V, \ I_D = -0.93A, \\ R_G &\cong 6.2\Omega, \ R_D \cong 11\Omega, \end{split}$	
Turn-Off Delay Time (Note 8)	t _{d(off)}	_	11.2	_	ns		
Turn-Off Fall Time (Note 8)	t _f	_	10.1	_			
Total Gate Charge (Note 8)	Qg	_	3.5	_			
Gate-Source Charge (Note 8)	Qgs	_	0.5	_	nC	$V_{DS} = -16V, V_{GS} = -4.5V,$ $I_{D} = -0.61A$	
Gate-Drain Charge (Note 8)	Q _{gd}	_	1.5	_	1		

 7. Measured under pulsed conditions. Pulse width = 300μs. Duty cycle ≤ 2%.
8. Switching characteristics are independent of operating junction temperature.
9. For design aid only, not subject to production testing. Notes:





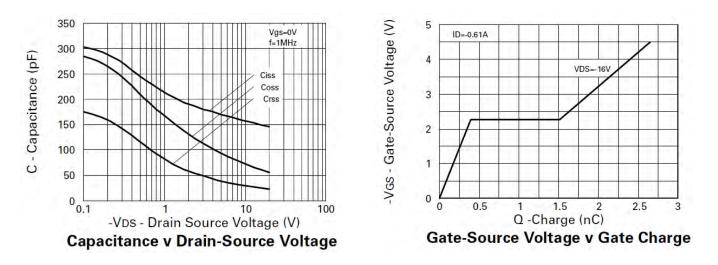
Typical Characteristics



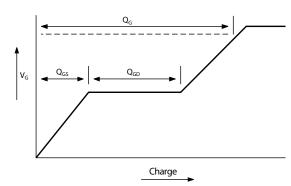




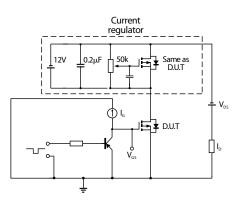
Typical Characteristics - continued



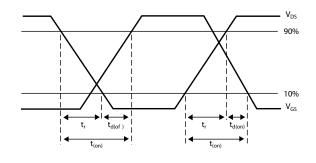
Test Circuits



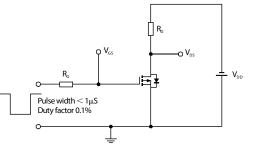
Basic gate charge waveform



Gate charge test circuit

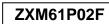


Switching time waveforms



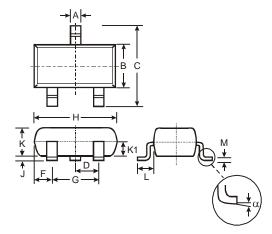
Switching time test circuit





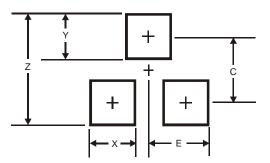
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Package Outline Dimensions



SOT23					
Dim	Min	Max	Тур		
Α	0.37	0.51	0.40		
В	1.20	1.40	1.30		
С	2.30	2.50	2.40		
D	0.89	1.03	0.915		
F	0.45	0.60	0.535		
G	1.78	2.05	1.83		
Н	2.80	3.00	2.90		
J	0.013	0.10	0.05		
κ	0.903	1.10	1.00		
K1	-	-	0.400		
L	0.45	0.61	0.55		
М	0.085	0.18	0.11		
α	0°	8°	-		
All Dimensions in mm					

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35





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