

### DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

## **Features**

- Low On-Resistance
  - $70m\Omega @V_{GS} = -4.5V$
  - 85mΩ @V<sub>GS</sub> = -2.5V
  - 86m $\Omega$  (typ) @V<sub>GS</sub> = -1.8V
  - Low Gate Threshold Voltage, -0.9V Max
- Fast Switching Speed
- Low Input/Output Leakage
- Low Profile, 0.5mm Max Height
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

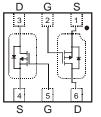
## **Mechanical Data**

- Case: U-DFN2020-6 Type B
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu annealed over Copper leadframe.
  Solderable per MIL-STD-202, Method 208 <sup>(4)</sup>
- Weight: 0.0065 grams (approximate)

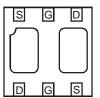
U-DFN2020-6 Type B



Bottom View



Top View Internal Schematic



Bottom View Pin Configuration

### Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2160UFDB-7	U-DFN2020-6 Type B	3000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com.

## **Marking Information**



P2 = Marking Code YM = Date Marking Y = Year (ex: V = 2008) M = Month (ex: 9 = September) Dot denotes Pin 1

Date Code Key												
Year	2008	2009	2010	2011	201	2 20	013	2014	2015	2016	2017	2018
Code	V	W	Х	Y	Z		A	В	С	D	Е	F
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D



## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage	V <sub>DSS</sub>	-20	V	
Gate-Source Voltage	V <sub>GSS</sub>	±12	V	
Drain Current (Note 5)	ID	-3.8	A	
Pulsed Drain Current (Note 6)	I <sub>DM</sub>	-13	А	

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	1.4	W
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	89	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

#### **Electrical Characteristics** $@T_A = 25^{\circ}C$ unless otherwise specified

			-				
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7	0						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20			V	$V_{GS} = 0V, I_D = -250 \mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			-1	μA	$V_{DS} = -20V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$	
Gale-Source Leakage		—	—	±800		$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	-0.45		-0.9	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$	
			54	70		$V_{GS} = -4.5V, I_D = -2.8A$	
Static Drain-Source On-Resistance	R <sub>DS (ON)</sub>		68	85	mΩ	V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -2.0A	
			86	—		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -1.0A	
Forward Transfer Admittance	Y <sub>fs</sub>		8	_	S	V <sub>DS</sub> = -5V, I <sub>D</sub> = -2.8A	
Diode Forward Voltage (Note 7)	V <sub>SD</sub>		0.7	-1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -1.6A	
DYNAMIC CHARACTERISTICS							
Input Capacitance	Ciss	_	536		pF		
Output Capacitance	Coss		68		pF	V <sub>DS</sub> = -10V, V <sub>GS</sub> = 0V f = 1.0MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>		59		pF	1 = 1.00012	
Gate Resistance	Rg	-	8.72	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge	Qg	-	6.5	-	nC	$V_{GS} = -4.5V, V_{DD} = -10V,$	
Gate-Source Charge	Q <sub>gs</sub>	-	0.8	-	nC	$v_{GS} = -4.5v, v_{DD} = -10v,$ $I_{D} = -1.5A$	
Gate-Drain Charge	Q <sub>gd</sub>	-	1.4	-	nC	1D = -1.3A	
Turn-On Delay Time	t <sub>D(on)</sub>	-	11.51	-	ns		
Turn-On Rise Time	tr	-	12.09	-	ns	$V_{GEN} = -4.5V, V_{DD} = -10V,$	
Turn-Off Delay Time	t <sub>D(off)</sub>	-	55.34	-	ns	$R_L = 10\Omega, R_G = 6\Omega$	
Turn-Off Fall Time	t <sub>f</sub>	-	27.54	-	ns		

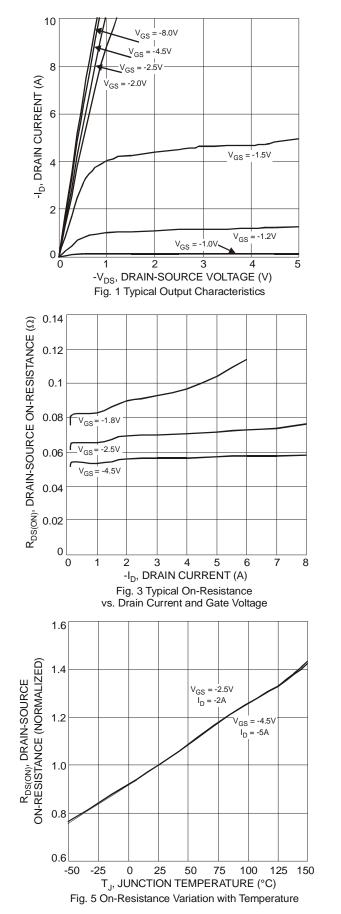
Notes:

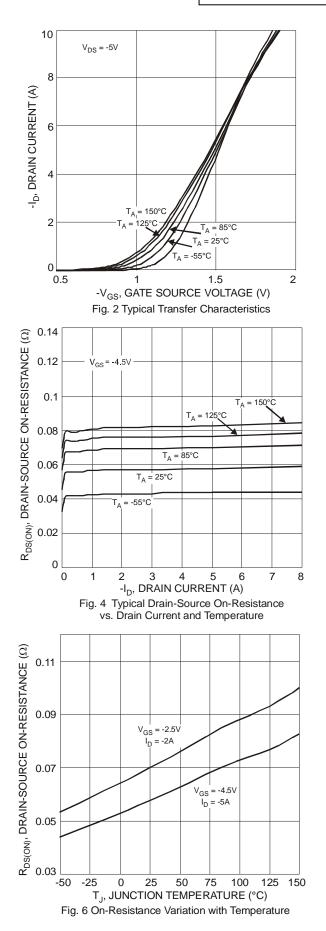
Device mounted on FR-4 PCB, on minimum recommended, 2oz Copper pad layout. 5.

6. 7. Repetitive rating, pulse width limited by junction temperature. Short duration pulse test used to minimize self-heating effect.

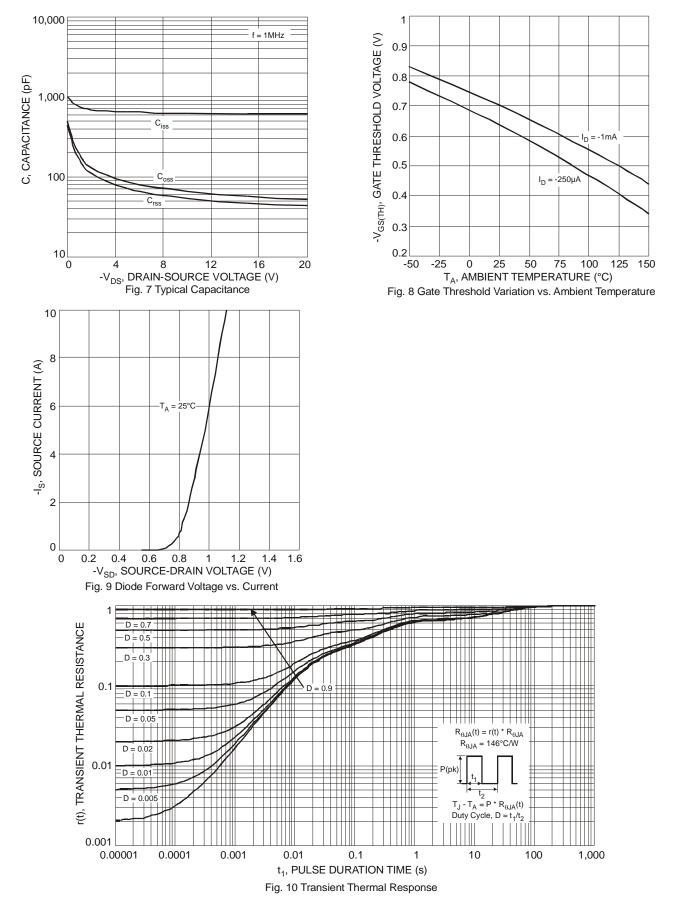


## DMP2160UFDB





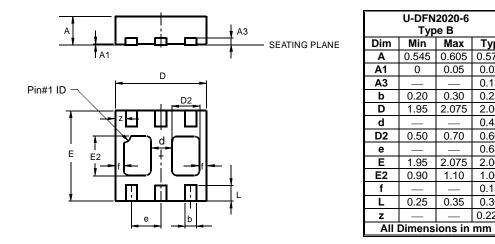






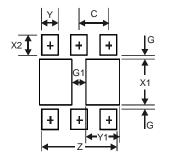
## **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	1.67
G	0.20
G1	0.40
X1	1.0
X2	0.45
Y	0.37
Y1	0.70
С	0.65

Max

0.605

0.05

0.30

2.075

0.70

\_

2.075

1.10

0.35

Тур

0.575

0.02

0.13

0.25

2.00

0.45

0.60 0.65

2.00

1.00

0.15

0.30

0.225



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