



A Product Line of Diodes Incorporated



ZVN4310G

100V N-CHANNEL ENHANCEMENT MODE VERTICAL MOSFET IN SOT223

Features and Benefits

- V_{(BR)DSS} > 100V
- $R_{DS(on)} \le 0.54\Omega @ V_{GS} = 10V$
- Maximum continuous drain current I_D = 1.67A
- "Green" component, Lead Free Finish / RoHS compliant (Note 1)
- Qualified to AEC-Q101 Standards for High Reliability

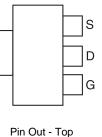
Applications

- DC-DC Converters
- Solenoids / Relay Driver for Automotive

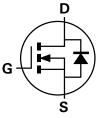
Mechanical Data

- Case: SOT223
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish
- Weight: 0.112 grams (approximate





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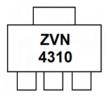
Equivalent Circuit

Ordering Information (Note 1)

Part Number	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZVN4310GTA	ZVN4310	7	8	1,000

Notes: 1. Diodes, Inc. defines "Green" products as those which are RoHS compliant and contain no halogens or antimony compounds. All applicable RoHS exemptions applied. Further information about Diodes Inc.'s "Green" Policy can be found on our website at https://www.diodes.com

Marking Information



ZVN4310 = Product Type Marking Code





Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	100	V	
Gate-Source Voltage	V _{GSS}	±20	V	
Continuous Drain Current	ID	1.67	А	
Pulsed Drain Current (Note 3)	I _{DM}	12	A	

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	
Power Dissipation	(Note 2)	PD	3	W
Thermal Resistance, Junction to Ambient	(Note 2)	R _{0JA}	41.7	°C/W
Thermal Resistance, Junction to Leads	(Note 4)	R _{θJL}	8.84	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 5)							
Drain-Source Breakdown Voltage	BV _{DSS}	100	-	-	V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current $T_J = 25^{\circ}C$	I _{DSS}	-	-	10 100	μΑ μΑ	V _{DS} = 100V, V _{GS} = 0V V _{DS} = 80V, V _{GS} = 0V, T _A = 125°C	
Gate-Source Leakage	Igss	-	-	±20	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
On-State Drain Current	I _{D(on)}	9	-	-	А	$V_{GS} = 10V, V_{DS} = 10V$	
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage	V _{GS(th)}	1	-	3	V	$V_{DS} = V_{GS}, I_D = 1mA$	
Static Drain-Source On-Resistance	R _{DS (on)}	-	0.4 0.5	0.54 0.75	Ω	$V_{GS} = 10V, I_D = 3.3A$ $V_{GS} = 5V, I_D = 1.5A$	
Forward Transconductance	g fs	0.6	-	-	S	V _{DS} = 10V, I _D = 3.3A	
DYNAMIC CHARACTERISTICS (Note 5)						·	
Input Capacitance	C _{iss}	-	-	350	pF	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1.0MHz	
Output Capacitance	C _{oss}	-	-	140	pF		
Reverse Transfer Capacitance	C _{rss}	-	-	20	pF		
Turn-On Delay Time	t _{D(on)}	-	-	8	ns	$V_{DD} = 25V, I_D = 3A, V_{GEN} = 10V, \label{eq:VDD} R_{GS} = 50\Omega$	
Turn-On Rise Time	tr	-	-	25	ns		
Turn-Off Delay Time	t _{D(off)}	-	-	30	ns		
Turn-Off Fall Time	t _f	-	-	16	ns		

2. For a device mounted on 50mm X 50mm X 1.6mm FR-4 PCB with high coverage of single sided 2oz copper, in still air condition. Notes:

3. Device mounted on minimum recommended pad layout test board, $10\mu s$ pulse duty cycle = 1%.

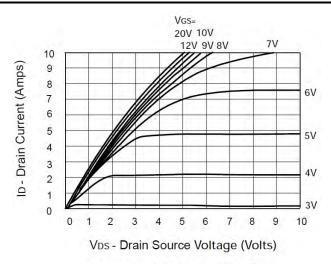
4. Thermal resistance from junction to solder-point (at the end of the drain lead).

5. Short duration pulse test used to minimize self-heating effect.

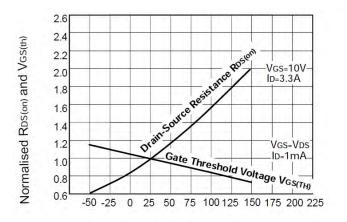




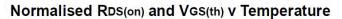
Electrical Characteristics

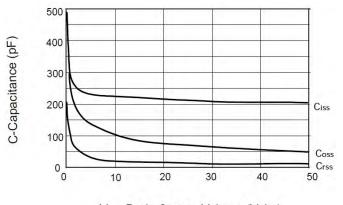


Saturation Characteristics



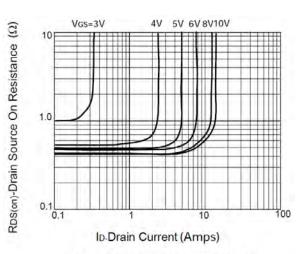
Tj-Junction Temperature (°C)



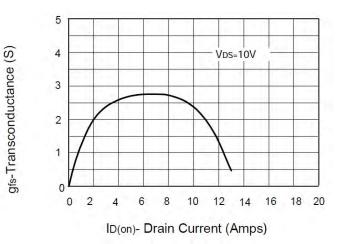


VDS-Drain Source Voltage (Volts)

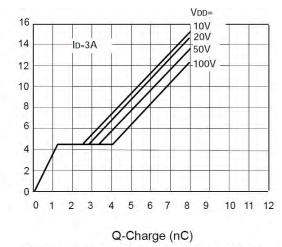
Capacitance v drain-source voltage



On-resistance v drain current



Transconductance v drain current



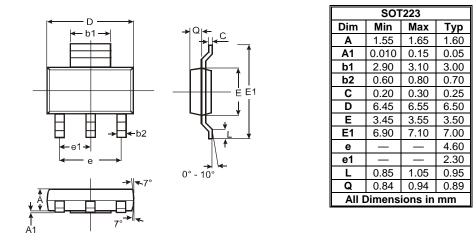
Gate charge v gate-source voltage

VGS-Gate Source Voltage (Volts)

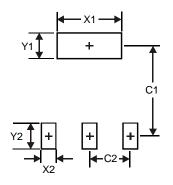




Package Outline Dimensions



Suggested Pad Layout



Dimensions	Value (in mm)			
X1	3.3			
X2	1.2			
Y1	1.6			
Y2	1.6			
C1	6.4			
C2	2.3			





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