

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)}$	I_D max $T_A = 25^\circ\text{C}$
30V	14m Ω @ $V_{GS} = 10\text{V}$	8.0A
	20m Ω @ $V_{GS} = 4.5\text{V}$	6.7A

Description and Applications

This new generation MOSFET has been designed to minimize the on-state resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

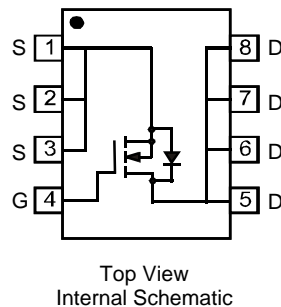
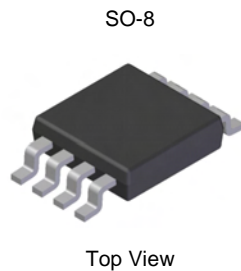
- DC-DC Converters
- Power management functions

Features and Benefits

- 14m Ω @ $V_{GS} = 10\text{V}$
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Lead Free By Design/RoHS Compliant (Note 1)**
- **"Green" Device (Note 2)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish - Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208
- Weight: 0.072 grams (approximate)

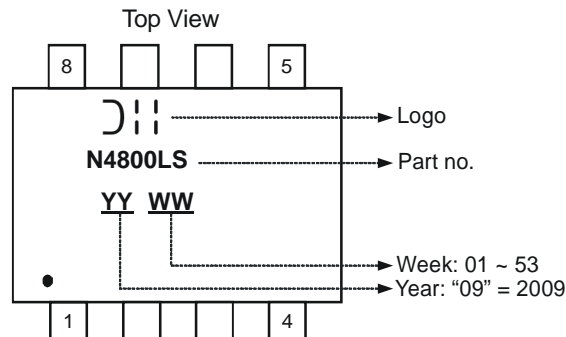


Ordering Information (Note 3)

Part Number	Case	Packaging
DMN4800LSSL-13	SO-8	2500/Tape & Reel

- Notes:
1. No purposefully added lead.
 2. Diodes Inc.'s "Green" policy can be found on our website at <http://www.diodes.com>
 3. For packaging details, go to our website at <http://www.diodes.com>.

Marking Information



Maximum Ratings @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Units	
Drain-Source Voltage	V_{DS}	30	V	
Gate-Source Voltage	V_{GS}	± 20	V	
Drain Current (Note 4) $V_{GS} = 10\text{V}$	Steady State	$T_A = 25^\circ\text{C}$	8.0	A
		$T_A = 70^\circ\text{C}$	6.4	
Drain Current (Note 4) $V_{GS} = 10\text{V}$	Steady State	$T_A = 25^\circ\text{C}$	6.7	A
		$T_A = 70^\circ\text{C}$	5.3	
Pulsed Drain Current (Note 5)	I_{DM}	50	A	

Thermal Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 4)	P_D	1.46	W
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	86	$^\circ\text{C/W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV_{DSS}	30	—	—	V	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1	μA	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	—	—	± 100	nA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	$V_{GS(th)}$	0.8	1.2	1.6	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(on)}$	—	11	14	m Ω	$V_{GS} = 10\text{V}, I_D = 8\text{A}$
		—	14	20		$V_{GS} = 4.5\text{V}, I_D = 7\text{A}$
Forward Transconductance	g_{fs}	—	8	—	S	$V_{DS} = 10\text{V}, I_D = 8\text{A}$
Diode Forward Voltage (Note 6)	V_{SD}	—	0.72	0.94	V	$V_{GS} = 0\text{V}, I_S = 1\text{A}$
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{iss}	—	798	—	pF	$V_{DS} = 10\text{V}, V_{GS} = 0\text{V}$ $f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	128	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	122	—	pF	
Gate Resistance	R_G	—	1.37	—	Ω	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Total Gate Charge	Q_g	—	8.7	—	nC	$V_{GS} = 5\text{V}, V_{DS} = 15\text{V}, I_D = 9\text{A}$
Gate-Source Charge	Q_{gs}	—	1.7	—		
Gate-Drain Charge	Q_{gd}	—	2.4	—		
Turn-On Delay Time	$t_{d(on)}$	—	5.03	—	ns	$V_{DD} = 15\text{V}, V_{GEN} = 10\text{V},$ $R_L = 15\Omega, R_G = 6.0\Omega, I_D = 1\text{A}$
Rise Time	t_r	—	4.50	—		
Turn-Off Delay Time	$t_{d(off)}$	—	26.33	—		
Fall Time	t_f	—	8.55	—		

- Notes:
- Device mounted on FR-4 PCB, with minimum recommended pad layout.
 - Repetitive rating, pulse width limited by junction temperature.
 - Short duration pulse test used to minimize self-heating effect.

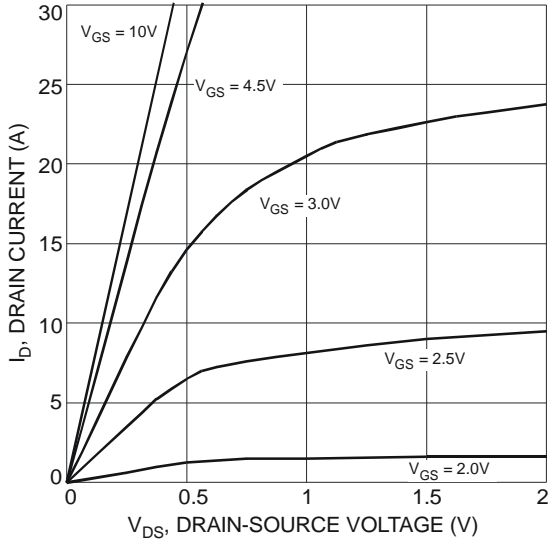


Fig. 1 Typical Output Characteristic

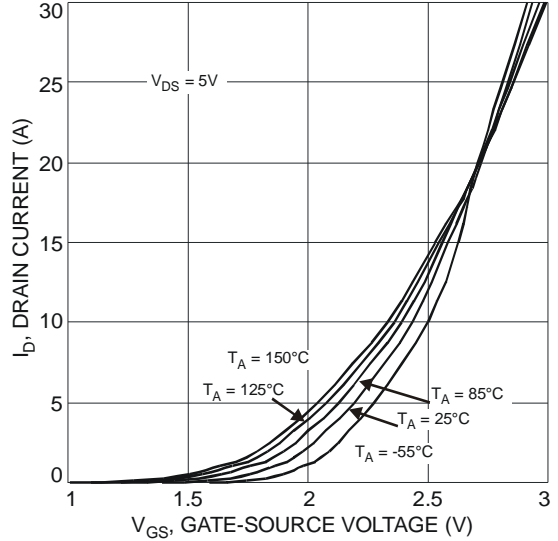


Fig. 2 Typical Transfer Characteristic

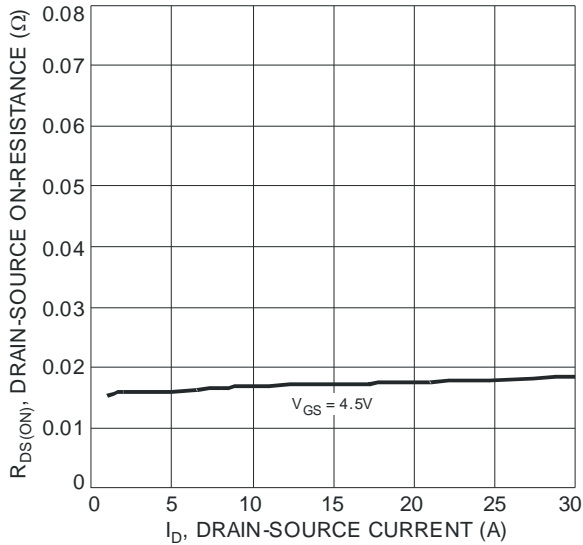


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

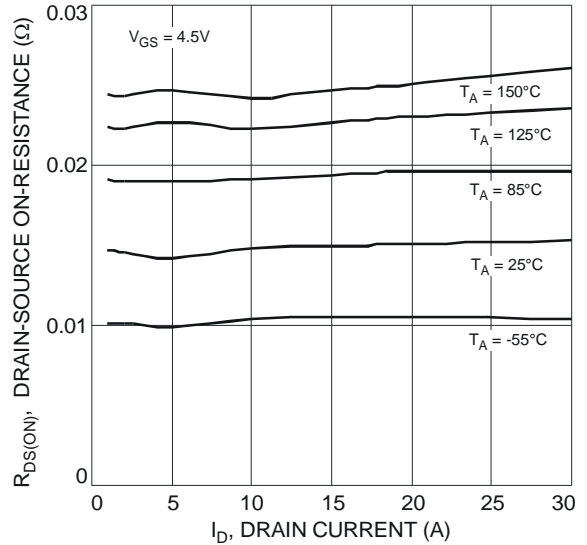


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

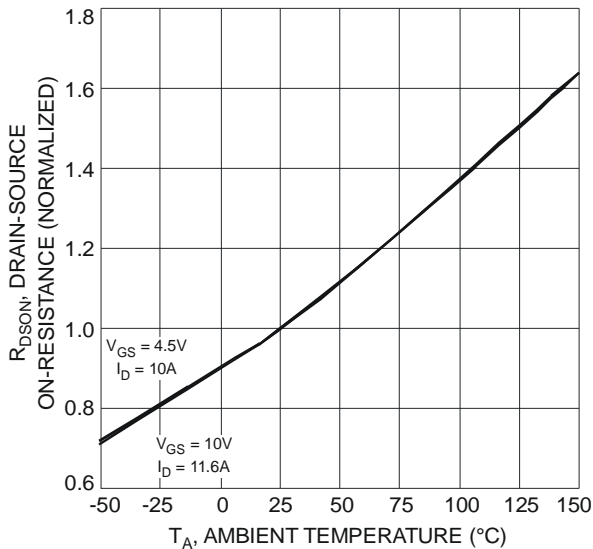


Fig. 5 On-Resistance Variation with Temperature

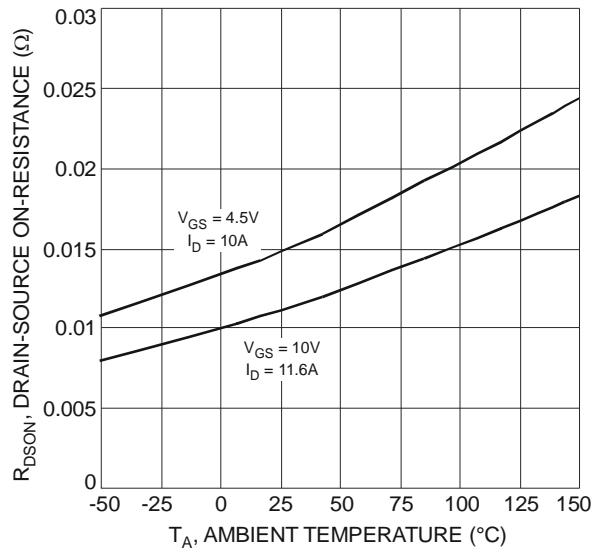


Fig. 6 On-Resistance Variation with Temperature

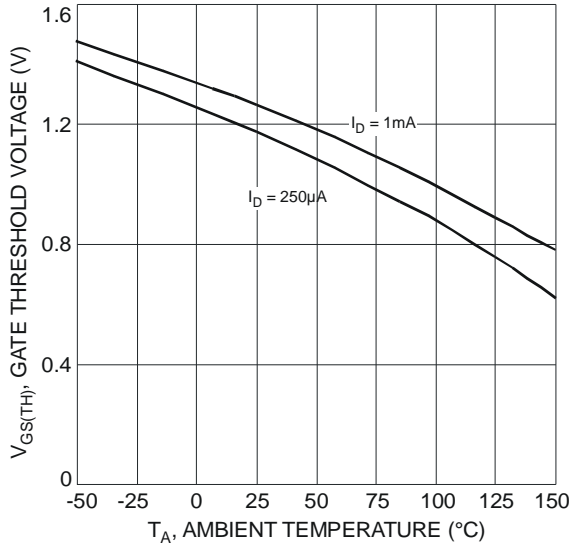


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

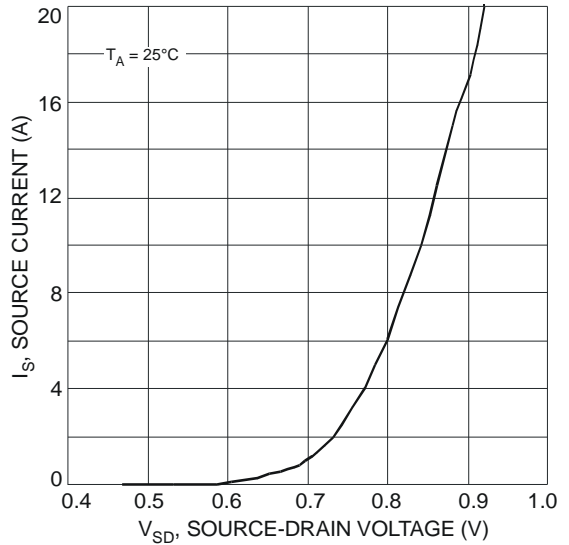


Fig. 8 Diode Forward Voltage vs. Current

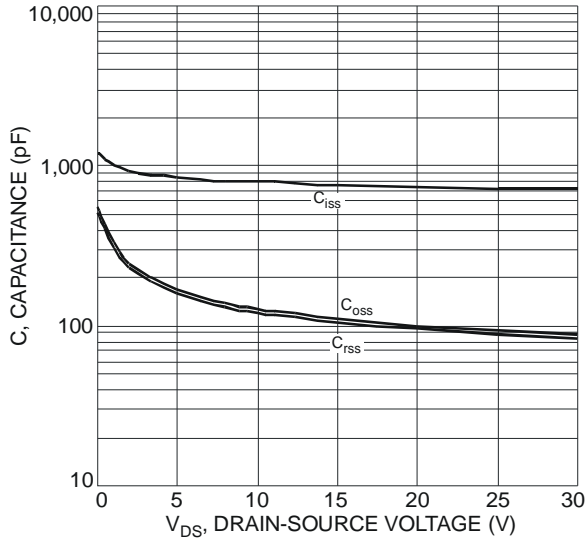


Fig. 9 Typical Total Capacitance

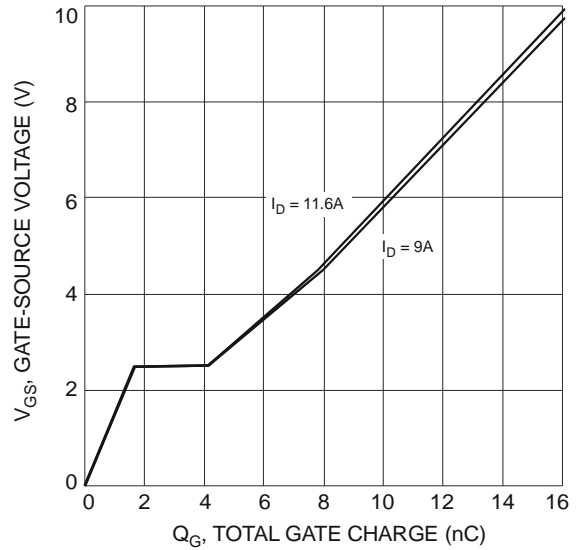


Fig. 10 Total Gate Charge

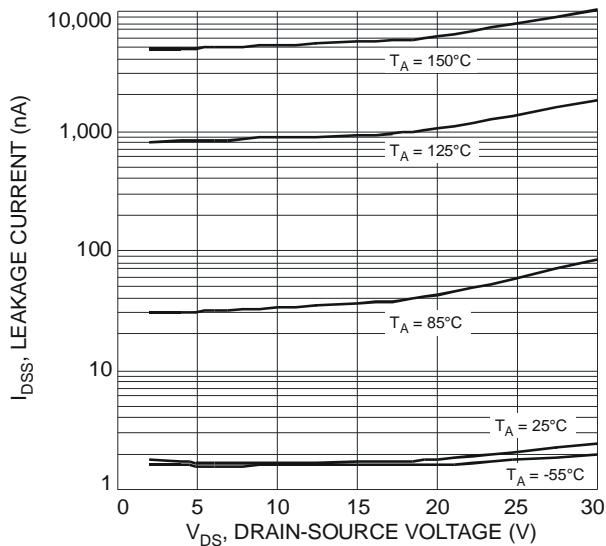


Fig. 11 Typical Leakage Current vs. Drain-Source Voltage

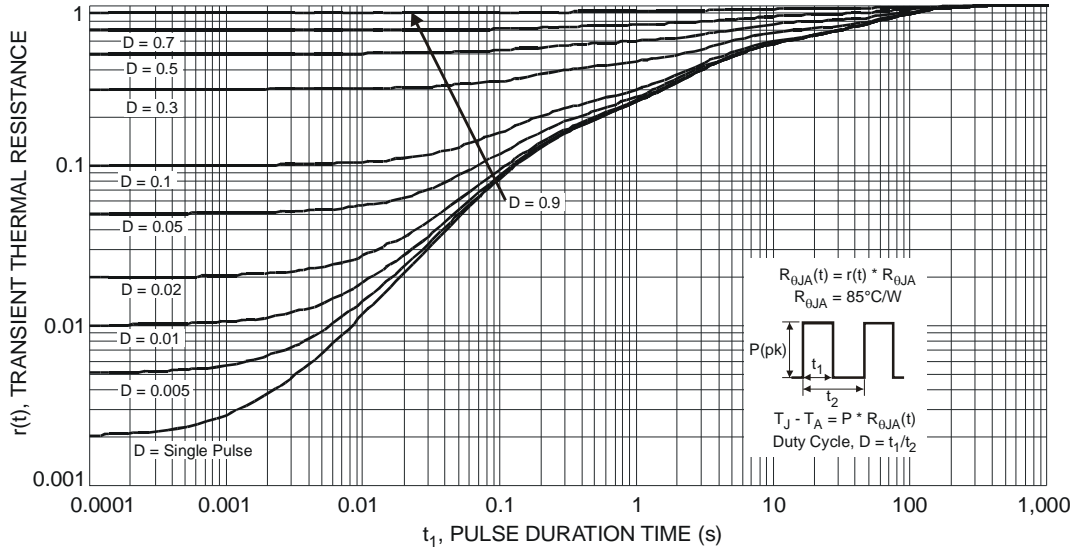
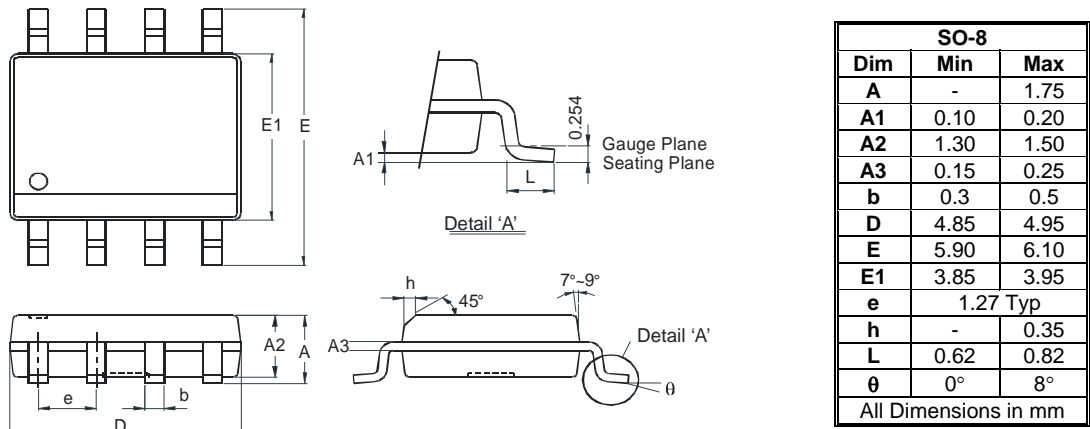
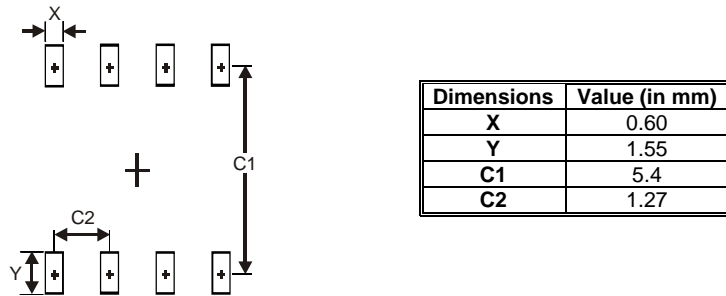


Fig. 12 Transient Thermal Response

Package Outline Dimensions



Suggested Pad Layout



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