



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = 25°C
30V	$28m\Omega$ @ $V_{GS} = 10V$	5.8A
	42mΩ @ V _{GS} = 4.5V	4.8A

Description and Applications

This MOSFET has been designed to minimize the on-state resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Battery Charging
- Power Management Functions
- DC-DC Converters
- Portable Power Adaptors

Features and Benefits

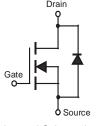
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

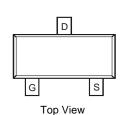
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe.
 Solderable per MIL-STD-202, Method 208
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (approximate)







Internal Schematic



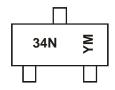
Ordering Information (Note 3)

Part Number	Qualification	Case	Packaging
DMN3404L-7	Commercial	SOT23	3000/Tape & Reel
DMN3404LQ-7	Automotive	SOT23	3000/Tape & Reel

Notes:

- 1. No purposefully added lead.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com.
- 3. For packaging details, go to our website at http://www.diodes.com.

Marking Information



34N = Product Type Marking Code YM = Date Code Marking Y = Year (ex: W = 2009) M = Month (ex: 9 = September)

Date Code Key

Year	200	9	2010		2011	20	12	2013		2014	2	2015
Code	W		Χ		Υ		7	Α		В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V_{DSS}	30	V		
Gate-Source Voltage	V _{GSS}	±20	V		
Continuous Drain Current (Note 4) V _{GS} = 10V	Steady State	$T_A = 25$ °C $T_A = 70$ °C	I _D	4.2 3.5	Α
Continuous Drain Current (Note 5) V _{GS} = 10V	I _D	5.8 4.9	А		
Pulsed Drain Current	I _{DM}	30	А		

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 4)	PD	0.72	W
Thermal Resistance, Junction to Ambient @T _A = 25°C	R _{0JA}	173	°C/W
Power Dissipation (Note 5)	P _D	1.4	W
Thermal Resistance, Junction to Ambient @T _A = 25°C	R _{0JA}	90	°C/W
Operating and Storage Temperature Range	$T_{J_i} T_{STG}$	-55 to +150	°C

Electrical Characteristics @TA = 25°C unless otherwise specified

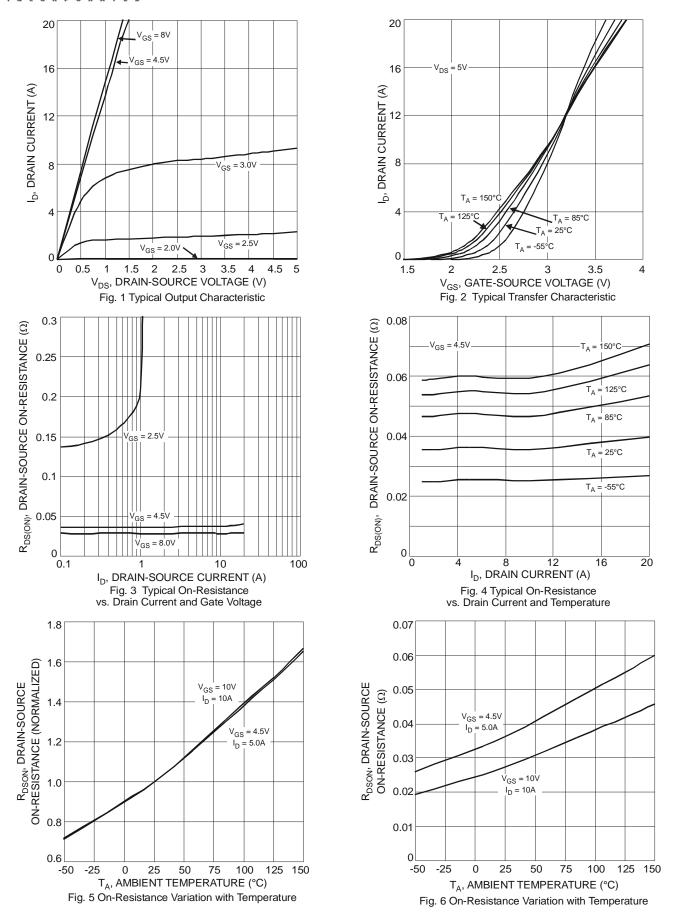
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current TJ = 25°C	I _{DSS}	1	-	1.0	μΑ	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V _{GS(th)}	1.0	1.5	2.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance	D		24	28	mΩ	$V_{GS} = 10V, I_D = 5.8A$	
Static Drain-Source On-Resistance	R _{DS (ON)}	-	33	42	111 2 2	$V_{GS} = 4.5V, I_D = 4.8A$	
Forward Transfer Admittance	Y _{fs}	-	10	-	S	$V_{DS} = 5V, I_{D} = 5.8A$	
Diode Forward Voltage	V_{SD}	-	0.75	1.0	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS							
Input Capacitance	C _{iss}	-	386	-	pF	15)/)/ 0)/	
Output Capacitance	Coss	-	44	-	рF	$V_{DS} = 15V, V_{GS} = 0V,$ -f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	-	39	-	pF	T = T.OIVII IZ	
Gate Resistance	R_g	-	1.51	-	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg	-	9.2	-	nC		
Gate-Source Charge	Q _{gs}	-	1.2	-	nC	$V_{GS} = 10V, V_{DS} = 15V, I_D = 5.8A$	
Gate-Drain Charge	Q _{qd}	-	1.8	-	nC		
Turn-On Delay Time	t _{D(on)}	-	3.41	-	ns		
Turn-On Rise Time	t _r	-	6.18	-	ns	$V_{DD} = 15V, V_{GS} = 10V,$	
Turn-Off Delay Time	t _{D(off)}	-	13.92	-	ns	$R_L = 2.6\Omega$, $R_G = 3\Omega$	
Turn-Off Fall Time	t _f	-	2.84	-	ns		

Notes:

6. Short duration pulse test used to minimize self-heating effect.

^{4.} Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.







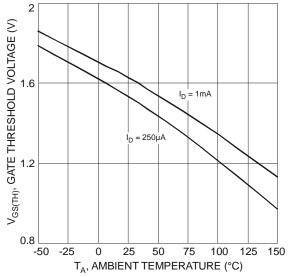
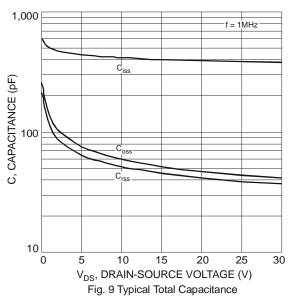
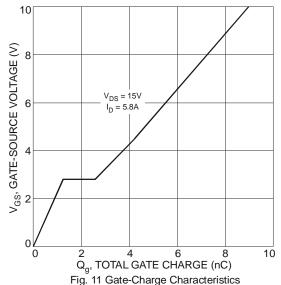
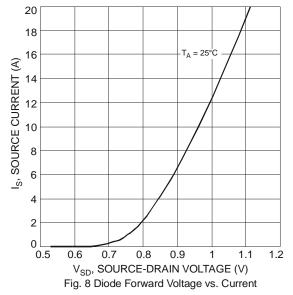


Fig. 7 Gate Threshold Variation vs. Ambient Temperature







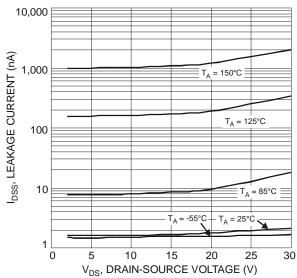
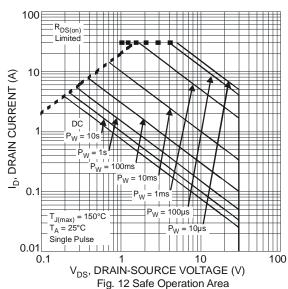
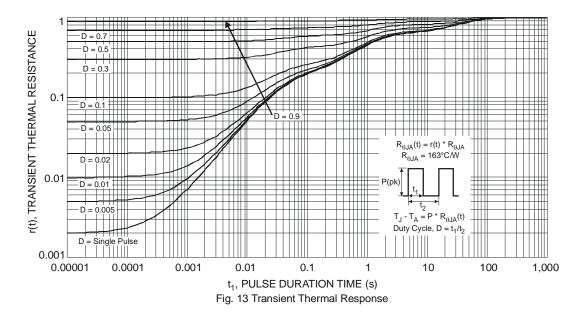


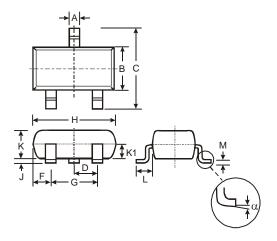
Fig. 10 Typical Leakage Current vs. Drain-Source Voltage





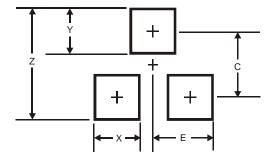


Package Outline Dimensions



SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
C	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Ι	2.80	3.00	2.90				
7	0.013	0.10	0.05				
K	0.903	1.10	1.00				
K1	-	-	0.400				
L	0.45	0.61	0.55				
М	0.085	0.18	0.11				
α	0°	8°	-				
All Dimensions in mm							

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35



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