





N-CHANNEL ENHANCEMENT MODE MOSFET

Features

- N-Channel MOSFET
- Low On-Resistance
- Very Low Gate Threshold Voltage, 1.2V max
- Low Input Capacitance
- · Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- ESD Protected Gate
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

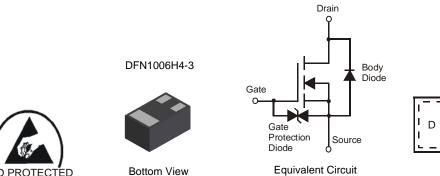
Mechanical Data

- Case: DFN1006H4-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208

Top View

Pin-Out

Weight: 0.001 grams (approximate)



Ordering Information (Note 3)

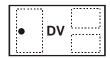
Part Number	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DMN32D2LFB4-7	DV	7	8	3000
DMN32D2LFB4-7B	DV	7	8	10,000

Notes:

- 1. No purposefully added lead
- 2. Diodes Inc's "Green" policy can be found on our website at http://www.diodes.com.
- 3. For packaging details, go to our website at http://www.diodes.com.

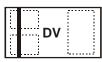
Marking Information





Top View Dot Denotes Drain Side

DMN32D2LFB4-7B



Top View Bar Denotes Gate and Source Side

DV = Product Type Marking Code



Maximum Ratings @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V_{DSS}	30	V
Gate-Source Voltage	V _{GSS}	±10	V
Drain Current (Note 4)	I _D	300	mA

Thermal Characteristics @TA = 25°C unless otherwise specified

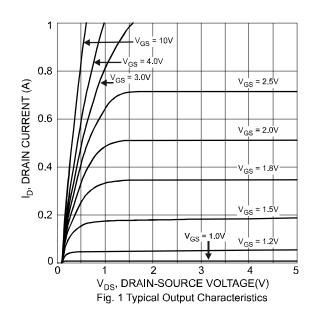
Total Power Dissipation (Note 4) @T _A = 25°C	P_{D}	350	mW
Thermal Resistance, Junction to Ambient (Note 4)	$R_{ hetaJA}$	357	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

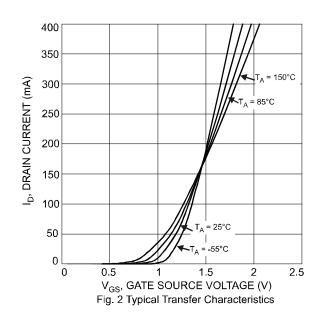
Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)							
Drain-Source Breakdown Voltage		BV _{DSS}	30	_	_	V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current	@ T _C = 25°C	I _{DSS}	_	_	1	μА	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Body Leakage	I _{GSS}	looo	ss —	_	±10	μА	$V_{GS} = \pm 10V$, $V_{DS} = 0V$
Gate Body Leakage		IGSS			±500	nA	$V_{GS} = \pm 5V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage		$V_{GS(th)}$	0.6	_	1.2	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
			_	_	2.2		$V_{GS} = 1.8V, I_D = 20mA$
Static Drain-Source On-Resistance		R _{DS (ON)}	_	_	1.5	Ω	$V_{GS} = 2.5V, I_D = 20mA$
			_	_	1.2		$V_{GS} = 4.0V, I_D = 100mA$
Forward Transconductance		Y _{fs}	100	_	_	mS	$V_{DS} = 10V, I_D = 0.1A$
Source-Drain Diode Forward Voltage		V_{SD}	0.5	_	1.4	V	$V_{GS} = 0V, I_{S} = 115mA$
DYNAMIC CHARACTERISTICS							
Input Capacitance		C _{iss}	_	39	_	рF	$V_{DS} = 3V, V_{GS} = 0V$ f = 1.0MHz
Output Capacitance		Coss	_	10	_	pF	
Reverse Transfer Capacitance		C _{rss}	_	3.6	_	pF	1 - 1.0IVII IZ
Switching Time	Turn-on Time	ton		11	_	nS	$V_{DD} = 5V, I_D = 10 \text{ mA},$
Switching Time	Turn-off Time	t _{off}	_	51	_	nS	$V_{GS} = 0.5V$

Notes:

- 4. Device mounted on FR-4 PCB, pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com.
- 5. Short duration pulse test used to minimize self-heating effect.







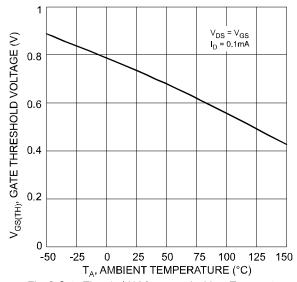


Fig. 3 Gate Threshold Voltage vs. Ambient Temperature

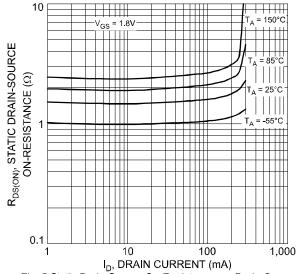


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current

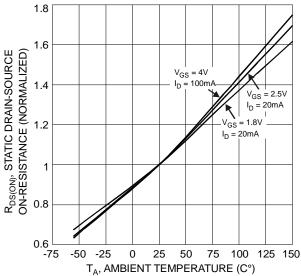


Fig. 7 Normalized Static Drain-Source On-Resistance vs. Ambient Temperature

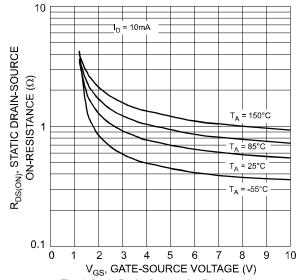
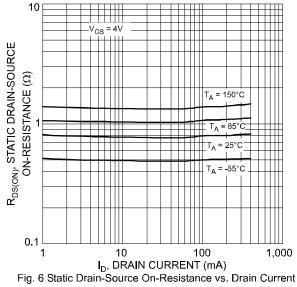


Fig. 4 Static Drain-Source On-Resistance vs. Gate-Source Voltage



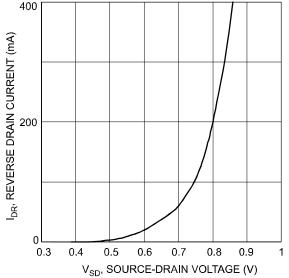
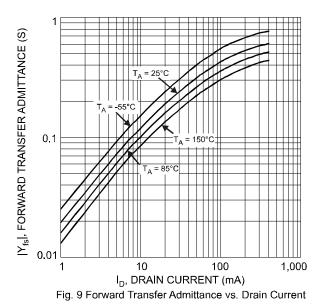
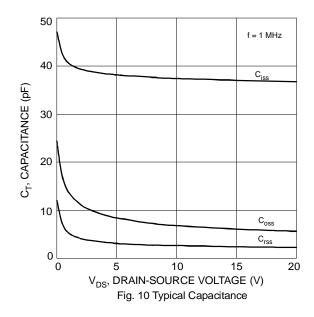


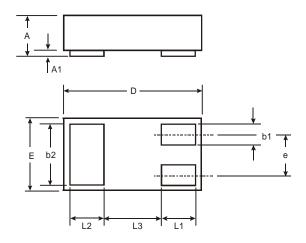
Fig. 8 Reverse Drain Current vs. Source-Drain Voltage





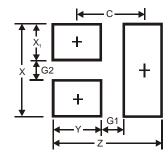


Package Outline Dimensions



DFN1006H4-3				
Dim	Min	Max	Тур	
Α		0.40	_	
A1	0	0.05	0.02	
b1	0.10	0.20	0.15	
b2	0.45	0.55	0.50	
D	0.95	1.075	1.00	
Е	0.55	0.675	0.60	
е	_	_	0.35	
L1	0.20	0.30	0.25	
L2	0.20	0.30	0.25	
L3	_	_	0.40	
All Dimensions in mm				

Suggested Pad Layout



Dimensions	Value (in mm)
Z	1.1
G1	0.3
G2	0.2
Х	0.7
X1	0.25
Υ	0.4
С	0.7



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