July 2011





N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = 25°C
	$73m\Omega$ @ $V_{GS} = 10V$	3.3A
30V	110mΩ @ V _{GS} = 4.5V	2.7A

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 standards for High Reliability

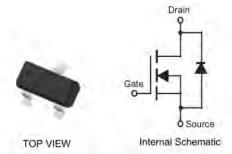
Description and Applications

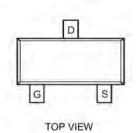
This MOSFET has been designed to minimize the on-state resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- General Purpose Interfacing Switch
- **Power Management Functions**
- **Boost Application**
- Analog Switch

Mechanical Data

- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See diagram
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.027 grams (approximate)





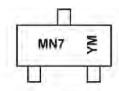
Ordering Information (Note 3)

ĺ	Part Number	Case	Packaging
	DMN3110S-7	SOT-23	3000/Tape & Reel

Notes:

- 1. No purposefully added lead.
- 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com.
- 3. For packaging details, go to our website at http://www.diodes.com.

Marking Information



MN7 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: W = 2009) M = Month (ex: 9 = September)

Date Code Key

Year	2009	9	2010		2011	20	12	2013		2014	2	2015
Code	W		X		Y	Z		Α		В		C
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
MOTILLI												



Maximum Ratings @TA = 25°C unless otherwise specified

Characteristic		Symbol	Value	Units	
Drain-Source Voltage		V _{DSS}	30	V	
Gate-Source Voltage		V_{GSS}	±20	V	
Continuous Drain Current (Note 4) V _{GS} = 10V	Steady State	T _A = 25°C T _A = 70°C	I _D	2.5 2.0	Α
Continuous Drain Current (Note 5) V _{GS} = 10V	Steady State	$T_A = 25$ °C $T_A = 70$ °C	I _D	3.3 2.7	А
Continuous Drain Current (Note 5) V _{GS} = 10V	t≦10sec	$T_A = 25$ °C $T_A = 70$ °C	I _D	3.8 3.1	А
Continuous Drain Current (Note 5) V _{GS} = 4.5V	Steady State	T _A = 25°C T _A = 70°C	I _D	2.7 2.1	А
Pulsed Drain Current (Note 6)			I _{DM}	25	A

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 4)	P _D	0.74	W
Thermal Resistance, Junction to Ambient (Note 4)	$R_{ heta JA}$	173.4	°C/W
Total Power Dissipation (Note 5)	P _D	1.3	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ heta JA}$	99.1	°C/W
Total Power Dissipation (Note 5) t≤10sec	P _D	1.8	W
Thermal Resistance, Junction to Ambient (Note 5) t≦10sec	$R_{ hetaJA}$	72	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

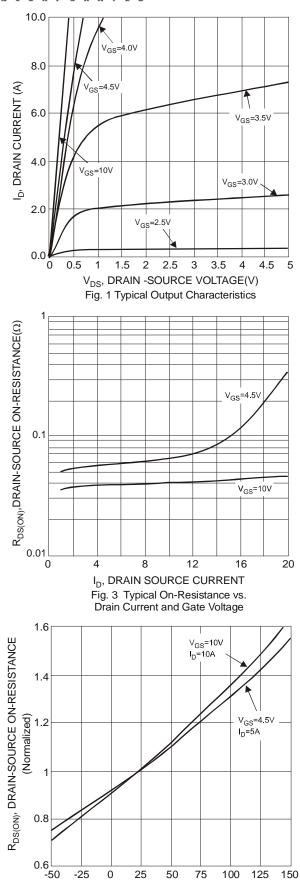
Electrical Characteristics @TA = 25°C unless otherwise specified

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						•	
Drain-Source Breakdown Voltage		BV_{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	$@T_c = 25^{\circ}C$	I _{DSS}	1	-	1.0	μΑ	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage		I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage		$V_{GS(th)}$	1.0	-	3.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance		D	ı	54	73	$\mathbf{m}\Omega$	$V_{GS} = 10V, I_D = 3.1A$
Static Drain-Source On-Resistance		R _{DS (ON)}	1	88	110	111 2 2	$V_{GS} = 4.5V, I_D = 2A$
Forward Transfer Admittance		Y _{fs}	ı	4.8	-	mS	$V_{DS} = 10V, I_D = 3.1A$
Diode Forward Voltage (Note 6)		V_{SD}	1	0.75	1.0	V	$V_{GS} = 0V, I_{S} = 1A$
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance		C_{iss}		305.8	-	pF	45)/)/
Output Capacitance		Coss	-	39.9	-	pF	$V_{DS} = 15V, V_{GS} = 0V,$ -f = 1.0MHz
Reverse Transfer Capacitance		C_{rss}		39.5	-	pF	I = 1.0IVII IZ
Gate Resistance		R_g	-	1.4	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V _{GS} = 4.5V)		Q_g	-	4.1	-	nC	
Total Gate Charge (V _{GS} = 10V)		Q_g	-	8.6	-	nC	$V_{GS} = 10V, V_{DS} = 10V,$
Gate-Source Charge		Q _{gs}	-	1.2	-	nC	$I_D = 3A$
Gate-Drain Charge		Q_{gd}	-	1.5	-	nC	1
Turn-On Delay Time		t _{D(on)}	-	2.6	-	ns	
Turn-On Rise Time		t _r	-	4.6	-	ns	$V_{DD} = 15V, V_{GS} = 10V,$
Turn-Off Delay Time		t _{D(off)}	-	13.1	-	ns	$R_L = 47\Omega$, $R_G = 3\Omega$,
Turn-Off Fall Time		t _f	-	2.5	-	ns	

4. Device mounted on FR-4 PCB, with minimum recommended pad layout.

- 4. Device mounted on FR-4 FCB, with minimum recommended pad layout.
 5. Device mounted on FR-4 substrate PC board, 2oz copper, on 1 inch square copper plate
 6. Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%
 7. Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to product testing.





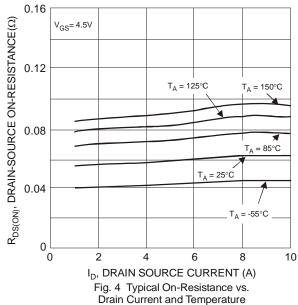
T_.I, JUNCTION TEMPERATURE (°C)

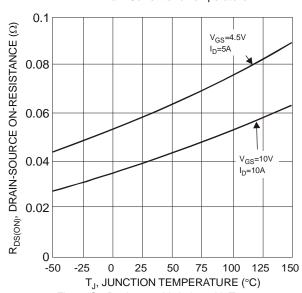
Fig. 5 On-Resistance Variation with Temperature

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| T_A = 150°C | T_A = 25°C |
| T_A = 85°C |
| T_A = -55°C |
| T







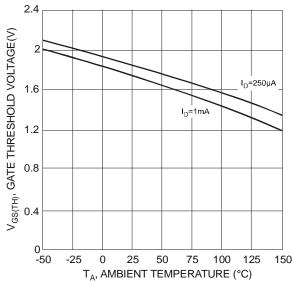
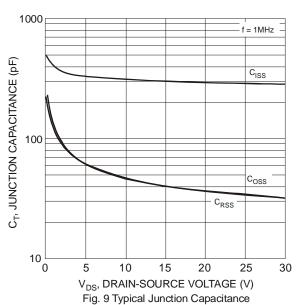
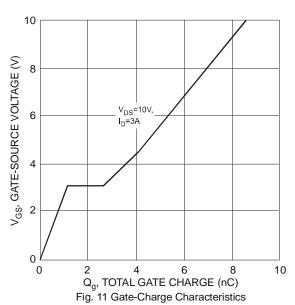
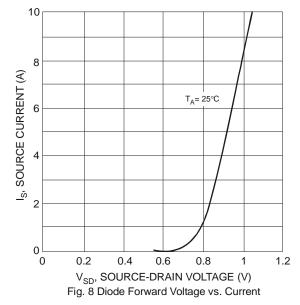


Fig. 7 Gate Threshold Variation vs. Ambient Temperature







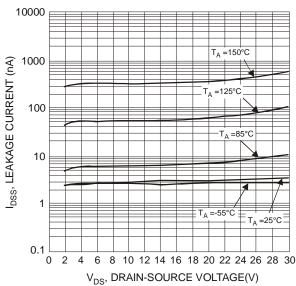
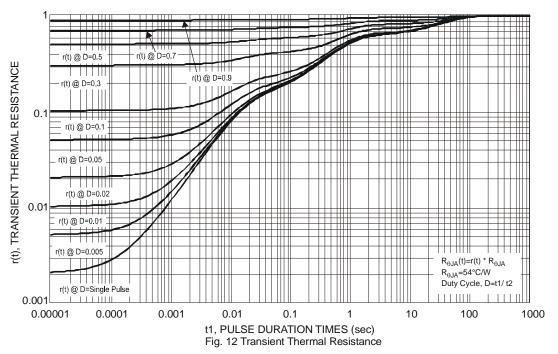
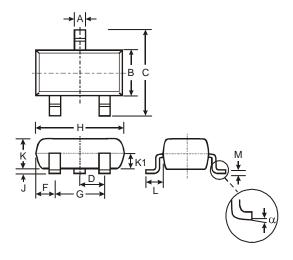


Fig. 10 Typical Drain-Source Leakage Current vs. Voltage



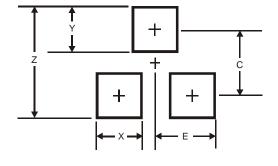


Package Outline Dimensions



SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
C	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
7	0.013	0.10	0.05				
K	0.903	1.10	1.00				
K1	-	1	0.400				
٦	0.45	0.61	0.55				
М	0.085	0.18	0.11				
α	0°	8°	-				
All Dimensions in mm							

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Υ	0.9
С	2.0
E	1.35



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