

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)}$	I_D max $T_A = 25^\circ\text{C}$ (Note 4)
20V	175m Ω @ $V_{GS} = 4.5\text{V}$	1.30A
	240m Ω @ $V_{GS} = 2.5\text{V}$	1.11A
	360m Ω @ $V_{GS} = 1.8\text{V}$	0.91A

Description and Applications

This MOSFET has been designed to minimize the on-state resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Load switch

Features and Benefits

- Footprint of just 0.6mm² – thirteen times smaller than SOT23
- 0.5mm profile – ideal for low profile applications
- On resistance <200m Ω @ $V_{GS} = 4.5\text{V}$
- Low Gate Threshold Voltage
- Fast Switching Speed
- “Lead Free”, RoHS Compliant (Note 1)
- Halogen and Antimony Free. “Green” Device (Note 2)
- ESD Protected Gate 2KV
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

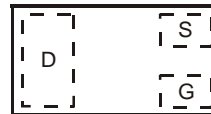
- Case: DFN1006-3
- Case Material: Molded Plastic, “Green” Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.001 grams (approximate)



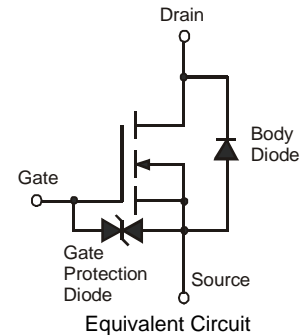
DFN1006-3



Bottom View



Top View
Internal Schematic



Ordering Information (Note 3)

Part Number	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DMN2300UFB-7	NI	7	8	3,000
DMN2300UFB-7B	NI	7	8	10,000

- Notes:
1. No purposefully added lead
 2. Diodes Inc's "Green" policy can be found on our website at <http://www.diodes.com>.
 3. For packaging details, go to our website at <http://www.diodes.com>.

Marking Information

DMN2300UFB-7



Top View
Dot Denotes
Drain Side

DMN2300UFB-7B



Top View
Bar Denotes Gate
and Source Side

NI = Product Type Marking Code

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Drain Current	Steady State	T _A = 25°C (Note 4)	I _D	1.32	A
		T _A = 85°C (Note 4)		0.94	
		T _A = 25°C (Note 5)		1.78	
Pulsed Drain Current (Note 6)			I _{DM}	8	A

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 4)	P _D	0.468	W
Power Dissipation (Note 5)	P _D	1.2	W
Thermal Resistance, Junction to Ambient (Note 4)	R _{θJA}	267	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	104	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	-	-	V	V _{GS} = 0V, I _D = 10μA
Zero Gate Voltage Drain Current T _J = 25°C	I _{DSS}	-	-	1	μA	V _{DS} = 20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	-	-	10	μA	V _{GS} = ±8V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	0.45	-	0.95	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(on)}	-	-	175	mΩ	V _{GS} = 4.5V, I _D = 300mA
		-	-	240		V _{GS} = 2.5V, I _D = 250mA
		-	-	360		V _{GS} = 1.8V, I _D = 100mA
Forward Transfer Admittance	Y _{fs}	40	-	-	mS	V _{DS} = 3V, I _D = 30mA
Diode Forward Voltage	V _{SD}	-	0.7	1.2	V	V _{GS} = 0V, I _S = 300mA
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iss}	-	67.62	-	pF	V _{DS} = 20V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	-	9.74	-	pF	
Reverse Transfer Capacitance	C _{riss}	-	7.58	-	pF	
Gate Resistance	R _g	-	68.51	-	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge	Q _g	-	0.89	-	nC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 1A
Gate-Source Charge	Q _{gs}	-	0.14	-	nC	
Gate-Drain Charge	Q _{gd}	-	0.16	-	nC	
Turn-On Delay Time	t _{D(on)}	-	4.92	-	ns	V _{DS} = 10V, I _D = 1A V _{GS} = 4.5V, R _G = 6Ω
Turn-On Rise Time	t _r	-	6.93	-	ns	
Turn-Off Delay Time	t _{D(off)}	-	21.71	-	ns	
Turn-Off Fall Time	t _f	-	10.62	-	ns	

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout
 - Device mounted on FR-4 substrate PC board, 2oz copper, with 25mm X 25mm square copper plate
 - Device mounted on minimum recommended pad layout test board, 10μs pulse duty cycle = 1%.
 - Short duration pulse test used to minimize self-heating effect.

DMN2300UFB

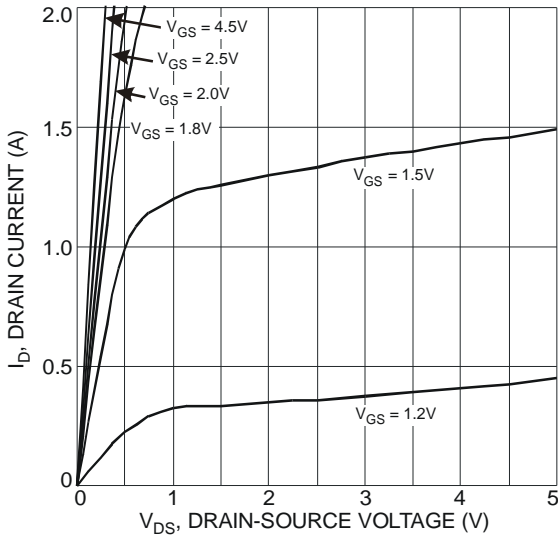


Fig. 1 Typical Output Characteristic

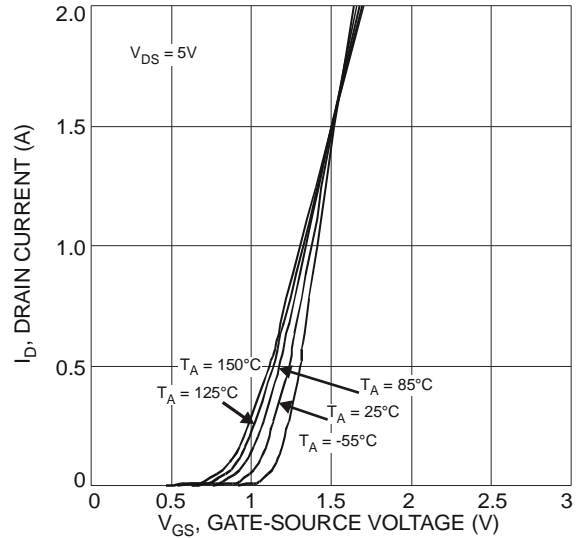


Fig. 2 Typical Transfer Characteristic

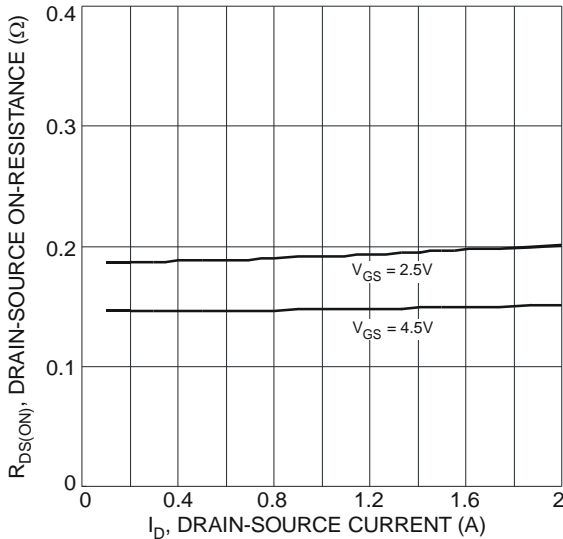


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

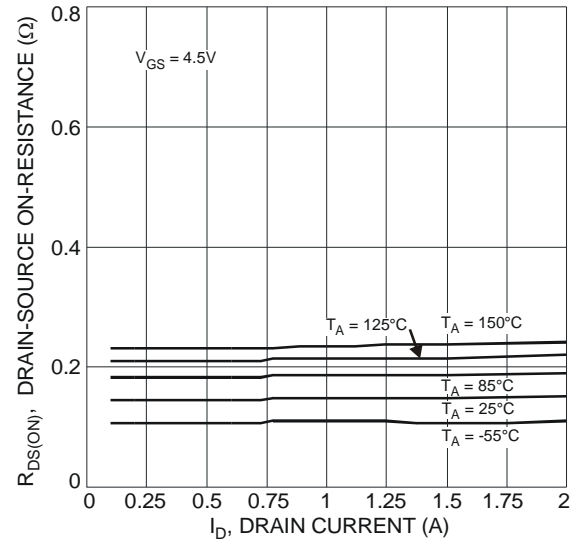


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

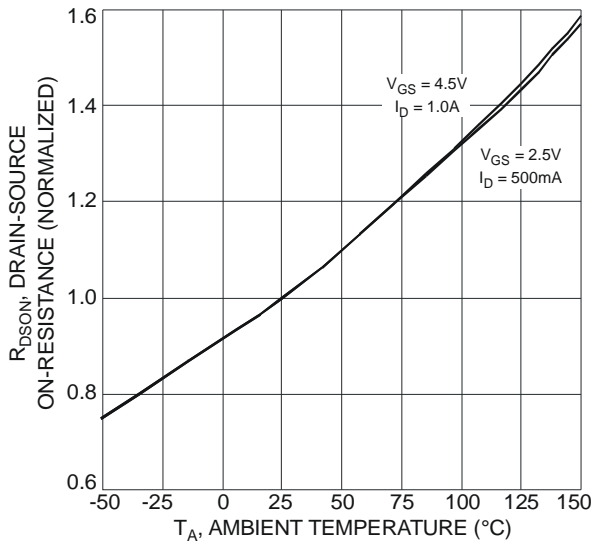


Fig. 5 On-Resistance Variation with Temperature

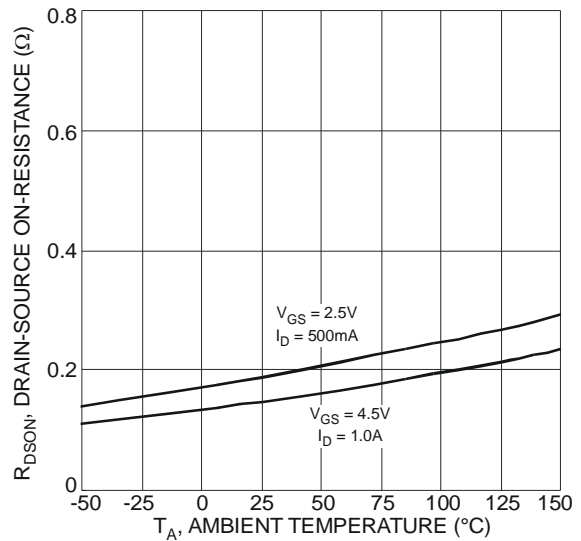


Fig. 6 On-Resistance Variation with Temperature

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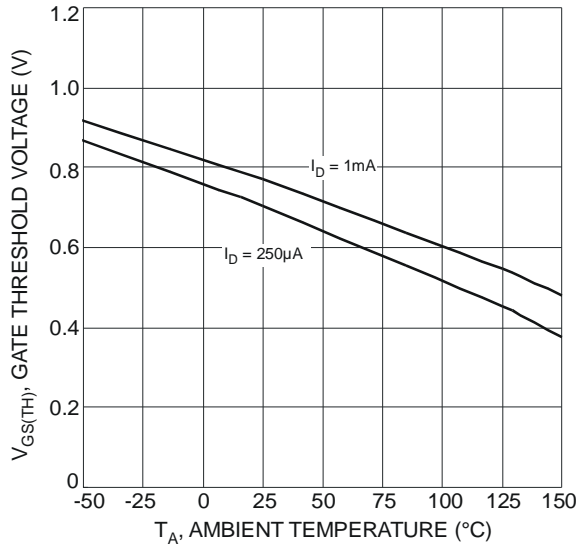


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

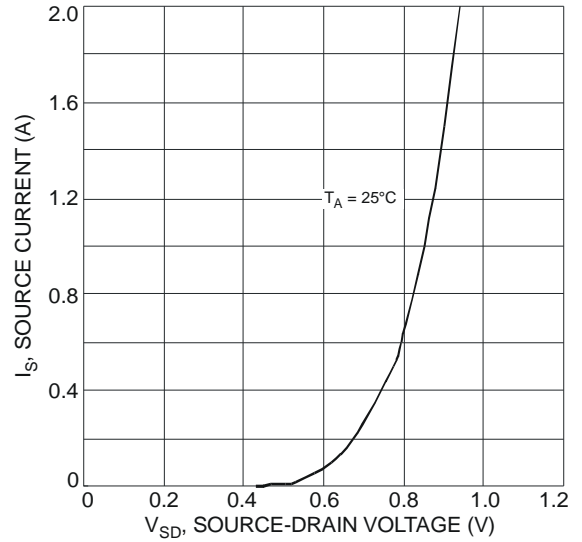


Fig. 8 Diode Forward Voltage vs. Current

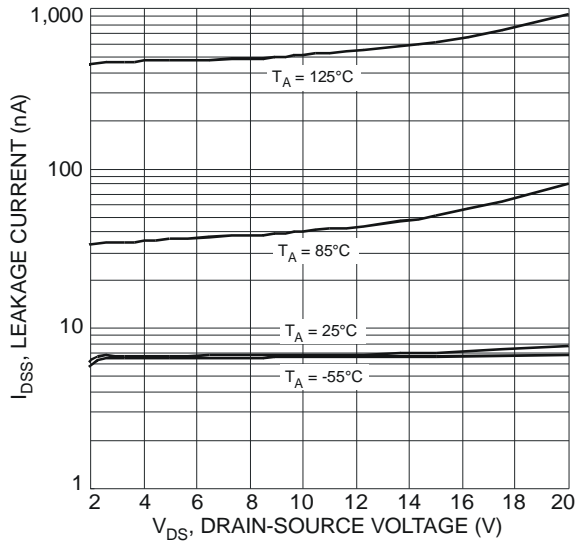


Fig. 9 Typical Leakage Current vs. Drain-Source Voltage

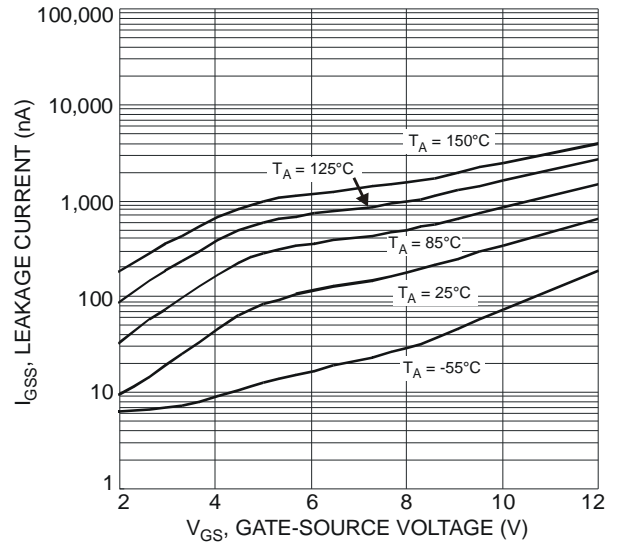


Fig.10 Leakage Current vs. Gate-Source Voltage

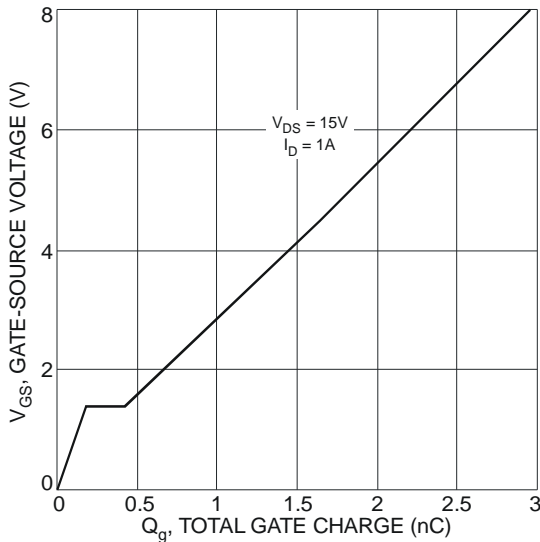


Fig. 11 Gate-Charge Characteristics

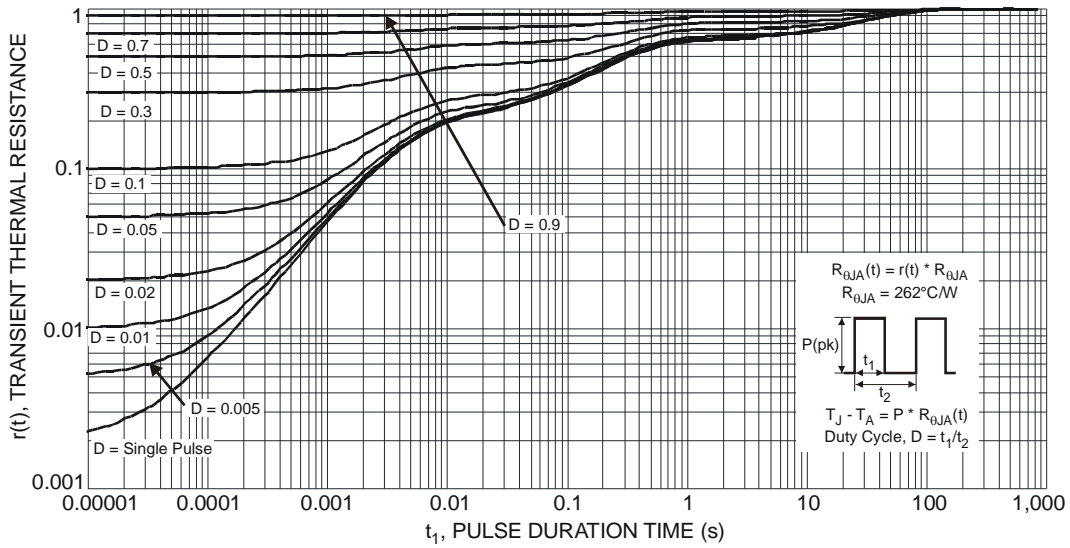
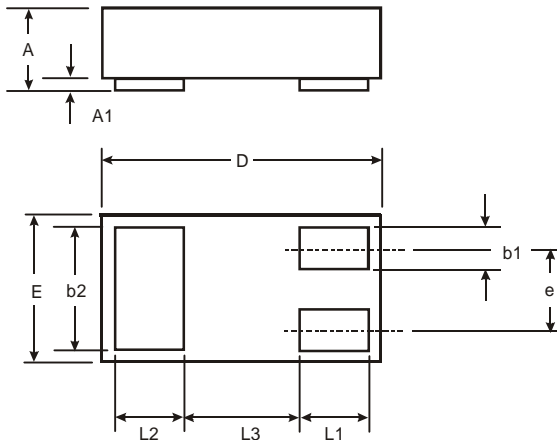


Fig. 12 Transient Thermal Response

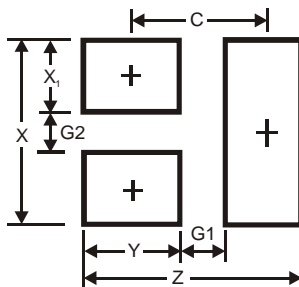
Package Outline Dimensions



DFN1006-3			
Dim	Min	Max	Typ
A	0.47	0.53	0.50
A1	0	0.05	0.03
b1	0.10	0.20	0.15
b2	0.45	0.55	0.50
D	0.95	1.075	1.00
E	0.55	0.675	0.60
e	—	—	0.35
L1	0.20	0.30	0.25
L2	0.20	0.30	0.25
L3	—	—	0.40

All Dimensions in mm

Suggested Pad Layout



Dimensions	Value (in mm)
Z	1.1
G1	0.3
G2	0.2
X	0.7
X1	0.25
Y	0.4
C	0.7

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