Ordering number : ENA1776A

SANYO Semiconductors

DATA SHEET

An ON Semiconductor Company

N-Channel Silicon MOSFET

MCH6437 — General-Purpose Switching Device Applications

Features

- ON-resistance $R_{DS}(on)1$=18mΩ (typ.)
- 1.8V drive
- Protection diode in

Specifications

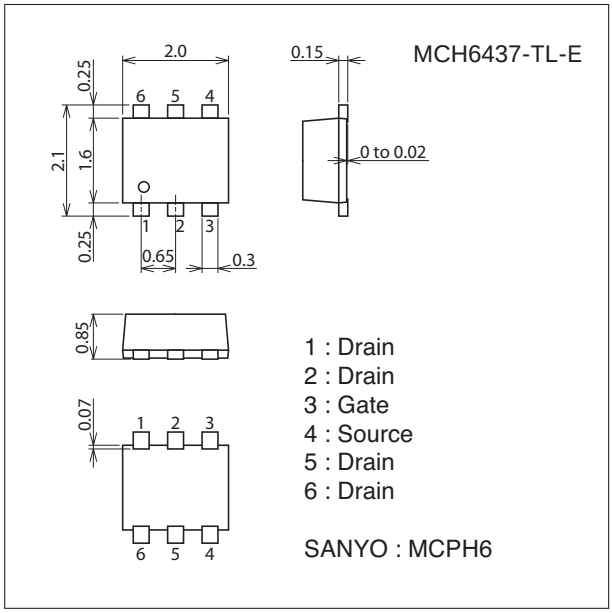
Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		20	V
Gate-to-Source Voltage	V_{GSS}		±12	V
Drain Current (DC)	I_D		7	A
Drain Current (Pulse)	I_{DP}	PW≤10μs, duty cycle≤1%	28	A
Allowable Power Dissipation	P_D	When mounted on ceramic substrate (1200mm²×0.8mm)	1.5	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		−55 to +150	°C

Package Dimensions

unit : mm (typ)

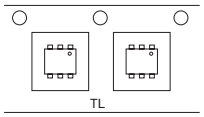
7022A-009



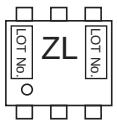
Product & Package Information

- Package : MCPH6
- JEITA, JEDEC : SC-88, SC-70-6, SOT-363
- Minimum Packing Quantity : 3,000 pcs./reel

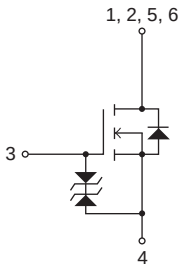
Taping Type : TL



Marking



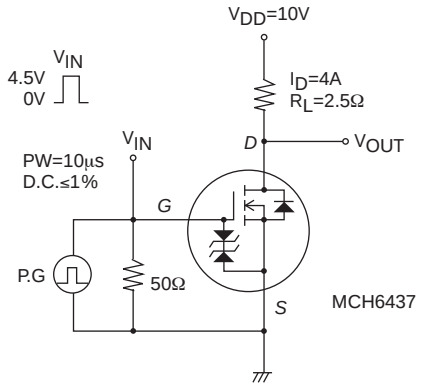
Electrical Connection



Electrical Characteristics at Ta=25°C

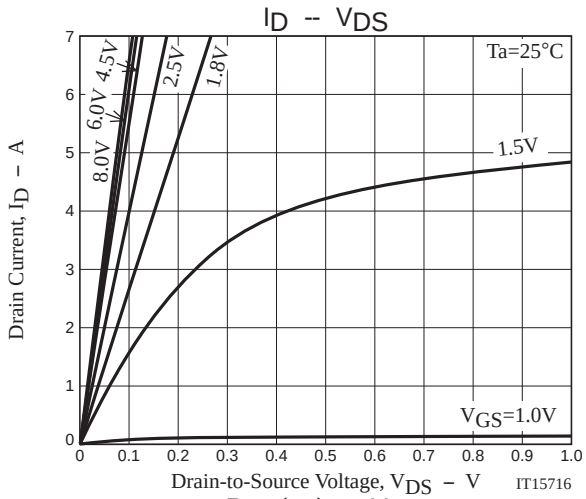
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=1mA, V_{GS}=0V	20			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=20V, V_{GS}=0V			1	μA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}=±8V, V_{DS}=0V			±10	μA
Cutoff Voltage	V_{GS}(off)	V_{DS}=10V, I_D=1mA	0.4		1.3	V
Forward Transfer Admittance	| yfs |	V_{DS}=10V, I_D=4A		6.2		S
Static Drain-to-Source On-State Resistance	R_{DS}(on)1	I_D=4A, V_{GS}=4.5V		18	24	mΩ
	R_{DS}(on)2	I_D=2A, V_{GS}=2.5V		25	35	mΩ
	R_{DS}(on)3	I_D=1A, V_{GS}=1.8V		38	65	mΩ
Input Capacitance	Ciss	V_{DS}=10V, f=1MHz		660		pF
Output Capacitance	Coss			125		pF
Reverse Transfer Capacitance	Crss			100		pF
Turn-ON Delay Time	t_{d}(on)	See specified Test Circuit.		9.7		ns
Rise Time	t_r			53		ns
Turn-OFF Delay Time	t_{d}(off)			72		ns
Fall Time	t_f			65		ns
Total Gate Charge	Qg	V_{DS}=10V, V_{GS}=4.5V, I_D=7A		8.4		nC
Gate-to-Source Charge	Qgs			1.0		nC
Gate-to-Drain “Miller” Charge	Qgd			2.4		nC
Diode Forward Voltage	V_{SD}	I_S=7A, V_{GS}=0V		0.81	1.2	V

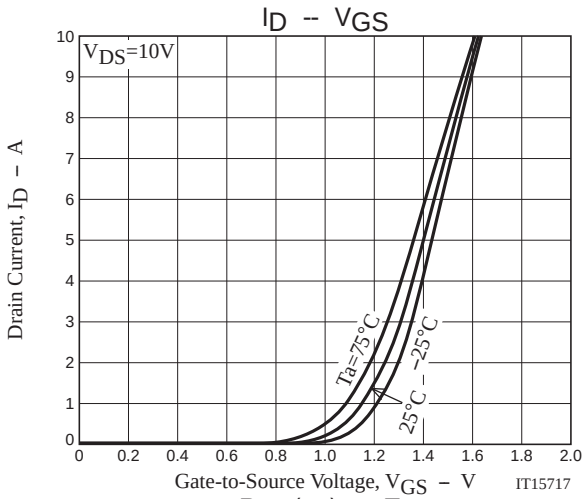
Switching Time Test Circuit

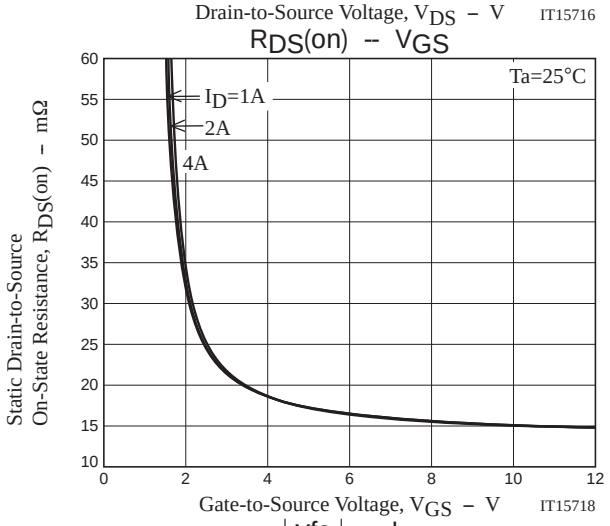


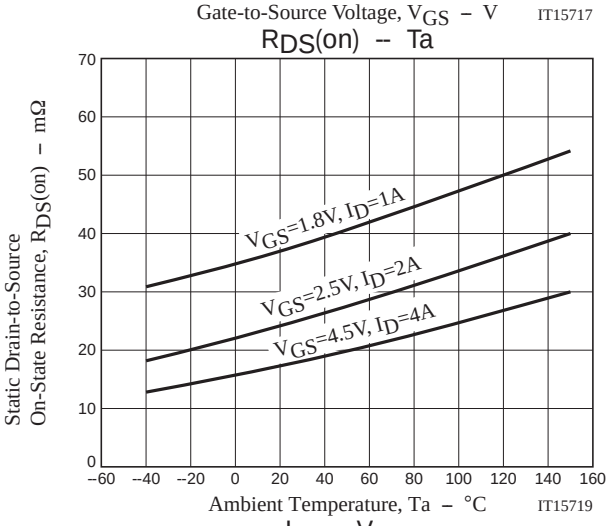
Ordering Information

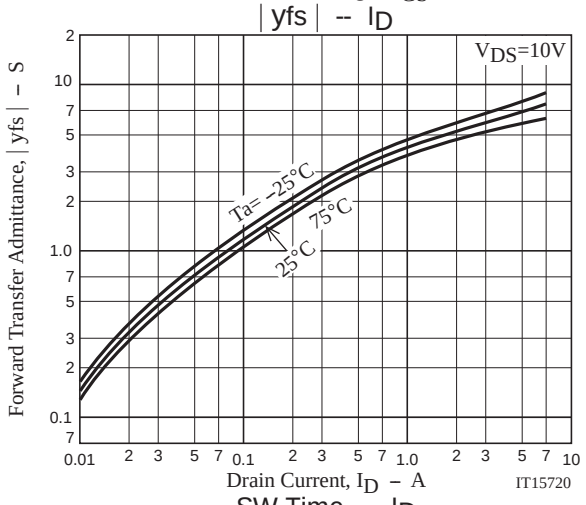
Device	Package	Shipping	memo
MCH6437-TL-E	MCPH6	3,000pcs./reel	Pb Free

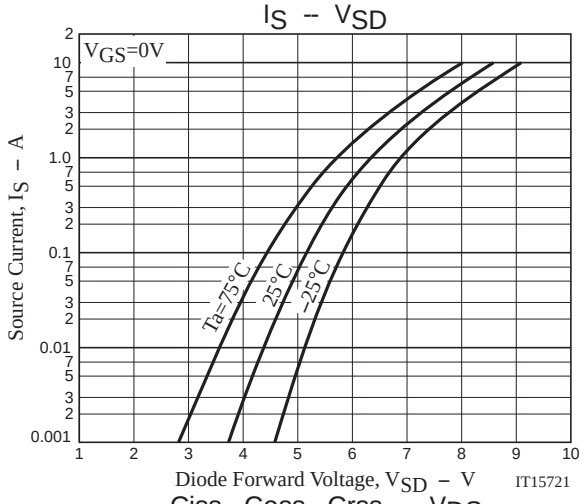

ID -- VDS
Ta=25°C
8.0V
6.0V
4.5V
2.5V
1.8V
1.5V
VGS=1.0V
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V
IT15716

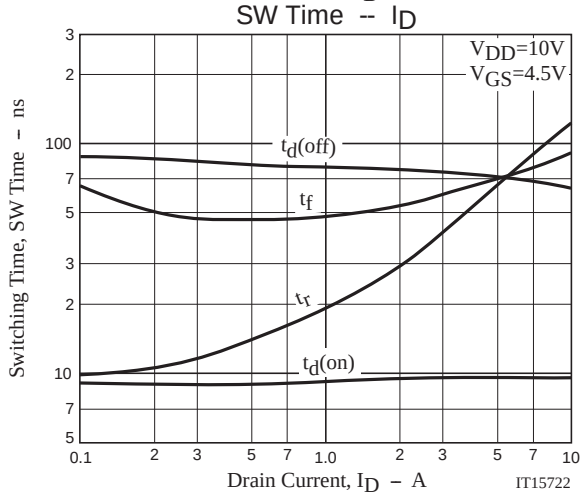

ID -- VGS
VDS=10V
Ta=75°C
25°C
–25°C
Drain Current, ID – A
Gate-to-Source Voltage, VGS – V
IT15717

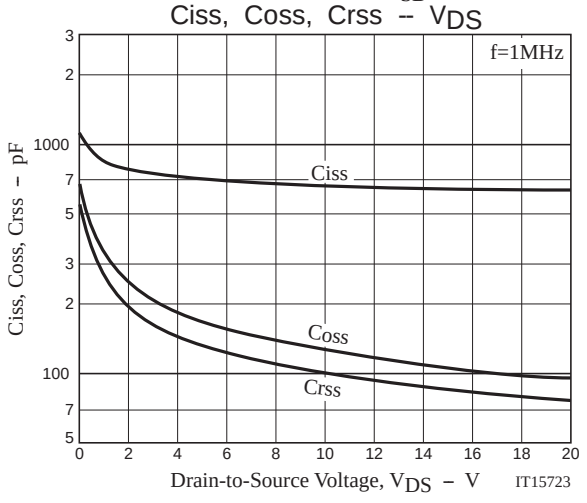

RDS(on) -- VGS
Ta=25°C
ID=1A
2A
4A
Static Drain-to-Source On-State Resistance, RDS(on) – mΩ
Gate-to-Source Voltage, VGS – V
IT15718

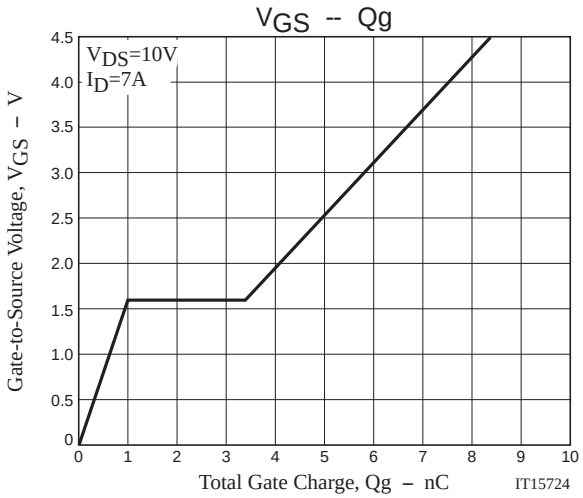

RDS(on) -- Ta
VGS=1.8V, ID=1A
VGS=2.5V, ID=2A
VGS=4.5V, ID=4A
Static Drain-to-Source On-State Resistance, RDS(on) – mΩ
Ambient Temperature, Ta – °C
IT15719

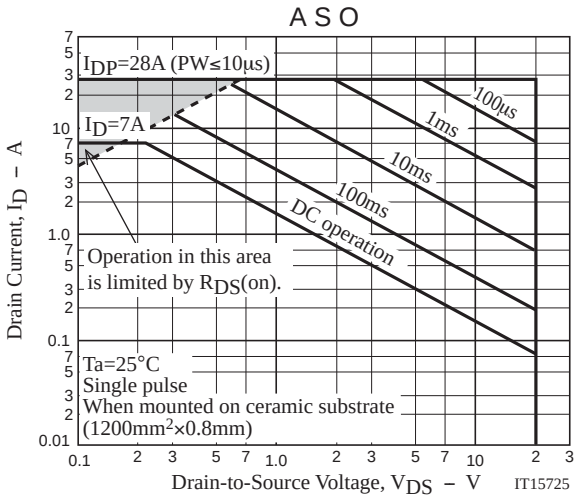

| yfs | -- ID
VDS=10V
Ta= –25°C
75°C
25°C
Forward Transfer Admittance, | yfs | – S
Drain Current, ID – A
IT15720

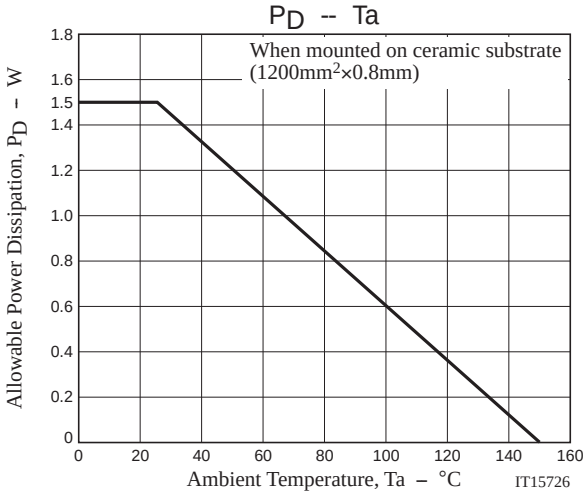

IS -- VSD
VGS=0V
Ta=75°C
25°C
–25°C
Source Current, IS – A
Diode Forward Voltage, VSD – V
IT15721


SW Time -- ID
VDD=10V
VGS=4.5V
td(off)
tf
tr
td(on)
Switching Time, SW Time – ns
Drain Current, ID – A
IT15722


Ciss, Coss, Crss -- VDS
f=1MHz
Ciss
Coss
Crss
Ciss, Coss, Crss – pF
Drain-to-Source Voltage, VDS – V
IT15723


VGS -- Qg
VDS=10V
ID=7A
Gate-to-Source Voltage, VGS – V
Total Gate Charge, Qg – nC
IT15724


A S O
IDP=28A (PW≤10μs)
ID=7A
100μs
1ms
10ms
100ms
DC operation
Operation in this area
is limited by RDS(on).
Ta=25°C
Single pulse
When mounted on ceramic substrate
(1200mm2×0.8mm)
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V
IT15725


PD -- Ta
When mounted on ceramic substrate
(1200mm2×0.8mm)
Allowable Power Dissipation, PD – W
Ambient Temperature, Ta – °C
IT15726

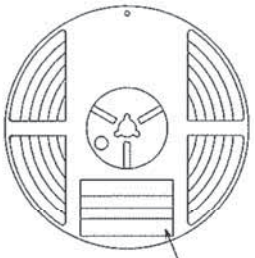
Taping Specification

MCH6437-TL-E

1. Packing Format

Package Name	Carrier Tape Type	Maximun Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
MCPH6	MCP4	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

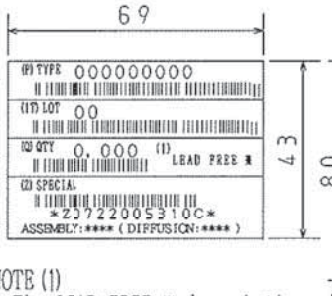
Packing method



Reel label

Reel label, Inner box label

(unit:mm)



Type No.

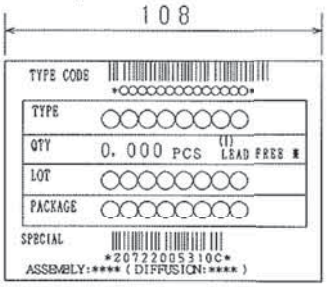
LOT No.

Quantity

Origin

Outer box label

It is a label at the time of factory shipments. The form of a label may change in physical distribution process.



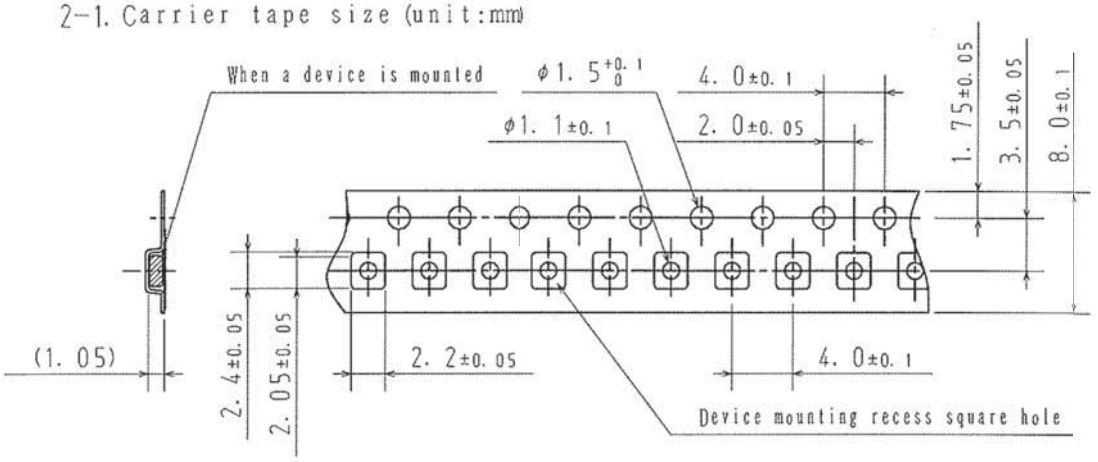
NOTE (1)

The LEAD FREE ※ description shows that the surface treatment of the terminal is lead free.

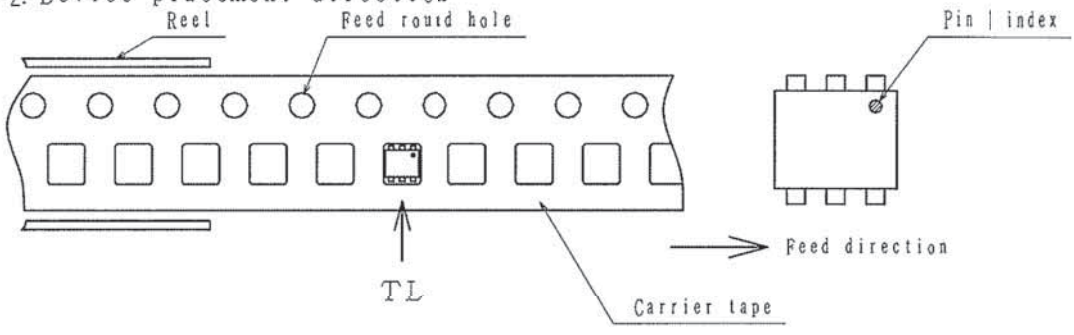
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



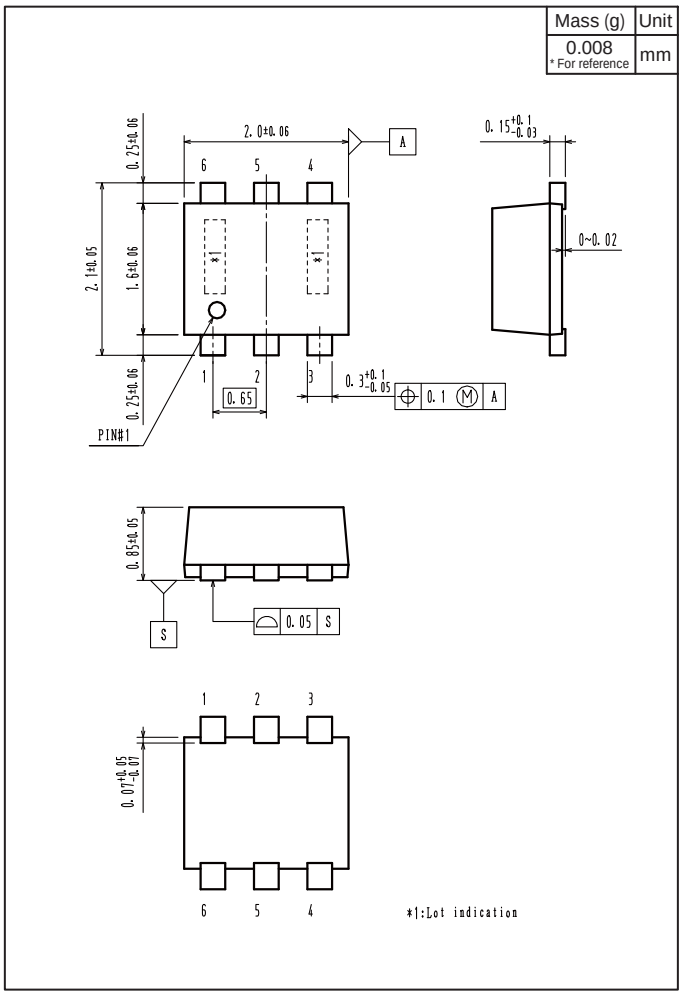
2-2. Device placement direction



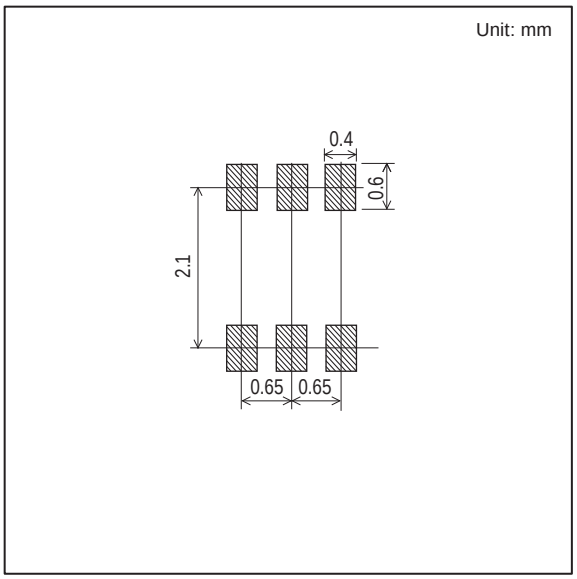
Those with pin 1 index on the feed hole side·····TL

Outline Drawing

MCH6437-TL-E



Land Pattern Example



Note on usage : Since the MCH6437 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

This catalog provides information as of June, 2012. Specifications and information herein are subject to change without notice.