TOSHIBA Transistor Silicon NPN Epitaxial Type

# **TPC6504**

High-Speed Switching Applications DC-DC Converter Applications Strobe Applications

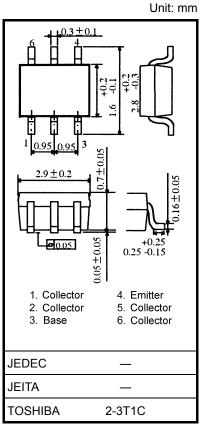
• High DC current gain :  $h_{FE} = 400 \text{ to } 1000 \text{ (I}_{C} = 0.1 \text{ A)}$ 

Low collector-emitter saturation voltage : V<sub>CE (sat)</sub> = 0.17 V (max)

• High-speed switching : t<sub>f</sub> = 85 ns (typ.)

## **Absolute Maximum Ratings (Ta = 25°C)**

Characteristic		Symbol	Rating	Unit	
Collector-base voltage		$V_{CBO}$	100	V	
Collector-emitter voltage		V <sub>CEX</sub>	80	V	
		V <sub>CEO</sub>	50	V	
Emitter-base voltage		V <sub>EBO</sub>	7	V	
Collector current (Note 1)	DC	Ic	1.0	Α	
	Pulse	I <sub>CP</sub>	2.0		
Base current		ΙΒ	0.1	Α	
Collector power	DC	-	0.8	W	
dissipation (Note 2)	t = 10 s	P <sub>C</sub>	1.6		
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	



Weight: 0.011 g (typ.)

- Note 1: Ensure that the channel temperature does not exceed 150°C during use of the device.
- Note 2: Mounted on an FR4 board (glass-epoxy; 1.6 mm thick; Cu area, 645 mm<sup>2</sup>)
- Note 3: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

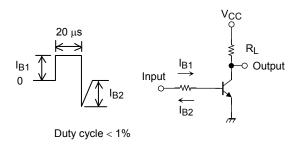
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



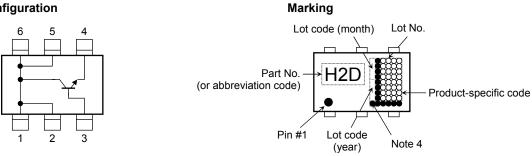
### **Electrical Characteristics (Ta = 25°C)**

Characteristic		Symbol	Test Conditions	Min	Тур.	Max	Unit
Collector cut-off current		I <sub>CBO</sub>	V <sub>CB</sub> = 100 V, I <sub>E</sub> = 0	_	_	100	nA
Emitter cut-off current		I <sub>EBO</sub>	V <sub>EB</sub> = 7 V, I <sub>C</sub> = 0	_	_	100	nA
Collector-emitter breakdown voltage		V (BR) CEO	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0	50	_	_	V
DC current gain		h <sub>FE</sub> (1)	V <sub>CE</sub> = 2 V, I <sub>C</sub> = 0.1 A	400	_	1000	
		h <sub>FE</sub> (2)	V <sub>CE</sub> = 2 V, I <sub>C</sub> = 0.3 A	200	_	_	
Collector-emitter saturation voltage		V <sub>CE</sub> (sat)	$I_C = 300 \text{ mA}, I_B = 6 \text{ mA}$	_	_	0.17	V
Base-emitter saturation voltage		V <sub>BE</sub> (sat)	$I_C = 300 \text{ mA}, I_B = 6 \text{ mA}$	_	_	1.1	V
Collector output capacitance		C <sub>ob</sub>	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1 MHz	_	5	_	pF
Switching time	Rise time	t <sub>r</sub>	See Figure 1 circuit diagram. $V_{CC} \approx 30 \text{ V, R}_L = 100 \Omega$	_	35	_	ns
	Storage time	t <sub>stg</sub>		_	680	_	
	Fall time	t <sub>f</sub>	$I_{B1} = I_{B2} = 10$ mA	_	85	_	

Figure 1. Switching Time Test Circuit & Timing Chart



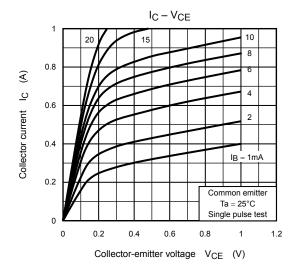
#### **Circuit Configuration**

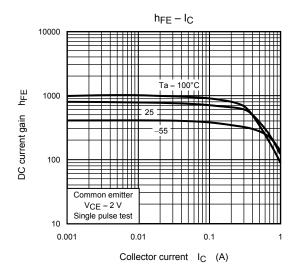


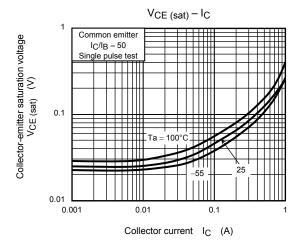
Note 4 : A dot marking identifies the indication of product Labels. [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

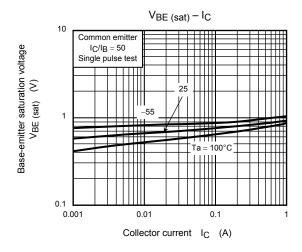
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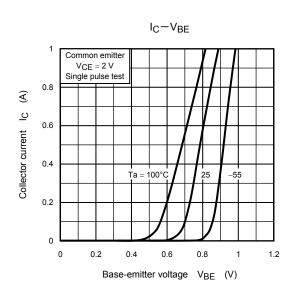
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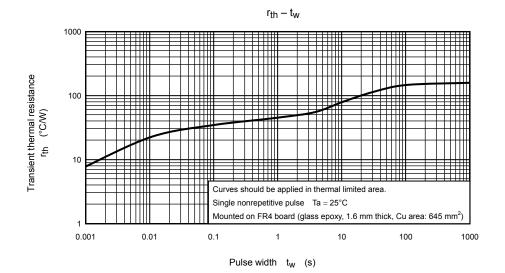


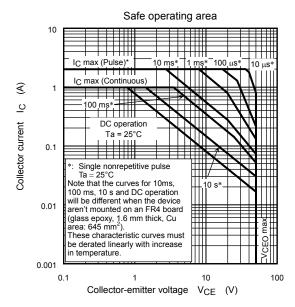






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