## DRC3A43E

### Silicon NPN epitaxial planar type

For digital circuits

Complementary to DRA3A43E

DRC9A43E in SSSMini3 type package

#### ■ Features

- ullet Low collector-emitter saturation voltage  $V_{\text{CE(sat)}}$
- Contributes to miniaturization of sets, reduction of component count.
- Eco-friendly Halogen-free package

#### ■ Packaging

Embossed type (Thermo-compression sealing): 10000 pcs / reel (standard)

#### ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	50	V
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	50	V
Collector current	$I_{C}$	80	mA
Total power dissipation	$P_{T}$	100	mW
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

#### ■ Package

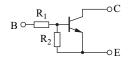
• Code

SSSMini3-F2-B

- Pin Name
  - 1: Base
  - 2: Emitter
  - 3: Collector

#### ■ Marking Symbol: J7

#### ■ Internal Connection



Resistance value	$R_1$	4.7	kΩ
	$R_2$	4.7	kΩ

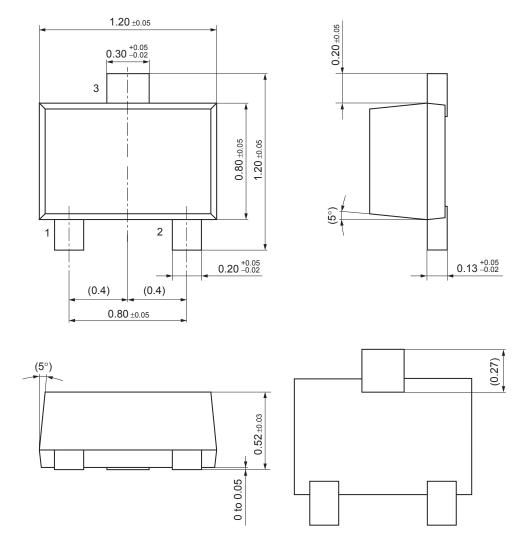
#### ■ Electrical Characteristics $T_a = 25$ °C±3°C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	$I_C = 10 \mu A, I_E = 0$	50			V
Collector-emitter voltage (Base open)	$V_{CEO}$	$I_C = 2 \text{ mA}, I_B = 0$	50			V
Collector-base cutoff current (Emitter open)	$I_{CBO}$	$V_{CB} = 50 \text{ V}, I_{E} = 0$			0.1	μΑ
Collector-emitter cutoff current (Base open)	I <sub>CEO</sub>	$V_{CE} = 50 \text{ V}, I_{B} = 0$			0.5	μΑ
Emitter-base cutoff current (Collector open)	I <sub>EBO</sub>	$V_{EB} = 6 \text{ V}, I_C = 0$			2.0	mA
Forward current transfer ratio	$h_{\mathrm{FE}}$	$V_{CE} = 10 \text{ V}, I_{C} = 5 \text{ mA}$	20			_
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = 10 \text{ mA}, I_B = 0.5 \text{ mA}$			0.25	V
Input voltage (ON)	V <sub>I(on)</sub>	$V_{CE} = 0.2 \text{ V}, I_{C} = 5 \text{ mA}$	1.9			V
Input voltage (OFF)	V <sub>I(off)</sub>	$V_{CE} = 5 \text{ V}, I_{C} = 100  \mu\text{A}$			0.8	V
Input resistance	$R_1$		-30%	4.7	+30%	kΩ
Resistance ratio	$R_1/R_2$		0.8	1.0	1.2	

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

### SSSMini3-F2-B

Unit: mm



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