DMC56606

Silicon NPN epitaxial planar type

For digital circuits

DMC26606 in SMini6 type package

Features

- \bullet High forward current transfer ratio h_{FE} with excellent linearity
- \bullet Low collector-emitter saturation voltage $V_{CE(sat)}$
- Contributes to miniaturization of sets, reduction of component count.
- Eco-friendly Halogen-free package

Basic Part Number

Dual DRC2143T (Individual)

Packaging

DMC566060R Embossed type (Thermo-compression sealing): 3000 pcs / reel (standard)

Absolute Maximum Ratings $T_a = 25^{\circ}C$

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V _{CBO}	50	V	
Collector-emitter voltage (Base open)	V _{CEO}	50	V	
Collector current	I _C	100	mA	
Total power dissipation	P _T	150	mW	
Junction temperature	Tj	150	°C	
Storage temperature	T _{stg}	-55 to +150	°C	

Package

• Code

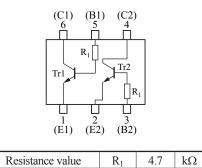
SMini6-F3-B

Package dimension clicks here. $\!\!\!\!\rightarrow$

• Pin Name

- 1: Emitter (Tr1)4: Collector (Tr2)2: Emitter (Tr2)5: Base (Tr1)
- 3: Base (Tr2) 6: Collector (Tr1)
- Marking Symbol: N0

Internal Connection



Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

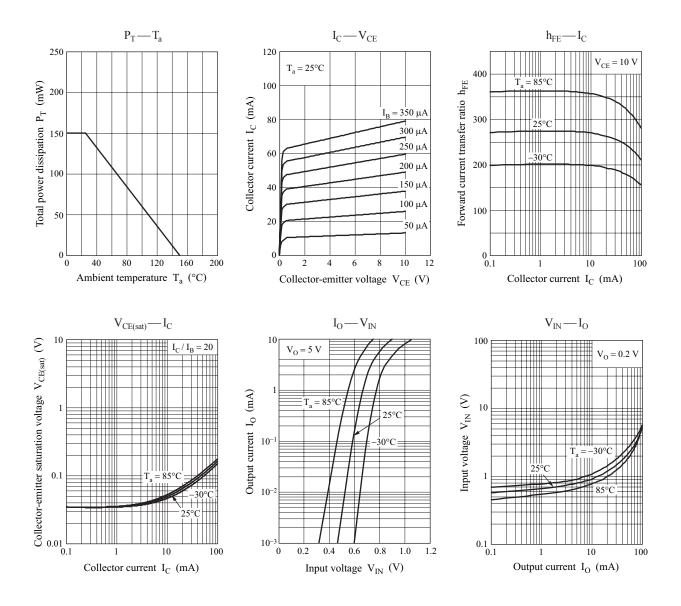
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = 10 \ \mu {\rm A}, I_{\rm E} = 0$	50			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = 2 {\rm mA}, I_{\rm B} = 0$	50			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = 50 \text{ V}, I_E = 0$			0.1	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{\rm CE} = 50$ V, $I_{\rm B} = 0$			0.5	μΑ
Emitter-base cutoff current (Collector open)	I _{EBO}	$V_{\rm EB} = 6 \text{ V}, I_{\rm C} = 0$			0.01	mA
Forward current transfer ratio	h _{FE}	$V_{CE} = 10 \text{ V}, I_C = 5 \text{ mA}$	160		460	_
h _{FE} ratio *	h _{FE} (Small/Large)	$V_{CE} = 10 \text{ V}, I_C = 5 \text{ mA}$	0.50	0.99		_
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 0.5 \text{ mA}$			0.25	V
Input voltage (ON)	V _{I(on)}	$V_{CE} = 0.2 \text{ V}, I_C = 5 \text{ mA}$	1.0			V
Input voltage (OFF)	V _{I(off)}	$V_{CE} = 5 \text{ V}, I_C = 100 \mu\text{A}$			0.4	V
Input resistance	R ₁		-30%	4.7	+30%	kΩ

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

2. *: Ratio between 2 elements

DMC56606

Panasonic



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