



## IMZ88

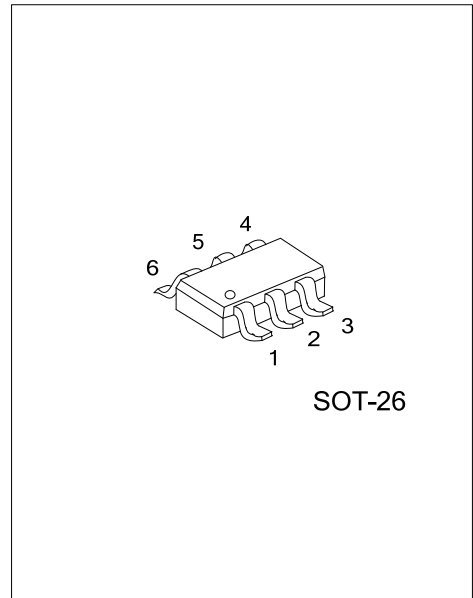
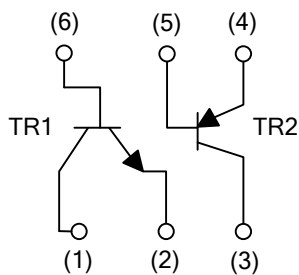
## DUAL TRANSISTOR

### GENERAL PURPOSE (DUAL TRANSISTOR)

#### FEATURES

\*Both a 8550S chip and 8050S chip in a SMT package

#### EQUIVALENT CIRCUITS

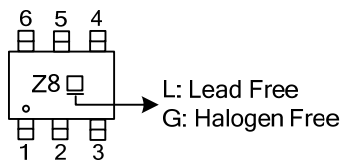


#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment						Packing
Lead Free	Halogen Free		1	2	3	4	5	6	
IMZ88L-AG6 -R	IMZ88G-AG6-R	SOT-26	C1	E1	C2	E2	B2	B1	Tape Reel

<p>IMZ88L-AG6-R</p> <p>(1) Packing Type (2) Package Type (3) Lead Free</p>	<p>(1) R: Tape Reel (2) AG6: SOT-26 (3) G: Halogen Free, L: Lead Free</p>
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#### MARKING



### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C)

PARAMETER		SYMBOL	RATING	UNIT
Collector-Base Voltage	TR1	V <sub>CBO</sub>	30	V
	TR2		-30	
Collector-Emitter Voltage	TR1	V <sub>CEO</sub>	20	V
	TR2		-20	
Emitter-Base Voltage	TR1	V <sub>EBO</sub>	5	V
	TR2		-5	
Collector Current	TR1	I <sub>C</sub>	700	mA
	TR2		-700	
Power Dissipation (Note 1)		P <sub>D</sub>	300	mW
Junction Temperature		T <sub>J</sub>	150	°C
Storage Temperature		T <sub>STG</sub>	-55~+150	°C

Note: 1. 200mW per element must not be exceeded.

- Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>TR1</b>						
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	I <sub>C</sub> =100μA, I <sub>E</sub> =0	30			V
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	I <sub>C</sub> =1mA, I <sub>B</sub> =0	20			V
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	I <sub>E</sub> =100μA, I <sub>C</sub> =0	5			V
Collector Cut-Off Current	I <sub>CBO</sub>	V <sub>CB</sub> =30V			1	μA
Emitter Cut-Off Current	I <sub>EBO</sub>	V <sub>EB</sub> =5V			100	μA
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	I <sub>C</sub> =500mA, I <sub>B</sub> =50mA			0.5	V
Base-emitter saturation voltage	V <sub>BE(SAT)</sub>	I <sub>C</sub> =500mA, I <sub>B</sub> =50mA			1.2	V
Base-emitter saturation voltage	V <sub>BE</sub>	V <sub>CE</sub> =1V, I <sub>C</sub> =10mA			1.0	V
DC Current Transfer Ratio	h <sub>FE1</sub>	V <sub>CE</sub> =1V, I <sub>C</sub> = 1mA	100			
	h <sub>FE2</sub>	V <sub>CE</sub> =1V, I <sub>C</sub> =150mA	120	110	400	
	h <sub>FE3</sub>	V <sub>CE</sub> =1V, I <sub>C</sub> =500mA	40			
Transition Frequency	f <sub>T</sub>	V <sub>CE</sub> =10V, I <sub>C</sub> =50mA	100			MHz
Output Capacitance	C <sub>OB</sub>	V <sub>CB</sub> =10V, I <sub>E</sub> =0, f=1MHz		9.0		pF
<b>TR2</b>						
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	I <sub>C</sub> =-100μA, I <sub>E</sub> =0	-30			V
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	I <sub>C</sub> = -1mA, I <sub>B</sub> =0	-20			V
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	I <sub>E</sub> =-100μA, I <sub>C</sub> =0	-5			V
Collector Cut-Off Current	I <sub>CBO</sub>	V <sub>CB</sub> =-30V, I <sub>E</sub> =0			-1	μA
Emitter Cut-Off Current	I <sub>EBO</sub>	V <sub>EB</sub> = -5V, I <sub>C</sub> =0			-100	μA
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	I <sub>C</sub> =-500mA, I <sub>B</sub> =-50mA			-0.5	V
Base-emitter saturation voltage	V <sub>BE(SAT)</sub>	I <sub>C</sub> = 500mA, I <sub>B</sub> =-50mA			-1.2	V
Base-emitter saturation voltage	V <sub>BE</sub>	V <sub>CE</sub> =-1V, I <sub>C</sub> =-10mA			-1.0	V
DC Current Transfer Ratio	h <sub>FE1</sub>	V <sub>CE</sub> = -1V, I <sub>C</sub> = -1mA	100			
	h <sub>FE2</sub>	V <sub>CE</sub> =-1V, I <sub>C</sub> =-150mA	120	110	400	
	h <sub>FE3</sub>	V <sub>CE</sub> =-1V, I <sub>C</sub> =-500mA	40			
Transition Frequency	f <sub>T</sub>	V <sub>CE</sub> =-10V, I <sub>C</sub> =-50mA	100			MHz
Output Capacitance	C <sub>OB</sub>	V <sub>CB</sub> =10V, I <sub>E</sub> =0, f=1MHz		9.0		pF

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