

TECHNICAL DATA
DATA SHEET 826, REV -

HERMETIC POWER MOSFET N-CHANNEL

FEATURES:

- 200 Volt, 0.045 Ohm, 50A MOSFET
- Hermetic Metal Package
- Low $R_{DS(on)}$

MAXIMUM RATINGS

ALL RATINGS ARE AT $T_C = 25^\circ\text{C}$ UNLESS OTHERWISE SPECIFIED.

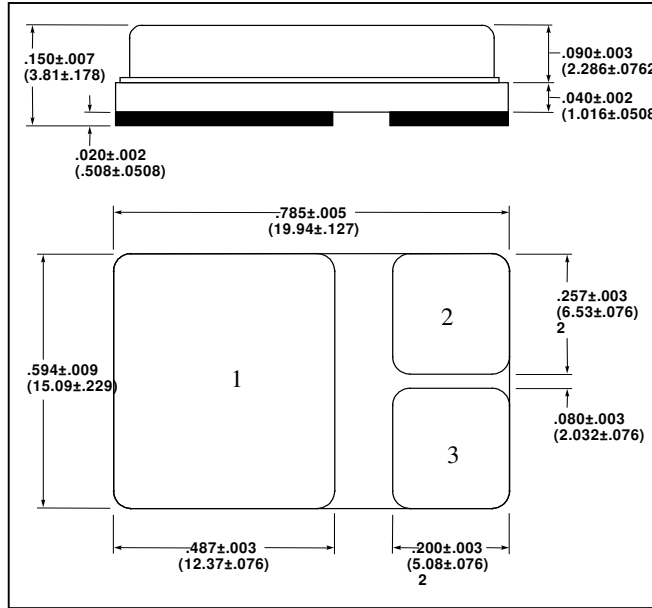
RATING	SYMBOL	MIN.	TYP.	MAX.	UNITS
GATE TO SOURCE VOLTAGE	V_{GS}	-	-	± 20	Volts
ON-STATE DRAIN CURRENT	I_{D25}	-	-	50	Amps
PULSED DRAIN CURRENT	I_{DM}	-	-	200	Amps
OPERATING AND STORAGE TEMPERATURE	T_J/T_{STG}	-55	-	+150	$^\circ\text{C}$
TOTAL DEVICE DISSIPATION	P_D	-	-	460	Watts
THERMAL RESISTANCE, JUNCTION TO CASE	$R_{\theta JC}$	-	-	0.27	$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS

DRAIN TO SOURCE BREAKDOWN VOLTAGE $V_{GS} = 0V, I_D = 250\mu A$	BV_{DSS}	200	-	-	Volts
STATIC DRAIN TO SOURCE ON STATE RESISTANCE $V_{GS} = 10V, I_D = 0.5I_{D25}$	$R_{DS(ON)}$	-	-	0.045	Ω
GATE THRESHOLD VOLTAGE $V_{DS} = V_{GS}, I_D = 4.0\text{ mA}$	$V_{GS(th)}$	2.0	-	4.0	Volts
FORWARD TRANSCONDUCTANCE $V_{DS} = 10V, I_D = 0.5I_{D25}$	g_{fs}	26	32	-	$S(1/\Omega)$
ZERO GATE VOLTAGE DRAIN CURRENT $V_{DS} = 0.8 \times \text{Max. rating}, V_{GS} = 0V, T_J = 25^\circ\text{C}$ $T_J = 125^\circ\text{C}$	I_{DSS}	-	-	200 1000	μA
GATE TO SOURCE LEAKAGE FORWARD $V_{GS} = 20V$	I_{GSS}	-	-	100	nA
GATE TO SOURCE LEAKAGE REVERSE $V_{GS} = -20V$				-100	
TURN ON DELAY TIME RISE TIME $V_{DS} = 0.5V \bullet$ $V_{DSS}, I_D = 0.5 \bullet I_{D25}$	$t_{d(ON)}$ t_r	-	18 15	25 20	nsec
TURN OFF DELAY TIME FALL TIME $V_{GS} = 10V$ $R_G = 1.0\Omega$	$t_{d(OFF)}$ t_f		72 16	90 25	
DIODE FORWARD VOLTAGE $I_F = I_S, V_{GS} = 0V$ Pulse test, $t \leq 300\mu s$, duty cycle $d \leq 2\%$	V_{SD}	-	-	1.5	Volts
REVERSE RECOVERY TIME $T_J = 25^\circ\text{C}$ $I_F = 25A, V_R = 100V$ $di/dt = 100A/\mu\text{sec}$	t_{rr}	-	400	-	nsec
INPUT CAPACITANCE OUTPUT CAPACITANCE REVERSE TRANSFER CAPACITANCE $V_{GS} = 0V,$ $V_{DS} = 25V,$ $f = 1.0\text{MHz}$	C_{iss} C_{oss} C_{rss}	-	4400 800 280	-	pF

SENSITRON
DATA SHEET 826
REVISION -

MECHANICAL DIMENSIONS: in Inches / mm



SHD-6

PINOUT TABLE

DEVICE TYPE	PIN 1	PIN 2	PIN 3
MOSFET SHD-6 PACKAGE	DRAIN	SOURCE	GATE

TECHNICAL DATA

DISCLAIMER:

- 1- The information given herein, including the specifications and dimensions, is subject to change without prior notice to improve product characteristics. Before ordering, purchasers are advised to contact the Sensitron Semiconductor sales department for the latest version of the datasheet(s).
- 2- In cases where extremely high reliability is required (such as use in nuclear power control, aerospace and aviation, traffic equipment, medical equipment, and safety equipment), safety should be ensured by using semiconductor devices that feature assured safety or by means of users' fail-safe precautions or other arrangement.
- 3- In no event shall Sensitron Semiconductor be liable for any damages that may result from an accident or any other cause during operation of the user's units according to the datasheet(s). Sensitron Semiconductor assumes no responsibility for any intellectual property claims or any other problems that may result from applications of information, products or circuits described in the datasheets.
- 4- In no event shall Sensitron Semiconductor be liable for any failure in a semiconductor device or any secondary damage resulting from use at a value exceeding the absolute maximum rating.
- 5- No license is granted by the datasheet(s) under any patents or other rights of any third party or Sensitron Semiconductor.
- 6- The datasheet(s) may not be reproduced or duplicated, in any form, in whole or part, without the expressed written permission of Sensitron Semiconductor.
- 7- The products (technologies) described in the datasheet(s) are not to be provided to any party whose purpose in their application will hinder maintenance of international peace and safety nor are they to be applied to that purpose by their direct purchasers or any third party. When exporting these products (technologies), the necessary procedures are to be taken in accordance with related laws and regulations.