

TECHNICAL DATA DATA SHEET 746, REV. -

# HERMETIC POWER MOSFET N-CHANNEL

#### **FEATURES:**

- 100 Volt, 0.077 Ohm, 28A MOSFET
- Isolated Hermetic Metal Package
- Fast Switching
- Low R<sub>DS (on)</sub>
- Equivalent to IRFM140 Series
- Add Suffix 'C' to the Part Number for Ceramic Seals

## **MAXIMUM RATINGS**

ALL RATINGS ARE AT  $T_{\rm C}$  = 25°C UNLESS OTHERWISE SPECIFIED.

RATING	SYMBOL	MIN.	TYP.	MAX.	UNITS
GATE TO SOURCE VOLTAGE	$V_{GS}$	-	-	±20	Volts
ON-STATE DRAIN CURRENT @ $T_C = 25^{\circ}C$	I <sub>D</sub>	-	-	28	Amps
$@ T_C = 100^{\circ}C$		-	-	20	
PULSED DRAIN CURRENT @ T <sub>C</sub> = 25°C	I <sub>DM</sub>	-	-	112	Amps
OPERATING AND STORAGE TEMPERATURE	T <sub>OP</sub> /T <sub>STG</sub>	-55	-	+150	°C
THERMAL RESISTANCE, JUNCTION TO CASE	$R_{ heta JC}$	-	-	0.88	°C/W
TOTAL DEVICE DISSIPATION @ T <sub>C</sub> = 25°C	P <sub>D</sub>	-	-	140	Watts

## **ELECTRICAL CHARACTERISTICS**

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNITS
DRAIN TO SOURCE BREAKDOWN VOLTAGE	BV <sub>DSS</sub>	100	-	-	Volts
$V_{GS} = 0V, I_{D} = 1.0 \text{mA}$	<b>A</b>				
STATIC DRAIN TO SOURCE ON STATE RESISTANCE		-	-		
$V_{GS} = 10V, I_D = 20A$	$R_{DS(ON)}$			0.077	Ω
GATE THRESHOLD VOLTAGE $V_{DS} = V_{GS}$ , $I_D = 250 \mu$ A	$V_{GS(th)}$	2.0	-	4.0	Volts
FORWARD TRANSCONDUCTANCE	$g_{fs}$	9.1	-	-	S(1/Ω)
$V_{DS} \ge 15V, I_{D} = 20A$	A				
ZERO GATE VOLTAGE DRAIN CURRENT		-	-		
$V_{DS} = 0.8xMax$ . Rating, $V_{GS} = 0V$	I <sub>DSS</sub>			25	μΑ
$V_{DS} = 0.8$ xMax. Rating, $V_{GS} = 0$ V, $T_{J} = 125$ °C	;			250	
GATE TO SOURCE LEAKAGE FORWARD V <sub>GS</sub> = 20V	/ I <sub>GSS</sub>	-	-	100	nA
GATE TO SOURCE LEAKAGE REVERSE $V_{GS} = -20$	/			-100	
TURN ON DELAY TIME $V_{DD} = 50V$	$t_{d(ON)}$	-	-	21	
RISE TIME $I_D = 20A$	, t <sub>r</sub>			145	nsec
TURN OFF DELAY TIME $R_G = 9.1\Omega$	$t_{d(OFF)}$			64	
FALL TIME	t <sub>f</sub>			105	
DIODE FORWARD VOLTAGE $T_J = 25$ °C, $I_S = 28$ A	, $V_{SD}$	-	-	1.5	Volts
$V_{GS} = 0$	/				
REVERSE RECOVERY TIME $T_J = 25^{\circ}C$	, t <sub>rr</sub>	-	-	400	
I <sub>F</sub> = 28A	,				nsec
$di/ds = 100A/\mu sec, V_{DD} \le 50V$	'				
INPUT CAPACITANCE $V_{GS} = 0 V$	C <sub>iss</sub>	-	1600	-	
OUTPUT CAPACITANCE $V_{DS} = 25 \text{ V}$	C <sub>oss</sub>		550		pF
REVERSE TRANSFER CAPACITANCE f = 1.0MHz	C <sub>rss</sub>		120		

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**TO-254** 

## **PINOUT TABLE**

DEVICE TYPE	PIN 1	PIN 2	PIN 3
MOSFET	DRAIN	SOURCE	GATE
TO-254 PACKAGE			



#### **TECHNICAL DATA**

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