TECHNICAL DATA DATA SHEET 607, REV B

# HERMETIC POWER MOSFET N-CHANNEL

### **FEATURES:**

- 400 Volt, 0.3 Ohm, 9.0A MOSFET
- Low R<sub>DS (on)</sub>
- Equivalent to IRF350 Series

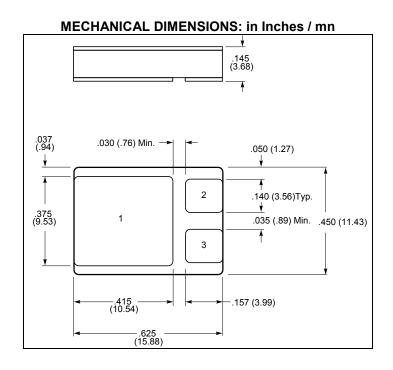
## MAXIMUM RATINGS

ALL RATINGS ARE AT  $T_{\rm C}$  = 25°C UNLESS OTHERWISE SPECIFIED.

RATING	SYMBOL	MIN.	TYP.	MAX.	UNITS
GATE TO SOURCE VOLTAGE	$V_{GS}$	-	-	±20	Volts
ON-STATE DRAIN CURRENT @ $T_C = 25^{\circ}C$	I <sub>D</sub>	-	ı	14	Amps
ON-STATE DRAIN CURRENT @ T <sub>C</sub> = 100°C	I <sub>D</sub>	-	ı	9.0	Amps
OPERATING AND STORAGE TEMPERATURE	$T_{OP}/T_{STG}$	-55	-	+150	°C
TOTAL DEVICE DISSIPATION @ T <sub>C</sub> = 25°C	$P_{D}$	-	-	347	Watts
THERMAL RESISTANCE, JUNCTION TO CASE	$R_{thJC}$	-	-	0.65	°C/W

## **ELECTRICAL CHARACTERISTICS**

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNITS
DRAIN TO SOURCE BREAKDOWN VOLTAGE	$BV_{DSS}$	400			Volts
$V_{GS} = 0V, I_D = 1.0mA$			_	-	
STATIC DRAIN TO SOURCE ON STATE RESISTANCE					Ω
$V_{GS} = 10V, I_{D} = 9.0A$	$R_{DS(ON)}$	-	-	0.3	
$V_{GS} = 10V, I_{D} = 14A$				0.4	
GATE THRESHOLD VOLTAGE $V_{DS} = V_{GS}$ , $I_D = 0.25$ mA	$V_{GS(th)}$	2.0	-	4.0	Volts
ZERO GATE VOLTAGE DRAIN CURRENT					
$V_{DS}$ = Max. Rating, $V_{GS}$ = 0V	$I_{DSS}$	-	-	250	μΑ
$V_{DS} = 0.8$ xMax. Rating, $V_{GS} = 0$ V, $T_{J} = 125$ °C				1000	·
GATE TO SOURCE LEAKAGE FORWARD V <sub>GS</sub> = 20V	$I_{GSS}$	-	-	100	nA
GATE TO SOURCE LEAKAGE REVERSE $V_{GS} = -20V$				-100	
TURN ON DELAY TIME $V_{DD} = 200V$ ,	$t_{d(ON)}$			35	
RISE TIME $I_D = 14A$ ,	t <sub>r</sub>	_	_	190	nsec
TURN OFF DELAY TIME $R_G = 2.2\Omega$	$t_{d(OFF)}$			170	
FALL TIME	t <sub>f</sub>			130	
TOTAL GATE CHARGE $I_D = 14A$ ,	$Q_{g}$	52	-	110	nC
GATE TO SOURCE CHARGE $V_{GS} = 10V$ ,	$Q_{gs}$	5.0	-	18	nC
GATE TO DRAIN CHARGE $V_{DS} = 0.5xMax$ . Rating	$Q_{gd}$	25	-	65	nC
DIODE FORWARD VOLTAGE $T_C = 25^{\circ}C$ , $I_S = 14A$ , $V_{GS} = 0V$	V <sub>SD</sub>	-	-	1.7	Volts
REVERSE RECOVERY CHARGE T <sub>J</sub> = 25°C,	$Q_{RR}$			11	μС
$di/dt = 100A/\mu sec, V_{DD} = 50V$		_	_		·
REVERSE RECOVERY TIME T <sub>J</sub> = 25°C,	t <sub>rr</sub>			1200	
$I_{\rm F} = 14$ Å,		-	_		nsec
$di/dt = 100A/\mu sec, V_{DD} = 50V$					
INPUT CAPACITANCE V <sub>GS</sub> = 0 V	C <sub>iss</sub>		2600		
OUTPUT CAPACITANCE $V_{DS} = 25 \text{ V}$	$C_{oss}$	-	680	-	pF
REVERSE TRANSFER CAPACITANCE f = 1.0MHz	$C_{rss}$		250		



LCC-3P

### **PINOUT TABLE**

	PIN 1	PIN 2	PIN 3
N CHANNEL MOSFET IN AN LCC-3P PACKAGE	DRAIN	SOURCE	GATE

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