

# FUJI POWER MOSFET Super FAP-G Series

## ■ Features

- |                        |                   |
|------------------------|-------------------|
| High speed switching   | Low on-resistance |
| No secondary breakdown | Low driving power |
| Avalanche-proof        |                   |

## ■ Applications

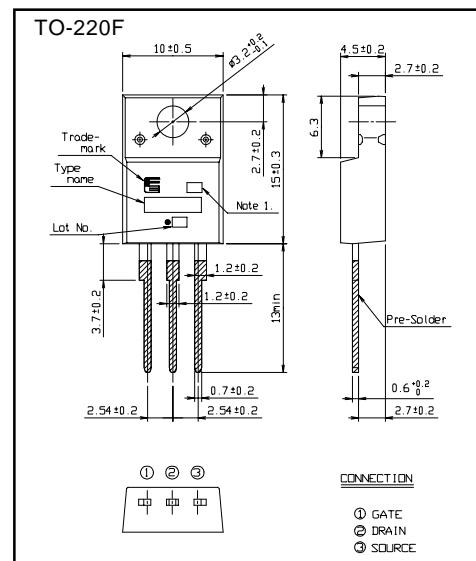
- |                                    |                  |
|------------------------------------|------------------|
| Switching regulators               | DC-DC converters |
| UPS (Uninterruptible Power Supply) |                  |

## ■ Maximum ratings and characteristic

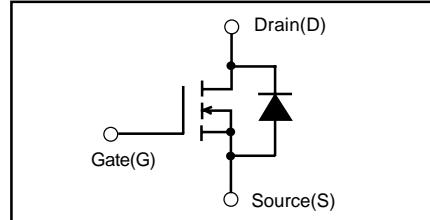
- Absolute maximum ratings  
( $T_c=25^\circ\text{C}$  unless otherwise specified)

Item	Symbol	Ratings	Unit	Remarks
Drain-source voltage	$V_{DS}$	100	V	
	$V_{DSX}$	70	V	$V_{GS}=-30\text{V}$
Continuous Drain Current	$I_D$	29	A	
Pulsed Drain Current	$I_{D(\text{puls})}$	$\pm 116$	A	
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V	
Maximum Avalanche current	$I_{AR}$	29	A	Note *1
Non-Repetitive	$E_{AS}$	376.4	mJ	Note *2
Maximum Avalanche Energy				
Repetitive	$E_{AR}$	3.7	mJ	Note *3
Maximum Avalanche Energy				
Maximum Drain-Source dV/dt	$dV_{DS}/dt$	20	kV/ $\mu\text{s}$	$V_{DS}\leq 100\text{V}$
Peak Diode Recovery dV/dt	$dV/dt$	5	kV/ $\mu\text{s}$	Note *4
Max. Power Dissipation	$P_D$	37	W	$T_c=25^\circ\text{C}$
		2.16		$T_a=25^\circ\text{C}$
Operating and Storage	$T_{ch}$	+150	$^\circ\text{C}$	
Temperature range	$T_{stg}$	-55 to +150	$^\circ\text{C}$	
Isolation Voltage	$V_{ISO}$	2	kVRms	$t=60\text{sec. } f=60\text{Hz}$

## ■ Outline Drawings (mm) 200407



## ■ Equivalent circuit schematic



Note \*1:  $T_{ch}\leq 150^\circ\text{C}$ , Repetitive and Non-repetitive

Note \*2: Starting  $T_{ch}=25^\circ\text{C}$ ,  $I_{AS}=12\text{A}$ ,  $L=3.14\text{mH}$ ,  $V_{CC}=48\text{V}$ ,  $R_G=50\Omega$

$E_{AS}$  limited by maximum channel temperature and Avalanche current.

See to the 'Avalanche Energy' graph

Note \*3: Repetitive rating: Pulse width limited by maximum channel temperature.

See to the 'Transient Thermal impedance' graph.

Note \*4:  $I_F \leq -I_D$ ,  $-di/dt = 50\text{A}/\mu\text{s}$ ,  $V_{CC}\leq BV_{DSS}$ ,  $T_{ch}\leq 150^\circ\text{C}$

## ■ Electrical characteristics ( $T_c=25^\circ\text{C}$ unless otherwise specified)

Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain-Source Breakdown Voltage	$BV_{DSS}$	$I_D=250\mu\text{A}$ $V_{GS}=0\text{V}$	100			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$I_D=250\mu\text{A}$ $V_{DS}=V_{GS}$		3.0	5.0	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=100\text{V}$ $V_{GS}=0\text{V}$ $V_{DS}=80\text{V}$ $V_{GS}=0\text{V}$			25	$\mu\text{A}$
Gate-Source Leakage Current	$I_{GS}$	$V_{GS}=\pm 30\text{V}$ $V_{DS}=0\text{V}$			250	$\mu\text{A}$
Drain-Source On-State Resistance	$R_{DS(on)}$	$I_D=14.5\text{A}$ $V_{GS}=10\text{V}$		45	59	$\text{m}\Omega$
Forward Transconductance	$g_{fs}$	$I_D=14.5\text{A}$ $V_{DS}=25\text{V}$	6	12		S
Input Capacitance	$C_{iss}$	$V_{DS}=75\text{V}$		740	1100	$\text{pF}$
Output Capacitance	$C_{oss}$	$V_{GS}=0\text{V}$		200	300	
Reverse Transfer Capacitance	$C_{rss}$	$f=1\text{MHz}$		15	22	
Turn-On Time $t_{on}$	$t_{d(on)}$	$V_{CC}=48\text{V}$		13	19	ns
	$t_r$	$I_D=14.5\text{A}$		6	9	
Turn-Off Time $t_{off}$	$t_{d(off)}$	$V_{GS}=10\text{V}$		20	30	
	$t_f$	$R_{GS}=10\Omega$		8	12	
Total Gate Charge	$Q_G$	$V_{CC}=50\text{V}$		23	35	nC
Gate-Source Charge	$Q_{GS}$	$I_D=29\text{A}$		10	15	
Gate-Drain Charge	$Q_{GD}$	$V_{GS}=10\text{V}$		7	11	
Diode forward on-voltage	$V_{SD}$	$I_F=29\text{A}$ $V_{GS}=0\text{V}$ $T_{ch}=25^\circ\text{C}$		1.00	1.50	V
Reverse recovery time	$t_{rr}$	$I_F=29\text{A}$ $V_{GS}=0\text{V}$		110		ns
Reverse recovery charge	$Q_{rr}$	$-di/dt=100\text{A}/\mu\text{s}$ $T_{ch}=25^\circ\text{C}$		0.5		$\mu\text{C}$

## ■ Thermal characteristics

Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal resistance	$R_{th(ch-c)}$	channel to case			3.378	$^\circ\text{C/W}$
	$R_{th(ch-a)}$	channel to ambient			58	$^\circ\text{C/W}$

## Characteristics

