2SK2691-01R   FUJI POWER MOSFET

FUJI ELECTRIC

N-CHANNEL SILICON POWER MOSFET

FAP-IIIB SERIES

■ Features
- **High speed switching**
- **Low on-resistance**
- **No secondary breakdown**
- **Low driving power**
- **High voltage**
- **Avalanche-proof**

■ Applications
- **Switching regulators**
- **DC-DC converters**
- **General purpose power amplifier**

■ Outline Drawings

TO-3PF

Note 1. Guaranteed mark of avalanche ruggedness.

CONNECTION
① GATE
② DRAIN
③ SOURCE

■ Equivalent circuit schematic

Drain(D)

Gate(G)

Source(S)

■ Maximum ratings and characteristics

● Absolute maximum ratings (Tc=25°C unless otherwise specified)

Item	Symbol	Rating	Unit	Remarks
Drain-source voltage	V_{DS}	60	V	
Continuous drain current	I_D	±70	A	
Pulsed drain current	$I_{D[puls]}$	±280	A	
Gate-source peak voltage	V_{GS}	±20	V	
Maximum avalanche energy	E_{AV}	685	mJ	*1
Maximum power dissipation	P_D	100	W	
Operating and storage temperature range	T_{ch}	+150	°C	
	T_{stg}	-55 to +150	°C	

*1 L=0.186mH, Vcc=24V

● Electrical characteristics (Tc =25°C unless otherwise specified)

Item	Symbol	Test Conditions		Min.	Typ.	Max.	Units
Drain-source breakdown voltage	$V_{(BR)DSS}$	I_D=1mA V_{GS}=0V		60			V
Gate threshold voltage	$V_{GS(th)}$	I_D=1mA V_{DS}=V_{GS}		1.0	1.5	2.0	V
Zero gate voltage drain current	I_{DSS}	V_{DS}=60V V_{GS}=0V	T_{ch}=25°C		10	500	μA
			T_{ch}=125°C		0.2	1.0	mA
Gate-source leakage current	I_{GSS}	V_{GS}=20V V_{DS}=0V			10	100	nA
Drain-source on-state resistance	$R_{DS(on)}$	I_D=40A V_{GS}=10V	V_{GS}=4V		12	17	mΩ
			V_{GS}=10V		7.5	10	mΩ
Forward transconductance	g_{fs}	I_D=40A V_{DS}=25V		25	55		S
Input capacitance	C_{iss}	V_{DS}=25V			3500	5250	
Output capacitance	C_{oss}	V_{GS}=0V			1250	1870	pF
Reverse transfer capacitance	C_{rss}	f=1MHz			360	540	
Turn-on time	$t_{d(on)}$	V_{CC}=30V R_G=10 Ω			15	23	
	t_r	I_D=75A			75	120	
Turn-off time	$t_{d(off)}$	V_{GS}=10V			190	285	ns
	t_f				110	165	
Avalanche capability	I_{AV}	L=100μH T_{ch}=25°C		70			A
Diode forward on-voltage	V_{SD}	I_F=160A V_{GS}=0V T_{ch}=25°C			1.15	1.65	V
Reverse recovery time	t_{rr}	I_F=80A			75	120	ns
Reverse recovery charge	Q_{rr}	-di/dt=100A/μs T_{ch}=25°C			0.17		μC

● Thermal characteristics

Item	Symbol	Min.	Typ.	Max.	Units
Thermal resistance	$R_{th(ch-c)}$			1.25	°C/W
	$R_{th(ch-a)}$			30.0	°C/W

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■ Characteristics

Power Dissipation
PD=f(Tc)
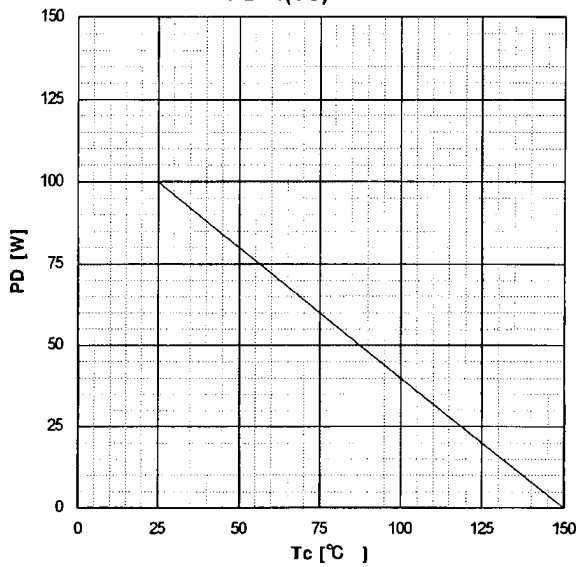
Safe operating area
ID=f(VDS):D=0.01,Tc=25℃
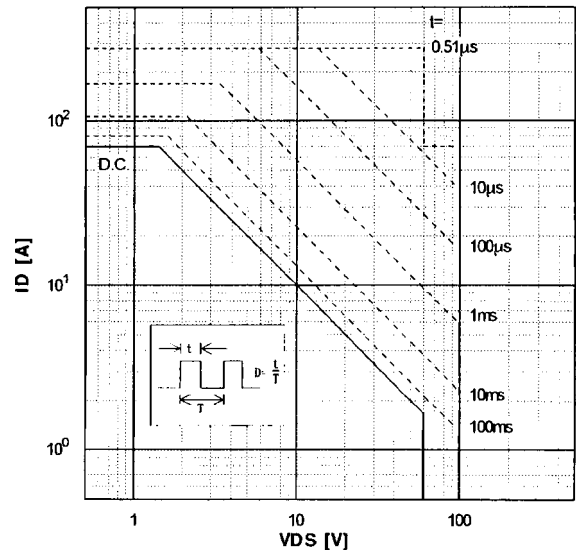
Typical Output Characteristics
ID=f(VDS):80μs pulse test,Tch=25℃
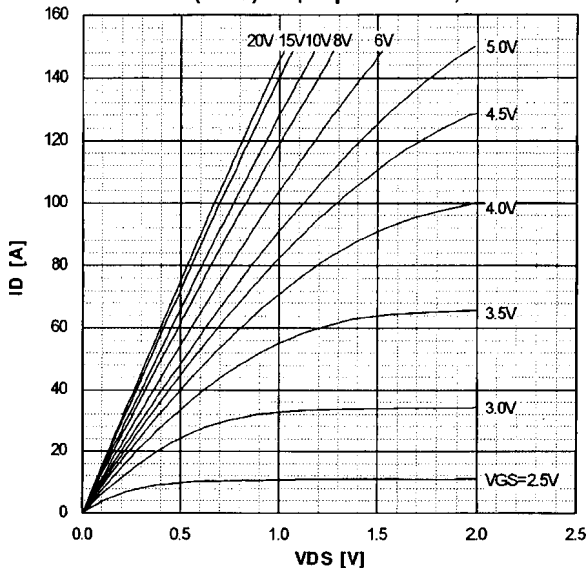
Typical Transfer Characteristic
ID=f(VGS):80μs pulse test,VDS=25V,Tch=25℃
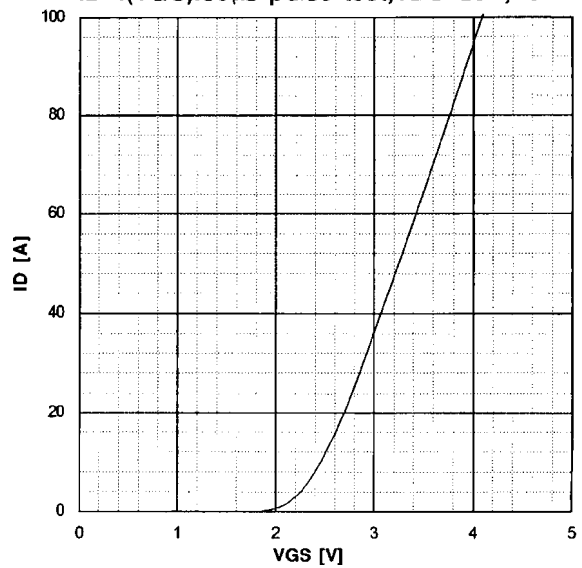
Typical Transconductance
gfs=f(ID):80 μs pulse test,VDS=25V,Tch=25℃
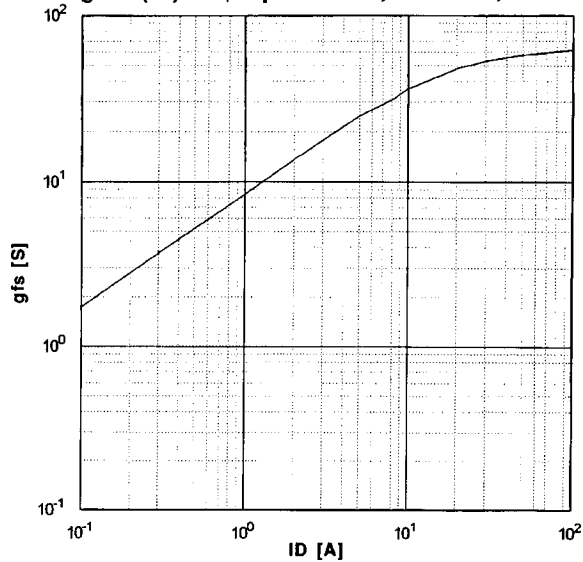
Typical Drain-Source on-State Resistance
RDS(on)=f(ID):80 μs pulse test,Tch=25℃

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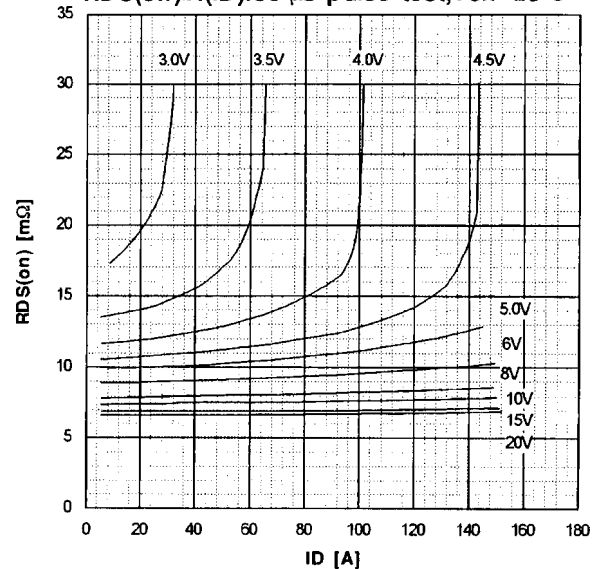
Drain-Source On-state Resistance
RDS(on)=f(Tch):ID=40A,VGS=10V
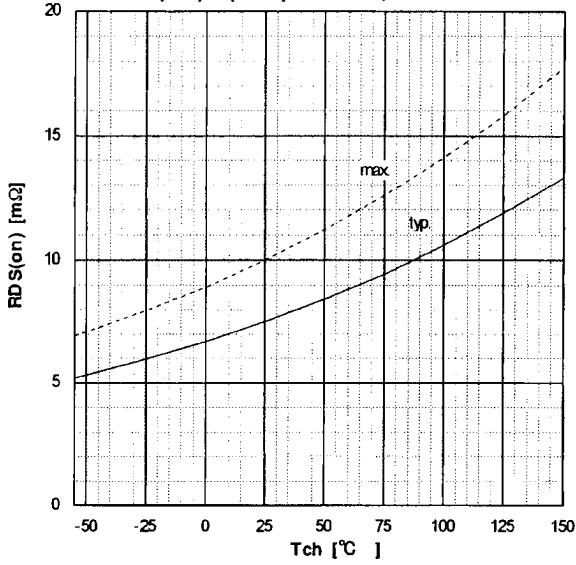
Gate Threshold Voltage vs. Tch
VGS(th)=f(Tch):VDS=VGS,ID=1mA
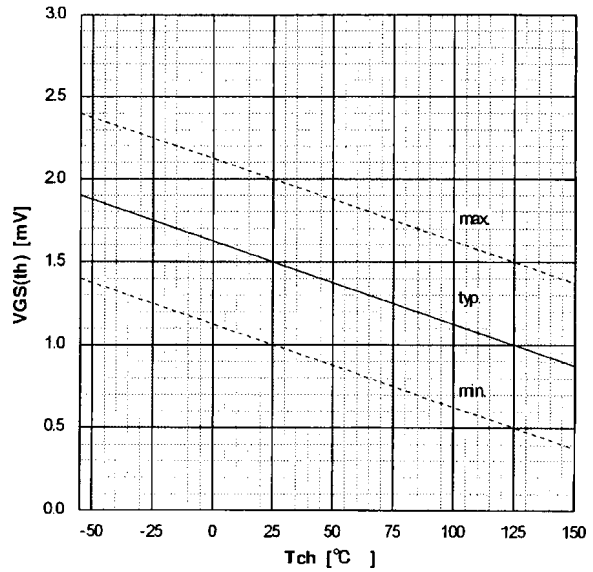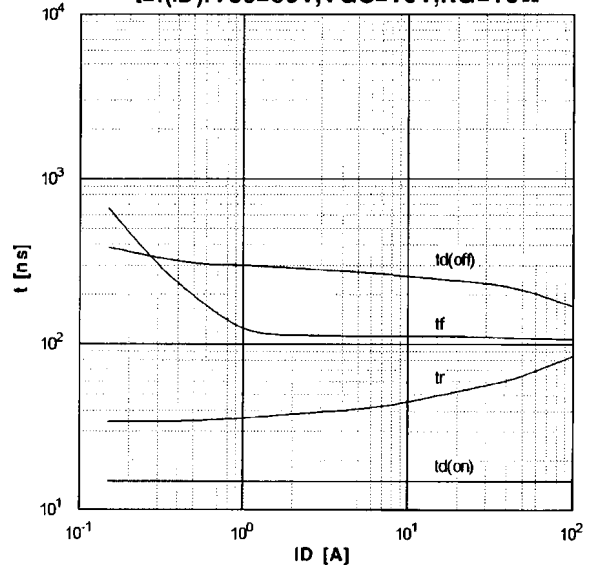
Typical Capacitance
C=f(VDS):VGS=0V,f=1MHz
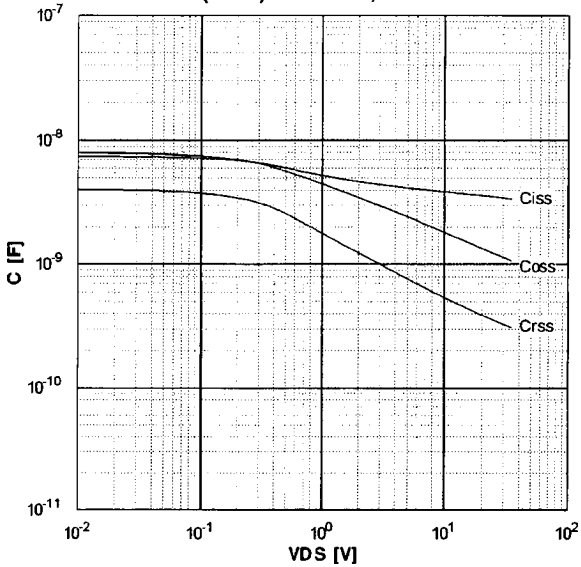
Typical Gate Charge Characteristics
VGS=f(Qg):ID=80A,Tch=25℃
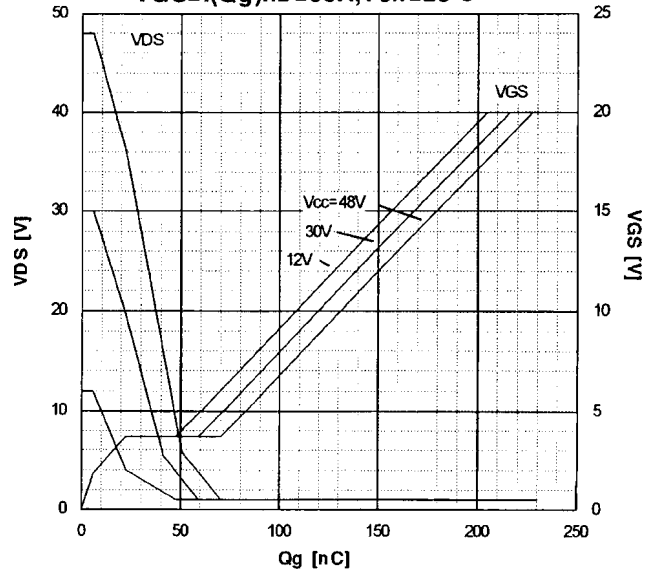
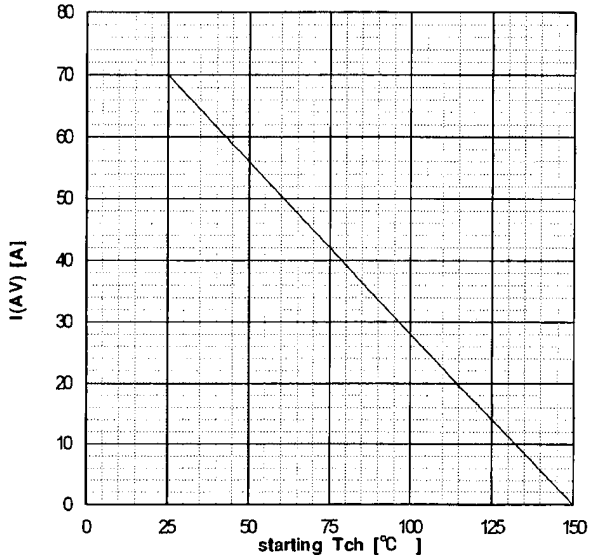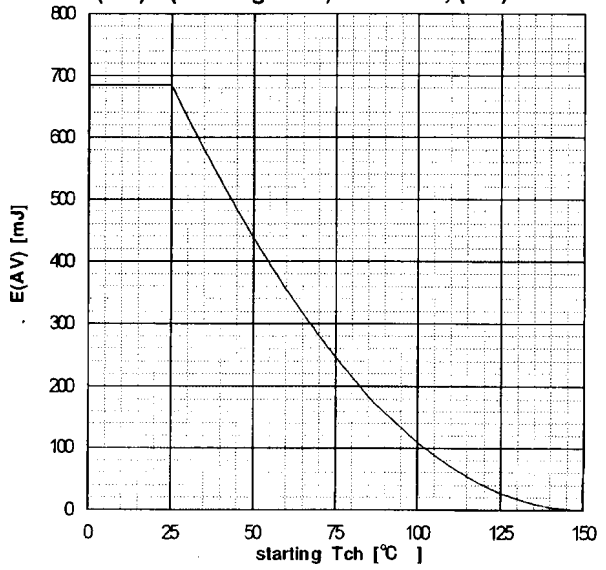### Typical Forward Characteristics of Reverse Diode
IF=f(VSD):80μs pulse test,Tch=25℃
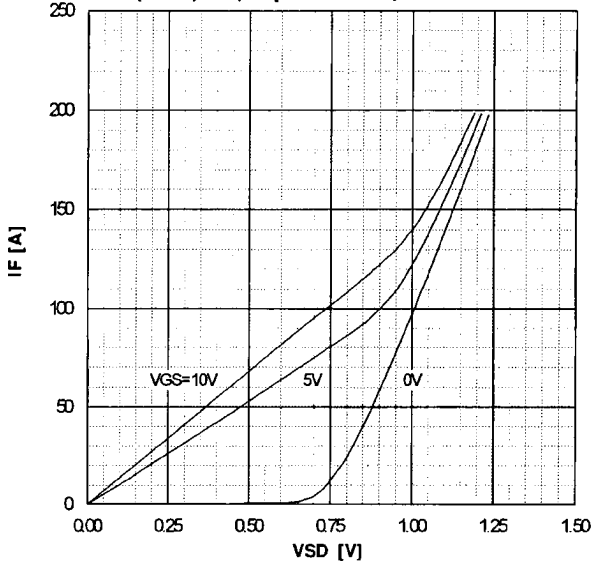
Typical Switching Characteristics vs. ID
t=f(ID):Vcc=30V,VGS=10V,RG=10Ω

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Maximum Avalanche Current vs. starting Tch
I(AV)=f(starting Tch)

Maximum Avalanche Energy vs. starting Tch
E(AV)=f(starting Tch):Vcc=24V,I(AV)≦ 70A

Transient Thermal Impedance
Zth(ch-c)=f(t) parameter:D=t/T

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