



SANYO Semiconductors

# DATA SHEET

An ON Semiconductor Company

## 2SK4197LS — N-Channel Silicon MOSFET General-Purpose Switching Device Applications

### Features

- Low ON-resistance, low input capacitance, ultrahigh-speed switching.
- Adoption of high reliability HVP process.
- Attachment workability is good by Mica-less package.
- Avalanche resistance guarantee.

### Specifications

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		600	V
Gate-to-Source Voltage	V <sub>GSS</sub>		±30	V
Drain Current (DC)	I <sub>DC</sub> *1	Limited only by maximum temperature T <sub>ch</sub> =150°C	3.5	A
	I <sub>Dpack</sub> *2	T <sub>c</sub> =25°C (SANYO's ideal heat dissipation condition)*3	3.3	A
Drain Current (Pulse)	I <sub>DP</sub>	PW≤10μs, duty cycle≤1%	13	A
Allowable Power Dissipation	P <sub>D</sub>		2.0	W
		T <sub>c</sub> =25°C (SANYO's ideal heat dissipation condition)*3	28	W
Channel Temperature	T <sub>ch</sub>		150	°C
Storage Temperature	T <sub>stg</sub>		-55 to +150	°C
Avalanche Energy (Single Pulse) *4	E <sub>AS</sub>		36.6	mJ
Avalanche Current *5	I <sub>AV</sub>		3.5	A

\*1 Shows chip capability

\*2 Package limited

\*3 SANYO's condition is radiation from backside.

The method is applying silicone grease to the backside of the device and attaching the device to water-cooled radiator made of aluminium.

\*4 V<sub>DD</sub>=99V, L=5mH, I<sub>AV</sub>=3.5A

\*5 L≤5mH, single pulse

Marking : K4197

■ Any and all SANYO Semiconductor Co.,Ltd. products described or contained herein are, with regard to "standard application", intended for the use as general electronics equipment (home appliances, AV equipment, communication device, office equipment, industrial equipment etc.). The products mentioned herein shall not be intended for use for any "special application" (medical equipment whose purpose is to sustain life, aerospace instrument, nuclear control device, burning appliances, transportation machine, traffic signal system, safety equipment etc.) that shall require extremely high level of reliability and can directly threaten human lives in case of failure or malfunction of the product or may cause harm to human bodies, nor shall they grant any guarantee thereof. If you should intend to use our products for applications outside the standard applications of our customer who is considering such use and/or outside the scope of our intended standard applications, please consult with us prior to the intended use. If there is no consultation or inquiry before the intended use, our customer shall be solely responsible for the use.

■ Specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

**SANYO Semiconductor Co., Ltd.**

TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

# 2SK4197LS

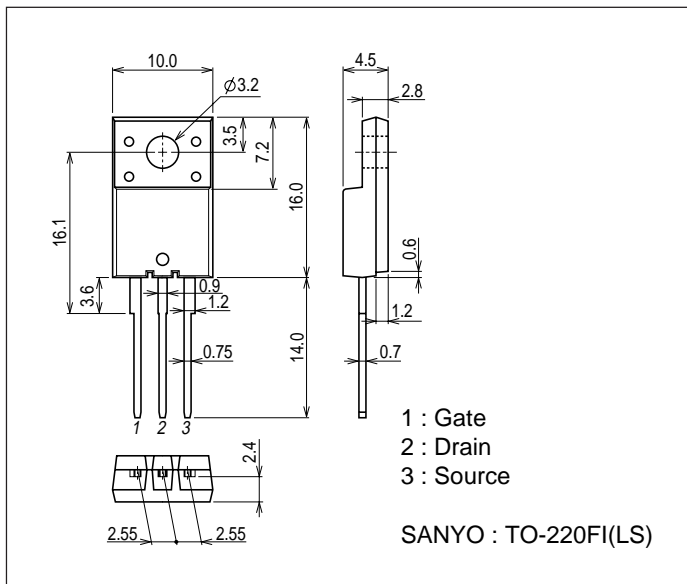
## Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=10mA, V_{GS}=0V$	600			V
Zero-Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=480V, V_{GS}=0V$			100	$\mu A$
Gate-to-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 30V, V_{DS}=0V$			$\pm 100$	nA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=10V, I_D=1mA$	3		5	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=10V, I_D=1.8A$	0.8	1.6		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)}$	$I_D=1.8A, V_{GS}=10V$		2.5	3.25	$\Omega$
Input Capacitance	$C_{iss}$	$V_{DS}=30V, f=1MHz$		260		pF
Output Capacitance	$C_{oss}$	$V_{DS}=30V, f=1MHz$		50		pF
Reverse Transfer Capacitance	$C_{rss}$	$V_{DS}=30V, f=1MHz$		9.7		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit.		12		ns
Rise Time	$t_r$	See specified Test Circuit.		20		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit.		28		ns
Fall Time	$t_f$	See specified Test Circuit.		12		ns
Total Gate Charge	$Q_g$	$V_{DS}=200V, V_{GS}=10V, I_D=3.5A$		11		nC
Gate-to-Source Charge	$Q_{gs}$	$V_{DS}=200V, V_{GS}=10V, I_D=3.5A$		2.6		nC
Gate-to-Drain "Miller" Charge	$Q_{gd}$	$V_{DS}=200V, V_{GS}=10V, I_D=3.5A$		5.8		nC
Diode Forward Voltage	$V_{SD}$	$I_S=3.5A, V_{GS}=0V$		0.9	1.2	V

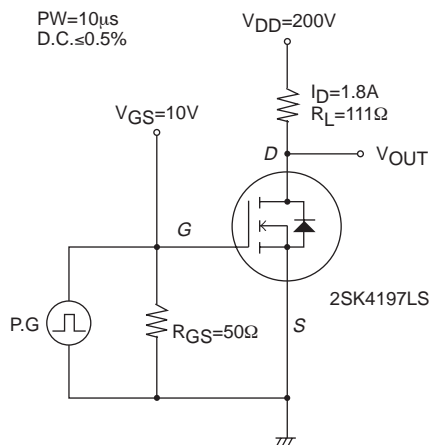
## Package Dimensions

unit : mm (typ)

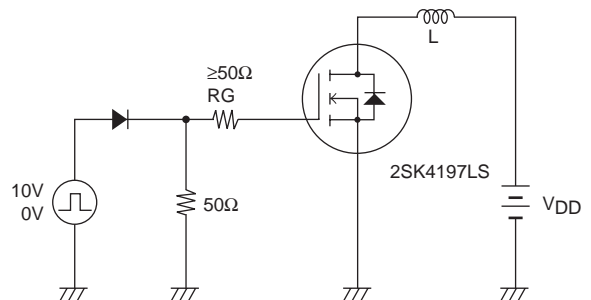
7509-002



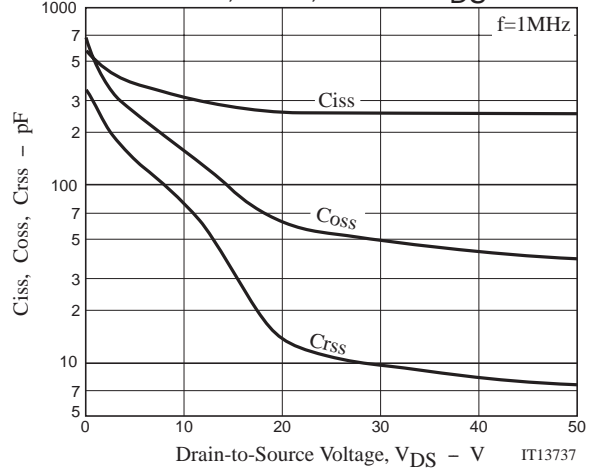
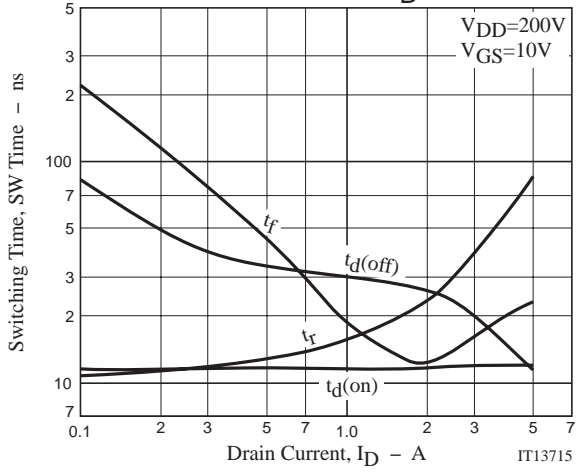
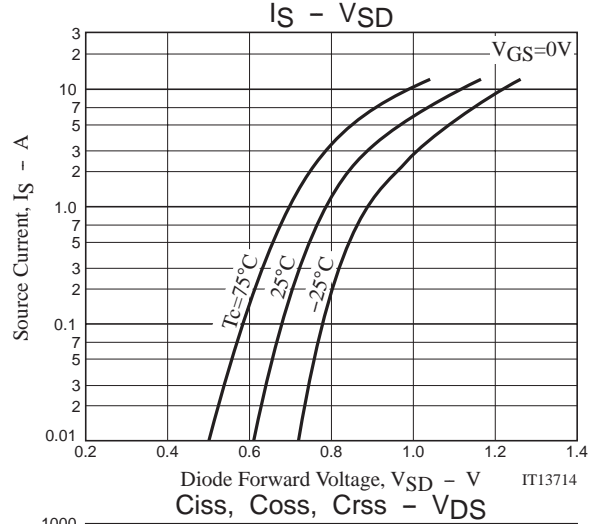
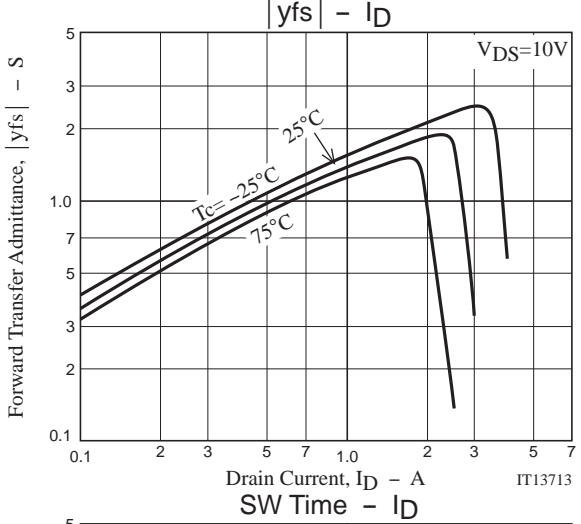
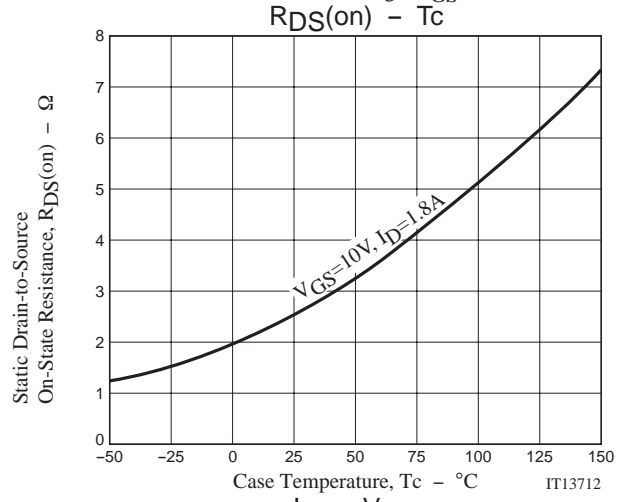
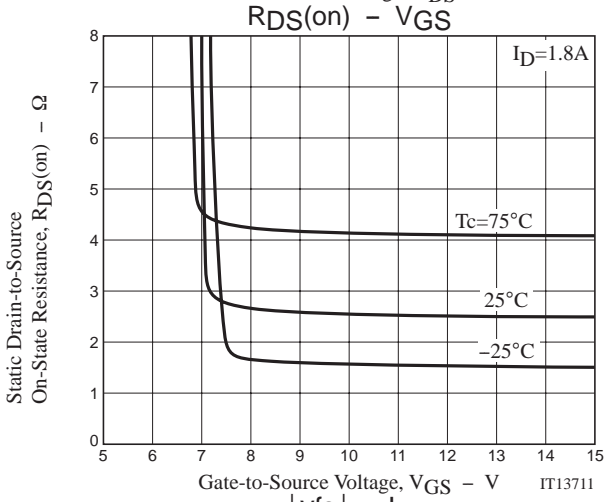
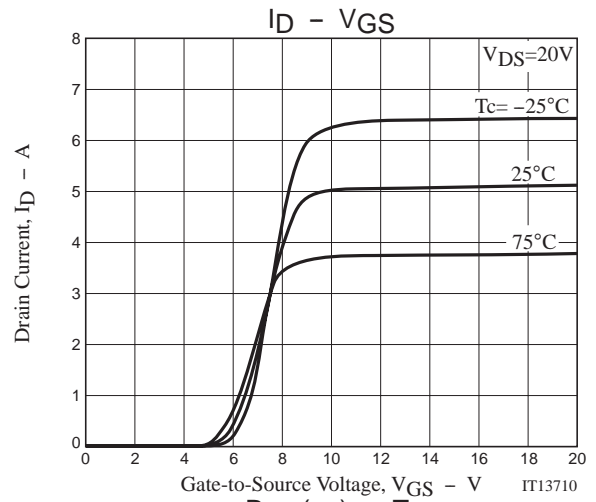
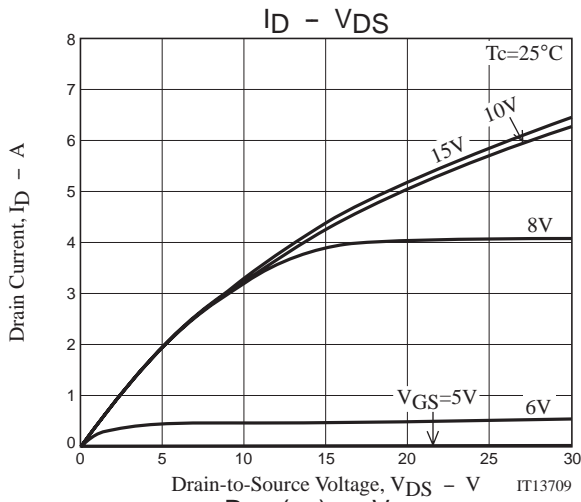
## Switching Time Test Circuit



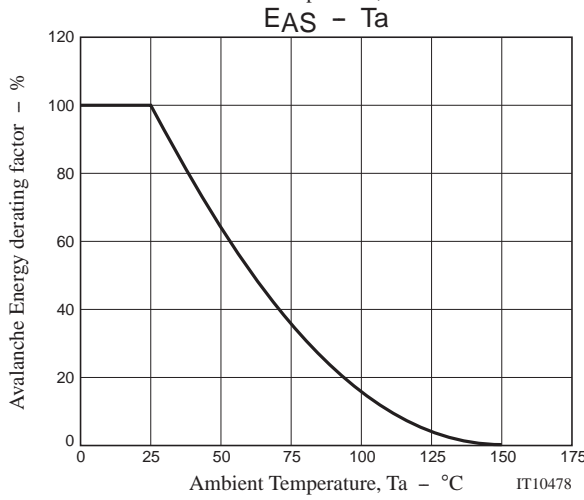
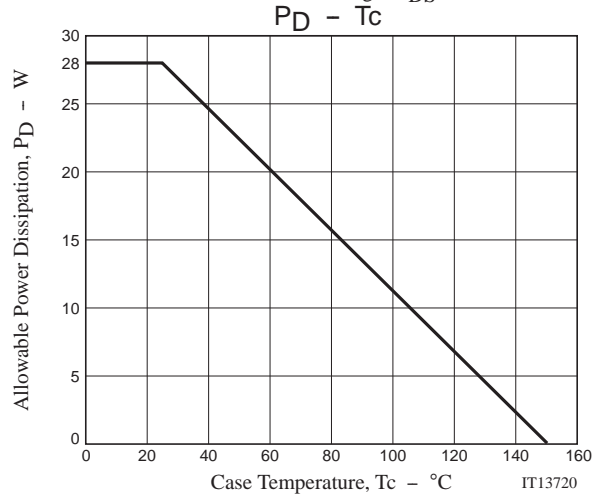
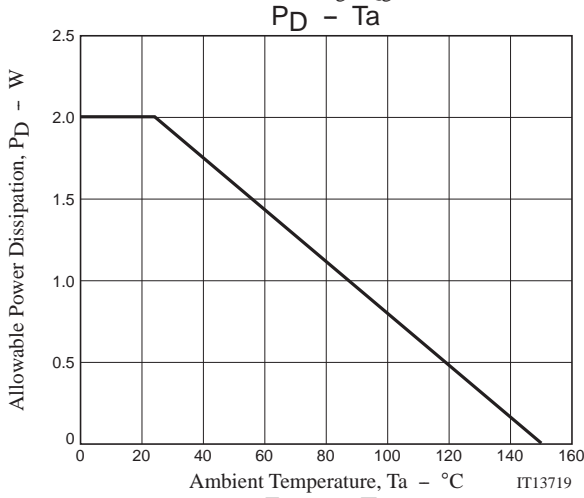
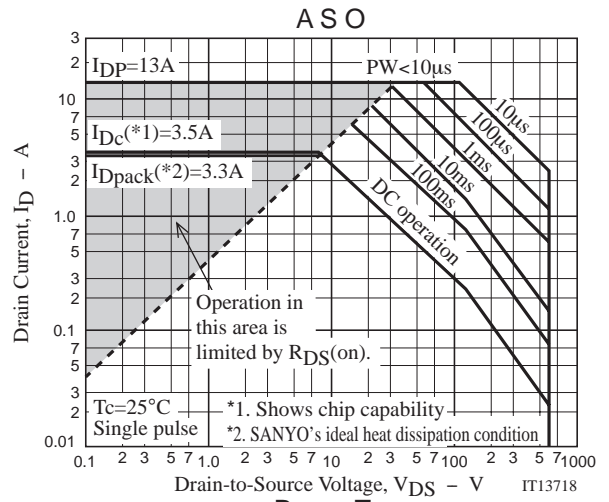
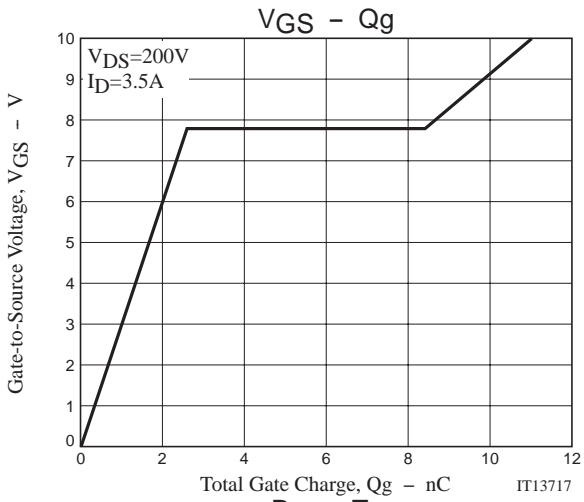
## Avalanche Resistance Test Circuit



# 2SK4197LS



# 2SK4197LS



Note on usage : Since the 2SK4197LS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

- SANYO Semiconductor Co.,Ltd. assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein.
- SANYO Semiconductor Co.,Ltd. strives to supply high-quality high-reliability products, however, any and all semiconductor products fail or malfunction with some probability. It is possible that these probabilistic failures or malfunction could give rise to accidents or events that could endanger human lives, trouble that could give rise to smoke or fire, or accidents that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all SANYO Semiconductor Co.,Ltd. products described or contained herein are controlled under any of applicable local export control laws and regulations, such products may require the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written consent of SANYO Semiconductor Co.,Ltd.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO Semiconductor Co.,Ltd. product that you intend to use.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production.
- Upon using the technical information or products described herein, neither warranty nor license shall be granted with regard to intellectual property rights or any other rights of SANYO Semiconductor Co.,Ltd. or any third party. SANYO Semiconductor Co.,Ltd. shall not be liable for any claim or suits with regard to a third party's intellectual property rights which has resulted from the use of the technical information and products mentioned above.

This catalog provides information as of June, 2008. Specifications and information herein are subject to change without notice.