

RoHS

COMPLIANT HALOGEN

FREE

# P-Channel 8 V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)			
- 8	0.122 at V <sub>GS</sub> = - 4.5 V	1.2				
	0.141 at V <sub>GS</sub> = - 2.5 V	1.1	5.91			
	0.168 at V <sub>GS</sub> = - 1.8 V	0.60	5.91			
	0.198 at V <sub>GS</sub> = - 1.5 V	0.50				

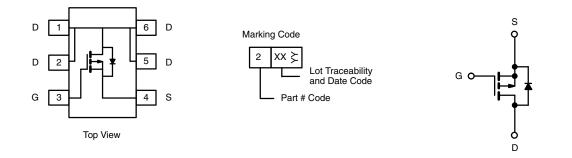
SC-89 (6-LEADS)

## FEATURES

- Halogen-free According to IEC 61249-2-21
  Definition
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC

#### APPLICATIONS

Load Switch for Portable Applications



Ordering Information: Si1051X-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_A = 25 \degree C$ , unless otherwise noted)						
Parameter		Symbol	Limit	Unit		
Drain-Source Voltage		V <sub>DS</sub>	- 8	v		
Gate-Source Voltage		V <sub>GS</sub>	± 5			
	T <sub>A</sub> = 25 °C	I_	1.2 <sup>b, c</sup>			
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		0.97 <sup>b, c</sup>	А		
Pulsed Drain Current		I <sub>DM</sub>	- 8			
Continuous Source-Drain Diode Current $T_A = 25 \text{ °C}$		۱ <sub>S</sub>	0.2 <sup>b, c</sup>	A		
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	0.236 <sup>b, c</sup>	W		
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C	'D	0.151 <sup>b, c</sup>			
Operating Junction and Storage Temperature Ra	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C			

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum lumetion to Archienth d	t ≤ 5 s	R <sub>thJA</sub>	440	530	°C/W	
Maximum Junction-to-Ambient <sup>b, d</sup>	Steady State	<sup>n</sup> thJA	540	650	0/11	

Notes:

a. Based on  $T_A = 25 \ ^{\circ}C$ .

b. Surface mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under steady state conditions is 650 °C/W.

# Si1051X

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Parameter	Symbol Test Conditions			Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_{D} = -250 \mu A$	- 8			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = - 250 μA		- 6.19		m)//ºC	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	iD = - 250 μA		2.13		mV/°C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 0.3		- 1	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 5 V$			± 100	nA	
Zana Cata Valta da Duzin Ourrent	I <sub>DSS</sub>	$V_{DS} = -8 V, V_{GS} = 0 V$			- 1	nA	
Zero Gate Voltage Drain Current		$V_{DS}$ = - 8 V, $V_{GS}$ = 0 V, $T_{J}$ = 85 °C			- 10	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS}$ = $\geq$ 5 V, $V_{GS}$ = - 4.5 V	- 8			А	
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 1.2 A		0.091	0.122	1	
	P	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 1.1A	0.106 0.141		0.141	1	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS}$ = - 1.8 V, I <sub>D</sub> = - 0.60 A		0.117	0.168	Ω	
		V <sub>GS</sub> = - 1.5 V, I <sub>D</sub> = - 0.50 A		0.129	0.198		
Forward Transconductance	9 <sub>fs</sub>	V <sub>DS</sub> = - 4 V, I <sub>D</sub> = - 1.2 A		4.93		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			560			
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = - 4 V, V <sub>GS</sub> = 0 V, f = 1 MHz		180		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			112			
Tabal Qada Qhaana	Q <sub>g</sub>	$V_{DS}$ = - 4 V, $V_{GS}$ = - 5 V, $I_D$ = - 1.2 A		6.3	9.45		
Total Gate Charge				5.91	8.87		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = - 4 V, $V_{GS}$ = - 4.5 V, $I_{D}$ = - 1.2 A		1.98		nC	
Gate-Drain Charge	Q <sub>gd</sub>			1.25			
Gate Resistance	R <sub>g</sub>	f = 1 MHz		9.8	14.7	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			7.2	10.8		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 4 V, $R_L$ = 4.16 $\Omega$		36	54		
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D\cong$ - 0.96 A, $V_{GEN}$ = - 4.5 V, $R_g$ = 1 $\Omega$		52	78	ns	
Fall Time	t <sub>f</sub>			16	24		
Drain-Source Body Diode Characteris	tics						
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				- 8	А	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 1.0 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			18.8	28.2	nC	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = - 1.0 A, dl/dt = 100 A/μs		4.7	7.05		
Reverse Recovery Fall Time	ta			15		ns	
Reverse Recovery Rise Time	t <sub>b</sub>			3.8		1	

Notes:

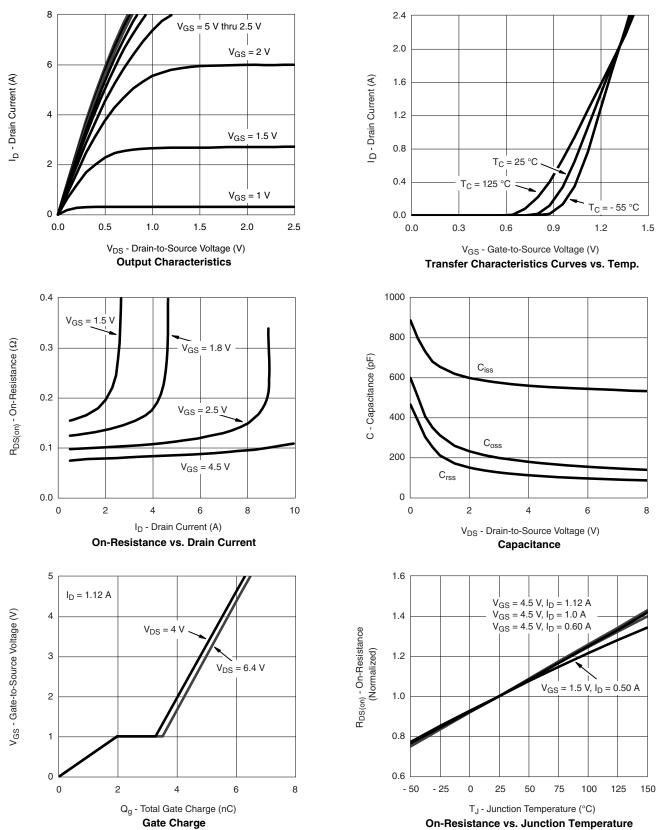
a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



## **TYPICAL CHARACTERISTICS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)

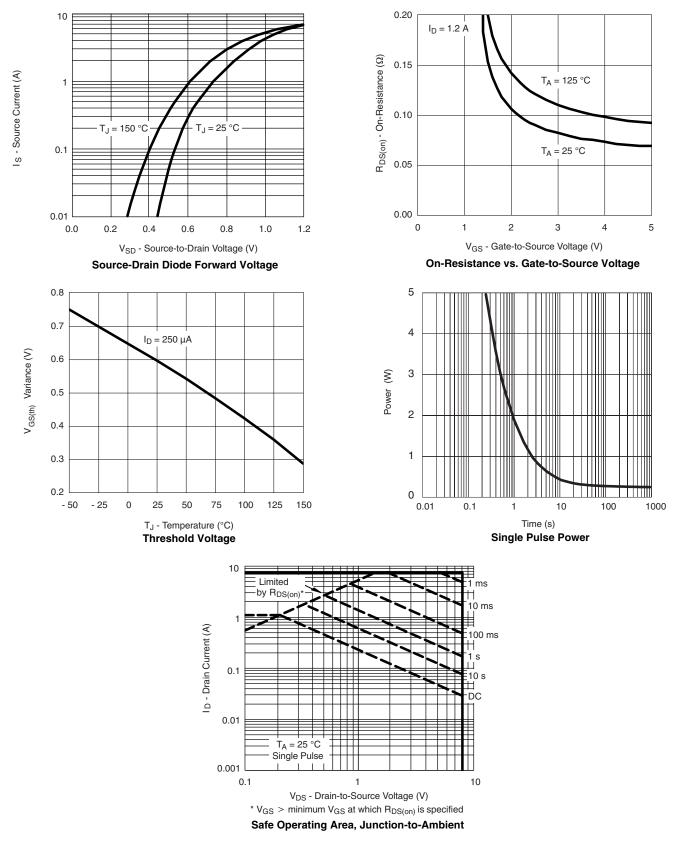


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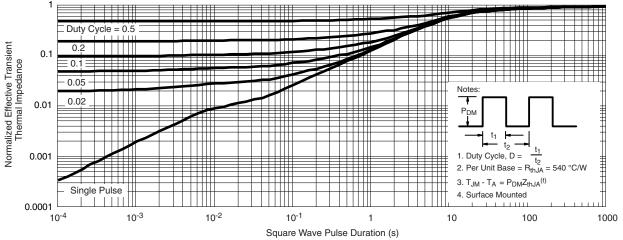


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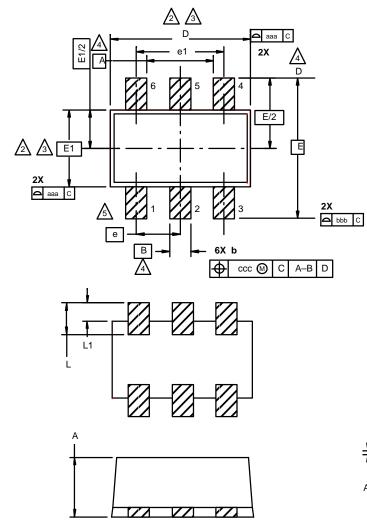


Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?74479">www.vishay.com/ppg?74479</a>.



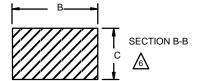
### SC89: 6- LEADS (SOT-563F)



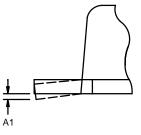
NOTES:

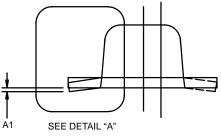
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- 1. Dimensions in millimeters.
- Dimension D does not include mold flash, protrusions or gate burrs. Mold flush, protrusions or gate burrs shall not exceed 0.15 mm per dimension E1 does not include interlead flash or protrusion, interlead flash or protrusion shall not exceed 0.15 mm per side.
- Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, the bar burrs, gate burrs and interlead flash, but including any mismatch between the top and the bottom of the plastic body.
- A Datums A, B and D to be determined 0.10 mm from the lead tip.
  - Terminal numbers are shown for reference only.
  - These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.









	MILLIMETERS				Tolerances Of Form And	
Dim	Min	Max	Note	Symbol	Position	
Α	0.56	0.60		aaa	0.10	
A1	0.00	0.10		bbb	0.10	
b	0.15	0.30		CCC	0.10	
С	0.10	0.18				
D	1.50	1.70	2, 3			
E	1.55	1.70				
E1	1.20 BSC		2, 3			
е	0.50 BSC					
e1	1.00 BSC					
L	0.35 BSC					
L1	0.20 BSC					
ECN: E-00499—Rev. B, 02-Jul-01 DWG: 5880						



# Application Note 826

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## **RECOMMENDED MINIMUM PADS FOR SC-89: 6-Lead**



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



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