

20V N-Channel Enhancement Mode MOSFET

DESCRIPTION

The SMC8810 is the Dual N-Channel logic enhancement mode power field effect transistor which is produced using high cell density, advanced trench technology to provide excellent $R_{DS(ON)}$.

This high density process is especially tailored to minimize on-state resistance. These devices are particularly suited for low voltage application, and low in-line power loss are needed in small outline surface mount package. It is ESD protected.

SMC8810W-TRG ROHS Compliant This is Halogen Free

FEATURE

- ◆ 20V/7.0A, $R_{DS(ON)} = 11.5m\Omega (typ.) @ V_{GS} = 4.5V$
- ◆ 20V/7.0A, $R_{DS(ON)} = 12.0m\Omega (typ.) @ V_{GS} = 4.0V$
- ◆ 20V/6.5A, $R_{DS(ON)} = 12.5m\Omega (typ.) @ V_{GS} = 3.2V$
- ◆ 20V/6.5A, $R_{DS(ON)} = 14m\Omega (typ.) @ V_{GS} = 2.5V$
- ◆ ESD protection 2KV
- ◆ Super high density cell design for extremely low $R_{DS(ON)}$
- ◆ Exceptional on-resistance and Maximum DC current capability

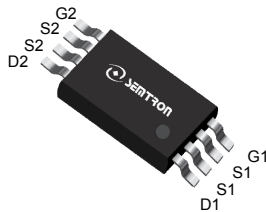
APPLICATIONS

- ◆ Load Switch
- ◆ Portable Equipment
- ◆ Battery Powered System

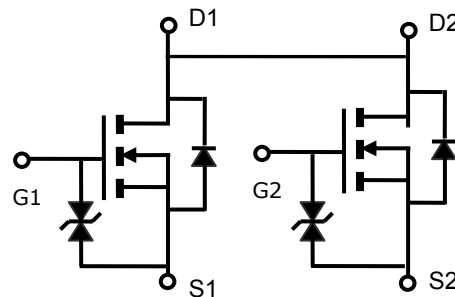


N-Channel Enhancement Mode MOSFET

PIN CONFIGURATION



TSSOP-8
Top View



PART NUMBER INFORMATION

<p>SMC 8810 W - TR G</p> <p>a b c d e</p>	<p>a : Company name. b : Product Serial number. c : Package code d : Handling code e : Green produce code</p>
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ORDERING INFORMATION

Part Number	Package Code	Handling Code	Shipping
SMC8810W-TRG	W : TSSOP-8	TR : Tape&Reel	3K/Reel

- ※ Year Code : 0 ~ 9, 2010 : 0
- ※ Week Code : A(1~2) ~ Z(53~54)
- ※ TSSOP-8 : Only available in tape and reel packaging.

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ Unless otherwise noted)

Symbol	Parameter	Typical	Unit
V_{DSS}	Drain-Source Voltage	20	V
V_{GSS}	Gate-Source Voltage	± 12	V
I_D	Continuous Drain Current ($T_C=25^\circ\text{C}$) ^A	7.0	A
	Continuous Drain Current ($T_C=70^\circ\text{C}$)		
	$V_{GS}=4.5\text{V}$		
I_{DM}	Pulsed Drain Current ^B	30	A
P_D	Power Dissipation	$T_A=25^\circ\text{C}$	1.5
		$T_A=70^\circ\text{C}$	1.0
T_J	Operation Junction Temperature	-55 to 150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.
 Absolute maximum ratings are stress ratings only and functional device operation is not implied.

THERMAL DATA

Symbol	Parameter	Typ	Max	Unit	
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient ^A	Steady-State	-	120	$^\circ\text{C}/\text{W}$
$R_{\theta JL}$	Thermal Resistance Junction to Lead ^A	Steady-State	-	75	$^\circ\text{C}/\text{W}$

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ Unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Parameters						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	20			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	0.6	0.7	1.0	V
I_{GSS}	Gate Leakage Current	$V_{DS}=0V, V_{GS}=\pm 10V$			± 10	μA
I_{DSS}	Zero Gate Voltage, Drain-Source Leakage Current	$V_{DS}=16V, V_{GS}=0V$ $T_J=25^\circ\text{C}$			1	μA
		$V_{DS}=16V, V_{GS}=0V$ $T_J=75^\circ\text{C}$			5	
$R_{DS(ON)}$	Drain-source On-Resistance ^B	$V_{GS}=4.5V, I_D=7.0A$		11.5	13.5	m Ω
		$V_{GS}=4.0V, I_D=7.0A$		12	14	
		$V_{GS}=3.2V, I_D=6.5A$		12.5	14.5	
		$V_{GS}=2.5V, I_D=5.5A$		14	16	
		$V_{GS}=1.8V, I_D=5.0A$		21	24	
G_{fs}	Forward Transconductance	$V_{DS}=5V, I_D=6.5A$		27		S
Source-Drain Diode						
V_{SD}	Diode Forward Voltage	$I_S=1.0A, V_{GS}=0V$		0.85	1.0	V
I_S	Continuous Source Current ^{AD}				6.5	A
Dynamic Parameters						
$Q_g (4.5V)$	Total Gate Charge	$V_{DS}=10V$ $V_{GS}=4.5V$ $I_D=7.0A$		11		nC
Q_{gs}	Gate-Source Charge			4.5		
Q_{gd}	Gate-Drain Charge			3.1		
R_g	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1\text{MHz}$		1.2		Ω
C_{iss}	Input Capacitance	$V_{DS}=10V$ $V_{GS}=0V$ $f=1\text{MHz}$		1185		pF
C_{oss}	Output Capacitance			298		
C_{riss}	Reverse Transfer Capacitance			158		
$t_{d(on)}$	Turn-On Time	$V_{DS}=10V$ $I_D=7A$		6.6		nS
t_r				13		
$t_{d(off)}$	Turn-Off Time	$V_{GEN}=4.5V$ $R_G=3.3\Omega$		54		
t_f				17		

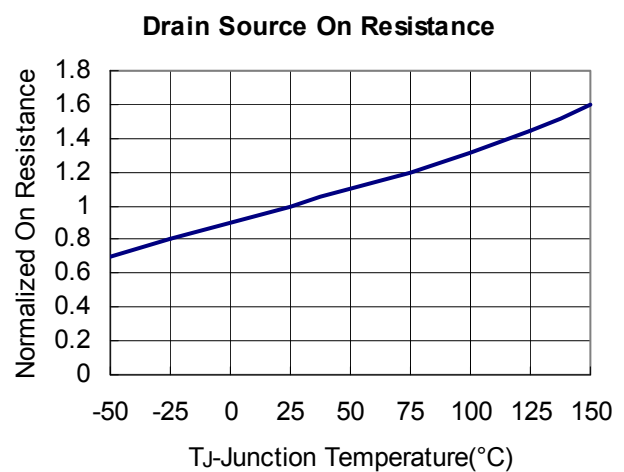
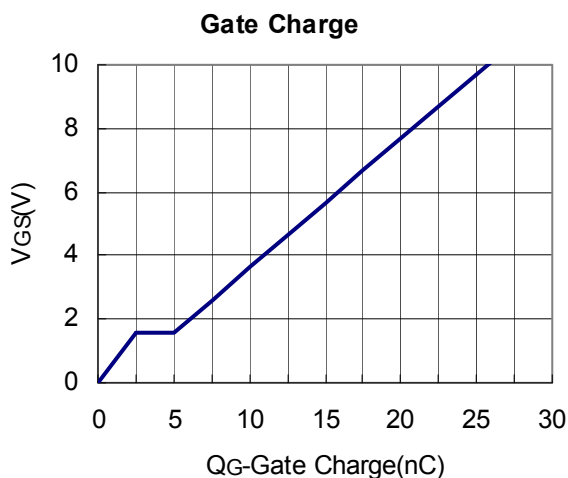
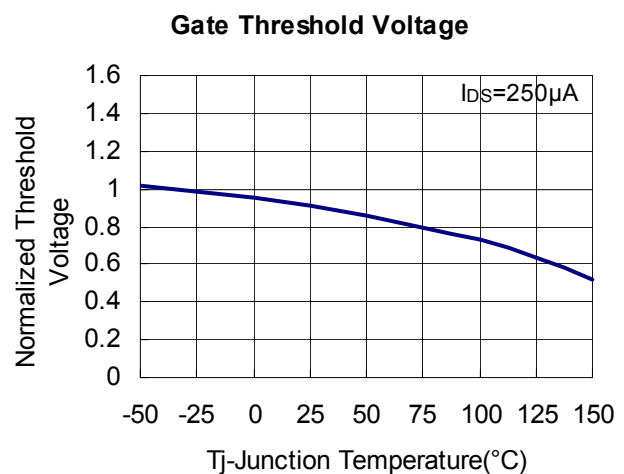
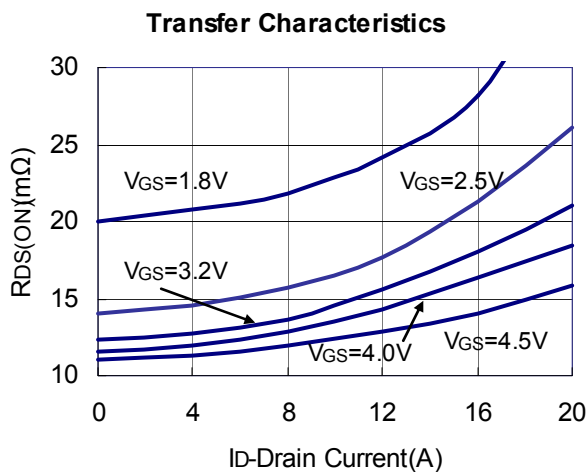
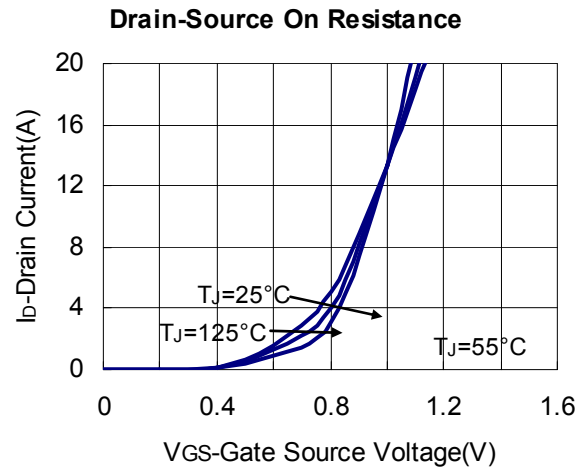
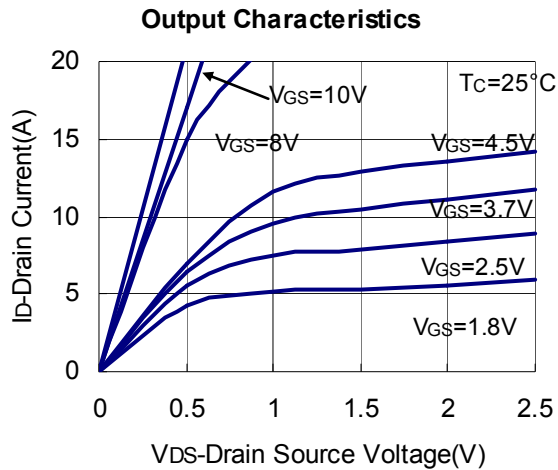
Note:

- The value of $R_{\theta JA}$ is measured with the device mounted on 1in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_C=25^\circ\text{C}$.
- The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- The EAS data shows Max. rating. The test condition is $V_{DD}=-25V, V_{GS}=-10V, L=0.1\text{mH}$.
- The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

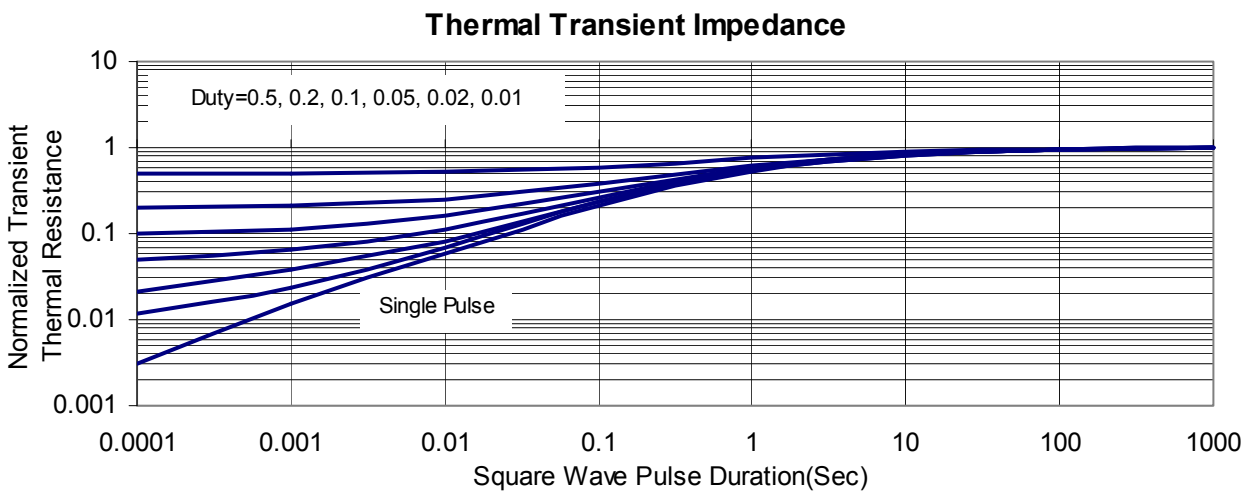
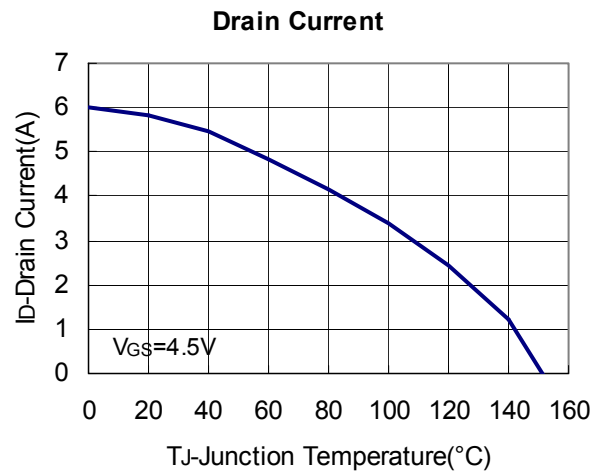
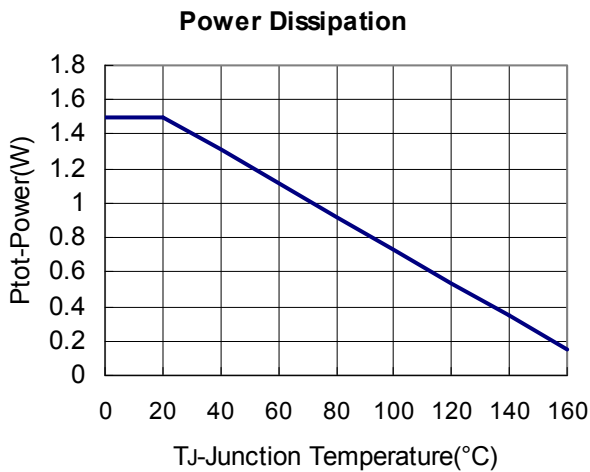
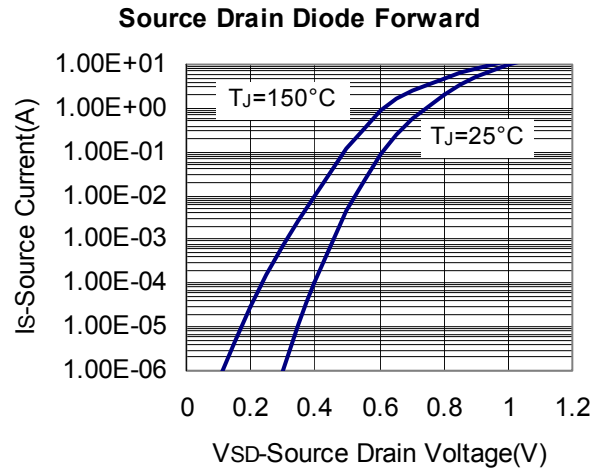
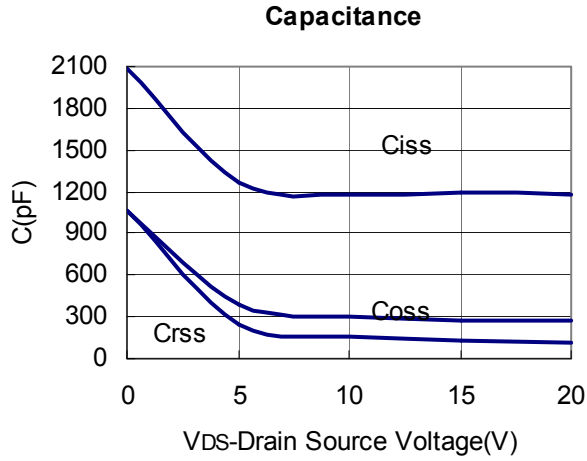
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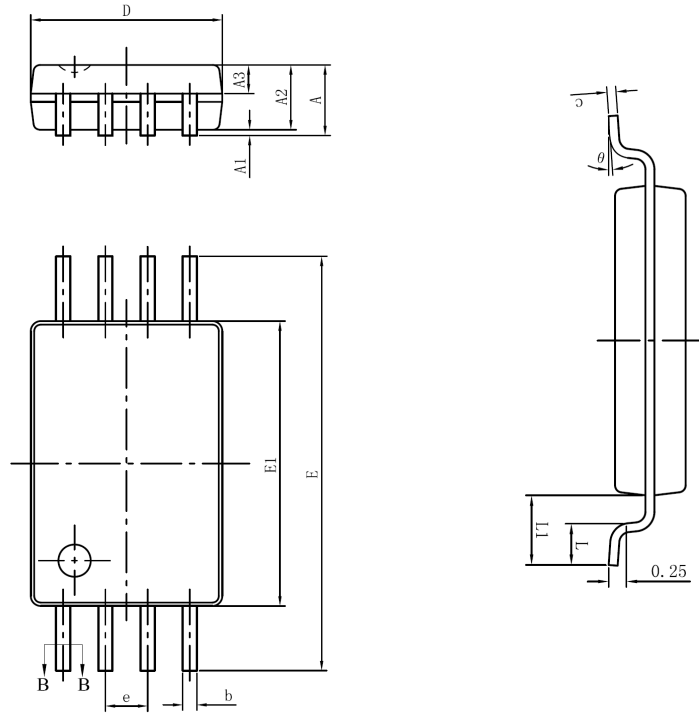
TYPICAL CHARACTERISTICS (25°C Unless Note)



TYPICAL CHARACTERISTICS (25°C Unless Note)



TSSOP-8 PACKAGE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	-	1.200	-	0.047
A1	0.050	0.150	0.002	0.006
A2	0.900	1.050	0.035	0.041
A3	0.390	0.490	0.015	0.019
b	0.210	0.300	0.008	0.012
b1	0.200	0.250	0.008	0.010
c	0.130	0.190	0.005	0.007
c1	0.120	0.140	0.005	0.006
D	2.900	3.100	0.114	0.122
E	6.200	6.600	0.244	0.260
E1	4.300	4.500	0.169	0.177
e	0.650 BSC		0.026 BSC	
L	0.450	0.750	0.018	0.030
θ	0°	8°	0°	8°