

Dual N-channel MOSFET (common drain)

ELM588822A-S

■General description

ELM588822A-S uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance.

■Features

- $V_{ds}=20V$
- $I_d=7.2A$
- $R_{ds(on)} = 28m\Omega$ ($V_{gs}=4.5V$)
- $R_{ds(on)} = 32m\Omega$ ($V_{gs}=2.5V$)
- $R_{ds(on)} = 45m\Omega$ ($V_{gs}=1.8V$)

■Maximum absolute ratings

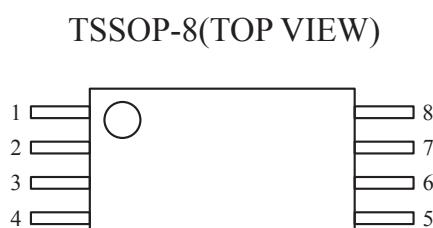
Ta=25°C. Unless otherwise noted.

Parameter	Symbol	Limit	Unit
Drain-source voltage	V_{ds}	20	V
Gate-source voltage	V_{gs}	± 12	V
Continuous drain current	I_d	7.2	A
		4.8	
Pulsed drain current	I_{dm}	20	A
Power dissipation	P_d	2.8	W
		1.8	
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	°C

■Thermal characteristics

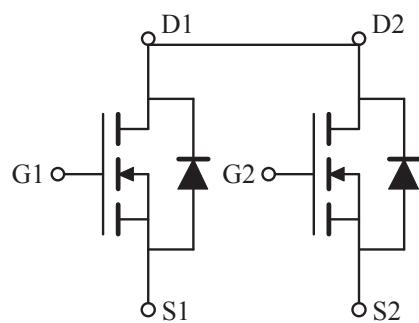
Parameter	Symbol	Typ.	Max.	Unit
Maximum junction-to-ambient	$R_{\theta ja}$		62.5	°C/W

■Pin configuration



Pin No.	Pin name
1	DRAIN1/DRAIN2
2	SOURCE1
3	SOURCE1
4	GATE1
5	GATE2
6	SOURCE2
7	SOURCE2
8	DRAIN1/DRAIN2

■Circuit



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■ Electrical characteristics

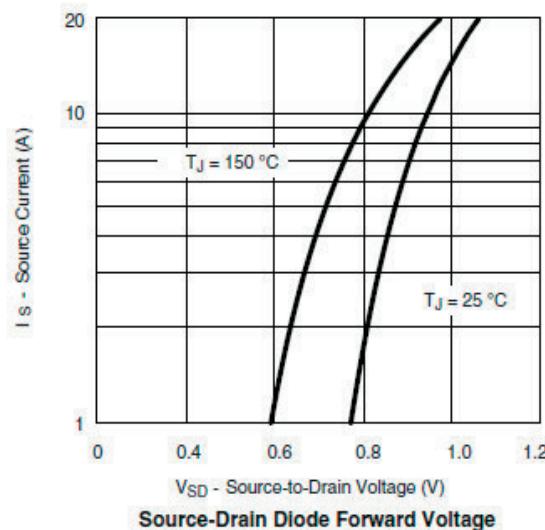
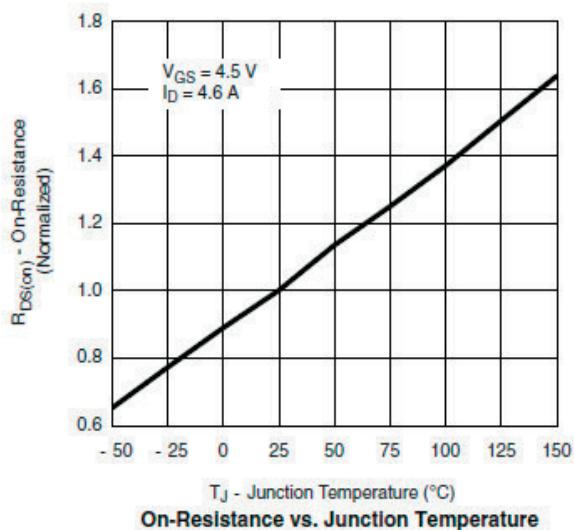
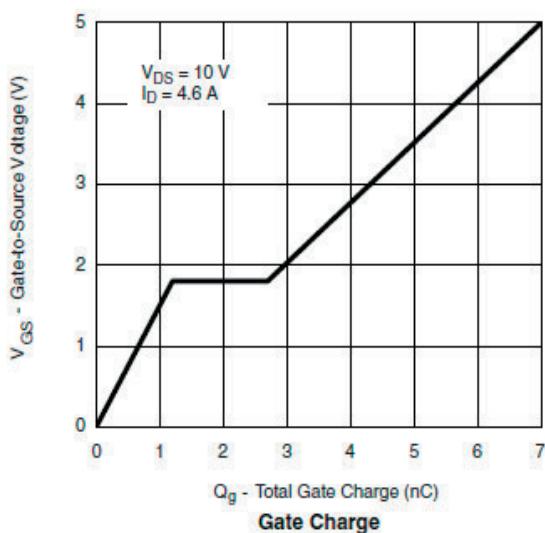
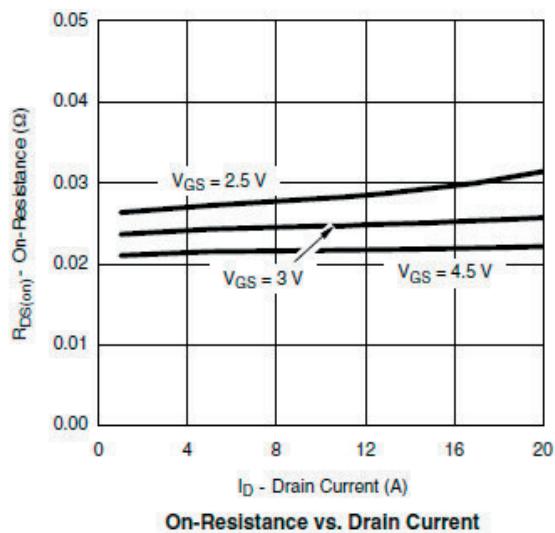
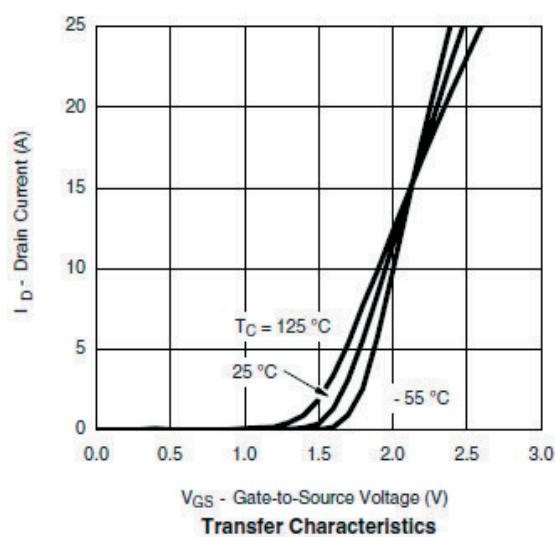
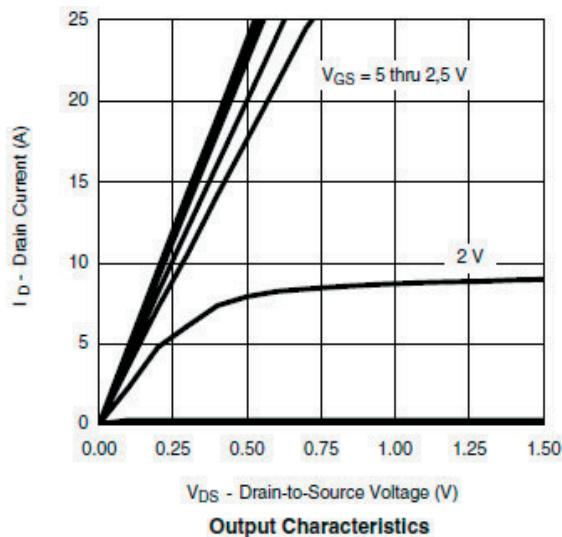
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Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
STATIC PARAMETERS						
Drain-source breakdown voltage	BVdss	Id=250µA, Vgs=0V	20			V
Zero gate voltage drain current	Idss	Vds=16V, Vgs=0V		1		µA
		Vds=16V, Vgs=0V, Ta=85°C		30		
Gate-body leakage current	Igss	Vds=0V, Vgs=±12V		±100		nA
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=250µA	0.4		0.8	V
On state drain current	Id(on)	Vgs=4.5V, Vds=5V	10			A
Static drain-source on-resistance	Rds(on)	Vgs=4.5V, Id=7.2A		24	28	mΩ
		Vgs=2.5V, Id=4.8A		27	32	
		Vgs=1.8V, Id=3.0A		36	45	
Forward transconductance	Gfs	Vds=5V, Id=7A		25		S
Diode forward voltage	Vsd	Is=1.6A, Vgs=0V		0.7	1.2	V
Max.body-diode continuous current	Is				1.5	A
DYNAMIC PARAMETERS						
Input capacitance	Ciss	Vgs=0V, Vds=20V, f=1MHz		700		pF
Output capacitance	Coss			75		pF
Reverse transfer capacitance	Crss			45		pF
SWITCHING PARAMETERS						
Total gate charge	Qg	Vgs=4.5V, Vds=10V, Id=7A		650		nC
Gate-source charge	Qgs			200		nC
Gate-drain charge	Qgd			180		nC
Turn-on delay time	td(on)	Vgs=4.5V, Vds=10V RL=1.4Ω, Id=1A Rgen=3Ω		8	12	ns
Turn-on rise time	tr			12	20	ns
Turn-off delay time	td(off)			32	40	ns
Turn-off fall time	tf			10	15	ns

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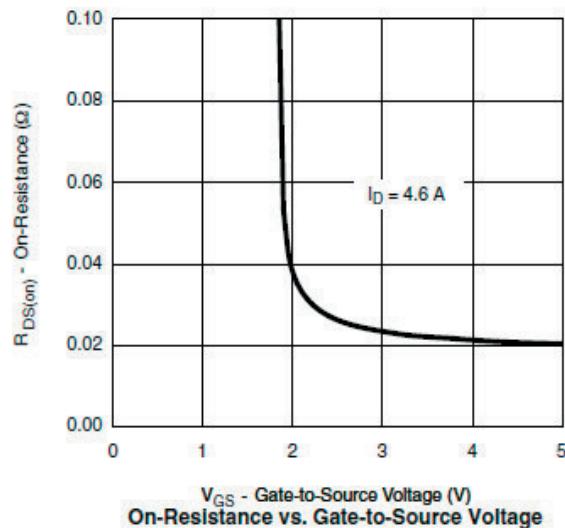
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■ Typical electrical and thermal characteristics

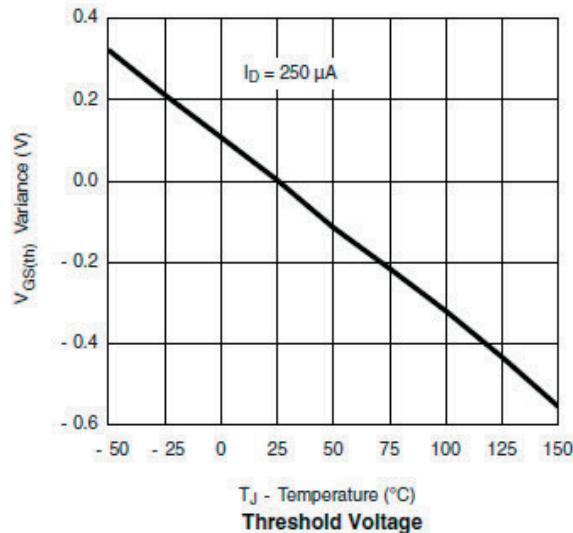


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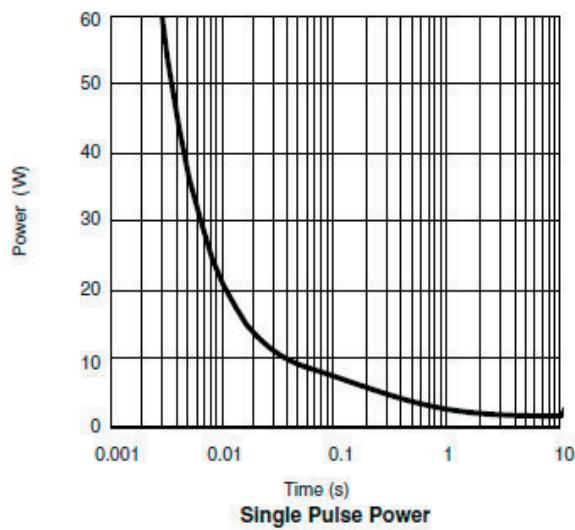
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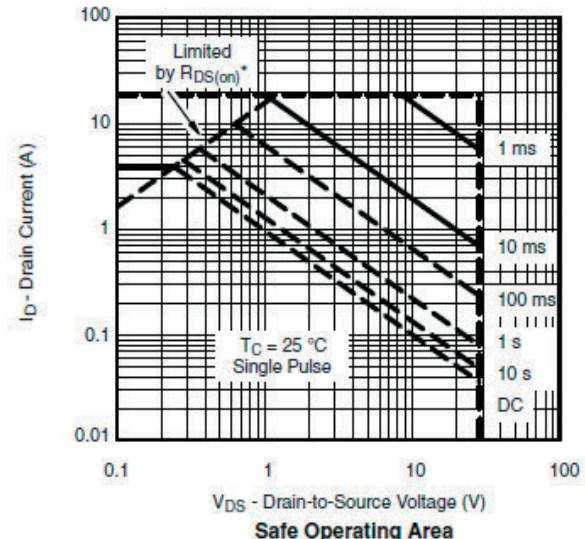
V_{GS} - Gate-to-Source Voltage (V)
On-Resistance vs. Gate-to-Source Voltage



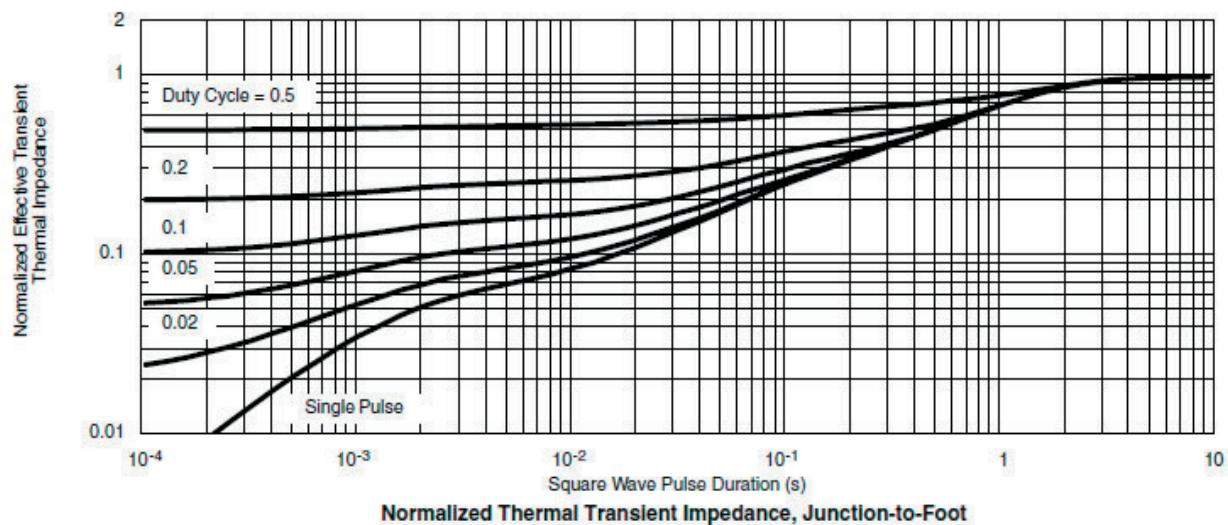
T_J - Temperature (°C)
Threshold Voltage



Single Pulse Power



V_{DS} - Drain-to-Source Voltage (V)
Safe Operating Area



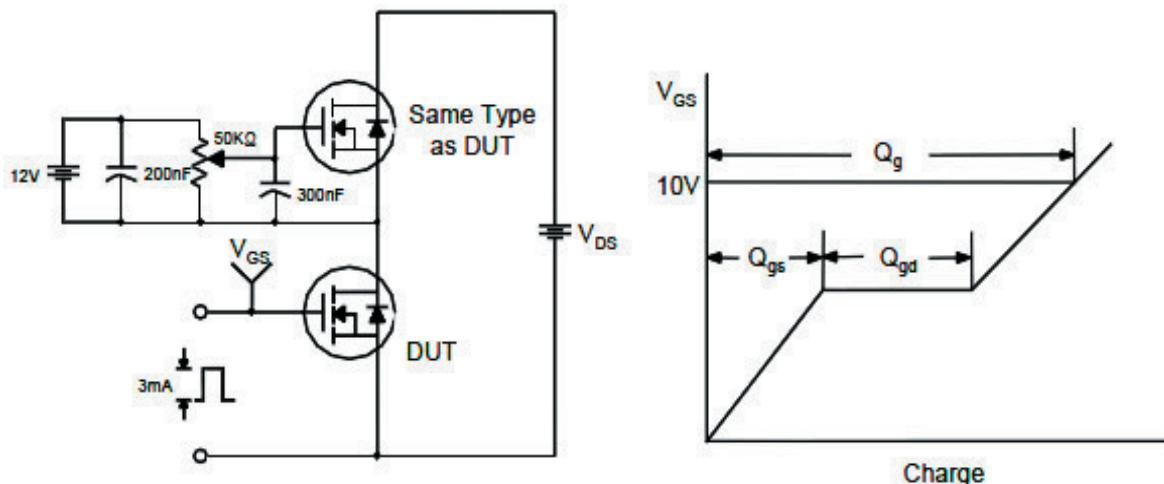
Normalized Thermal Transient Impedance, Junction-to-Foot

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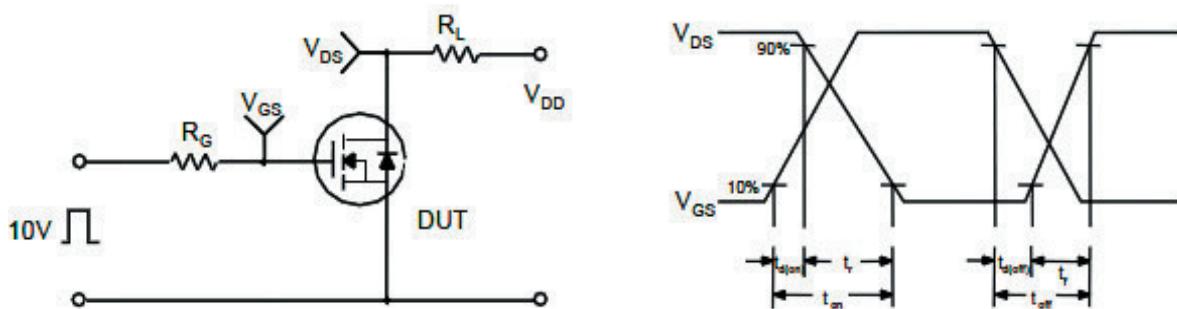
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■ Test circuit and waveform

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

