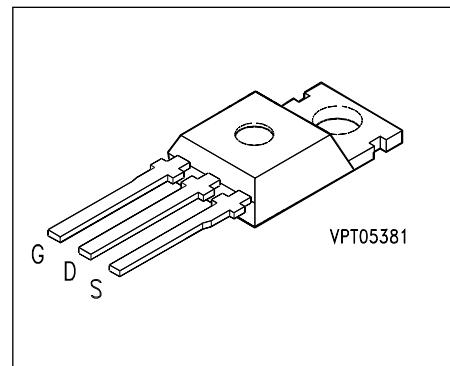


## SIPMOS® Power Transistors

**BUZ 72**  
**BUZ 72 A**

- N channel
- Enhancement mode
- Avalanche-rated



Type	$V_{DS}$	$I_D$	$R_{DS\ (on)}$	Package <sup>1)</sup>	Ordering Code
<b>BUZ 72</b>	100 V	10 A	0.20 $\Omega$	TO-220 AB	C67078-S1313-A2
<b>BUZ 72 A</b>	100 V	9.0 A	0.25 $\Omega$	TO-220 AB	C67078-S1313-A3

### Maximum Ratings

Parameter	Symbol	BUZ		Unit
		72	72 A	
Continuous drain current, $T_C = 25^\circ\text{C}$	$I_D$	<b>10</b>	<b>9.0</b>	A
Pulsed drain current, $T_C = 25^\circ\text{C}$	$I_{D\ puls}$	<b>40</b>	<b>36</b>	
Avalanche current, limited by $T_{j\ max}$	$I_{AR}$	<b>10</b>		
Avalanche energy, periodic limited by $T_{j\ (max)}$	$E_{AR}$	<b>7.9</b>		mJ
Avalanche energy, single pulse $I_D = 10\text{ A}$ , $V_{DD} = 25\text{ V}$ , $R_{GS} = 25\text{ }\Omega$ $L = 885\text{ }\mu\text{H}$ , $T_j = 25^\circ\text{C}$	$E_{AS}$	<b>59</b>		
Gate-source voltage	$V_{GS}$	<b>± 20</b>		V
Power dissipation, $T_C = 25^\circ\text{C}$	$P_{tot}$	<b>40</b>		W
Operating and storage temperature range	$T_j$ , $T_{stg}$	<b>– 55 ... + 150</b>		°C

Thermal resistance, chip-case	$R_{th\ JC}$	<b>≤ 3.1</b>	K/W
DIN humidity category, DIN 40 040	–	<b>E</b>	–
IEC climatic category, DIN IEC 68-1	–	<b>55/150/56</b>	

1) See chapter Package Outlines.

**Electrical Characteristics**at  $T_j = 25^\circ\text{C}$ , unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

**Static characteristics**

Drain-source breakdown voltage $V_{GS} = 0 \text{ V}, I_D = 0.25 \text{ mA}$	$V_{(\text{BR})\text{DSS}}$	100	–	–	V
Gate threshold voltage $V_{GS} = V_{DS}, I_D = 1 \text{ mA}$	$V_{GS\text{(th)}}$	2.1	3.0	4.0	
Zero gate voltage drain current $V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}$ $T_j = 25^\circ\text{C}$ $T_j = 125^\circ\text{C}$	$I_{DSS}$	–	0.1	1.0	$\mu\text{A}$
–	–	–	10	100	
Gate-source leakage current $V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$	$I_{GSS}$	–	10	100	nA
Drain-source on-resistance $V_{GS} = 10 \text{ V}, I_D = 6 \text{ A}$ BUZ 72 BUZ 72 A	$R_{DS\text{(on)}}$	–	0.15	0.2	$\Omega$
–	–	–	0.2	0.25	

**Dynamic characteristics**

Forward transconductance $V_{DS} \geq 2 \times I_D \times R_{DS\text{(on)max}}, I_D = 6 \text{ A}$	$g_{fs}$	3.0	4.3	–	S
Input capacitance $V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	$C_{iss}$	–	400	530	pF
Output capacitance $V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	$C_{oss}$	–	120	180	
Reverse transfer capacitance $V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	$C_{rss}$	–	70	105	
Turn-on time $t_{on}$ , ( $t_{on} = t_{d\text{(on)}} + t_r$ ) $V_{DD} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3.0 \text{ A}, R_{GS} = 50 \Omega$	$t_{d\text{(on)}}$ $t_r$	–	10	15	ns
Turn-off time $t_{off}$ , ( $t_{off} = t_{d\text{(off)}} + t_f$ ) $V_{DD} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3.0 \text{ A}, R_{GS} = 50 \Omega$	$t_{d\text{(off)}}$ $t_f$	–	45	70	
–	–	–	55	75	
–	–	–	40	55	

**Electrical Characteristics** (cont'd)  
at  $T_j = 25^\circ\text{C}$ , unless otherwise specified.

<b>Parameter</b>	<b>Symbol</b>	<b>Values</b>			<b>Unit</b>
		<b>min.</b>	<b>typ.</b>	<b>max.</b>	

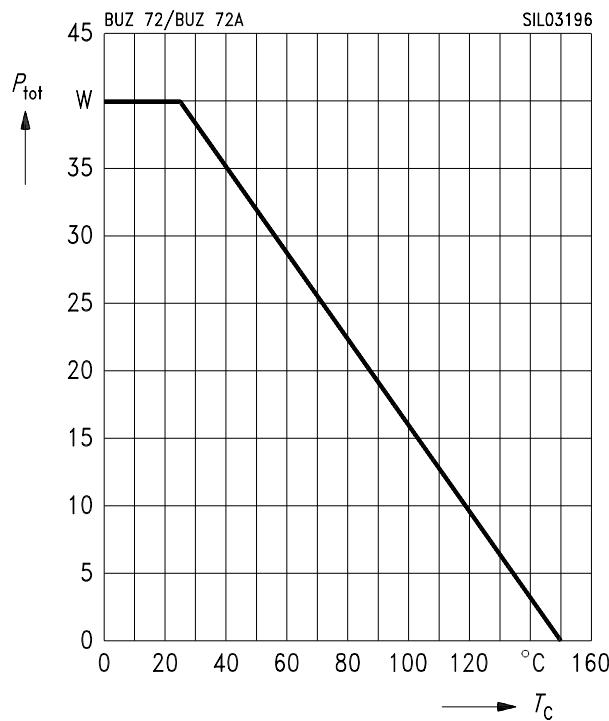
#### Reverse diode

Continuous reverse drain current $T_C = 25^\circ\text{C}$ BUZ 72 BUZ 72 A	$I_S$	— —	— —	10 9.0	A
Pulsed reverse drain current $T_C = 25^\circ\text{C}$ BUZ 72 BUZ 72 A	$I_{SM}$	— —	— —	40 36	
Diode forward on-voltage $I_S = 20\text{ A}$ , $V_{GS} = 0\text{ V}$	$V_{SD}$	—	1.4	1.6	V
Reverse recovery time $V_R = 30\text{ V}$ , $I_F = I_S$ , $di_F / dt = 100\text{ A}/\mu\text{s}$	$t_{rr}$	—	170	—	ns
Reverse recovery charge $V_R = 30\text{ V}$ , $I_F = I_S$ , $di_F / dt = 100\text{ A}/\mu\text{s}$	$Q_{rr}$	—	0.30	—	$\mu\text{C}$

**Characteristics** at  $T_j = 25^\circ\text{C}$ , unless otherwise specified.

### Total power dissipation

$$P_{\text{tot}} = f(T_C)$$

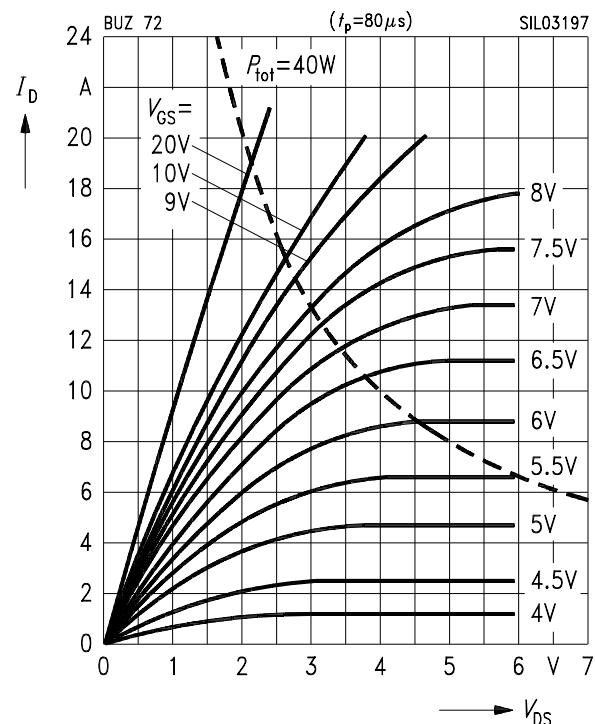


### Typ. output characteristics

$$I_D = f(V_{DS})$$

parameter:  $t_p = 80 \mu\text{s}$

**BUZ 72**

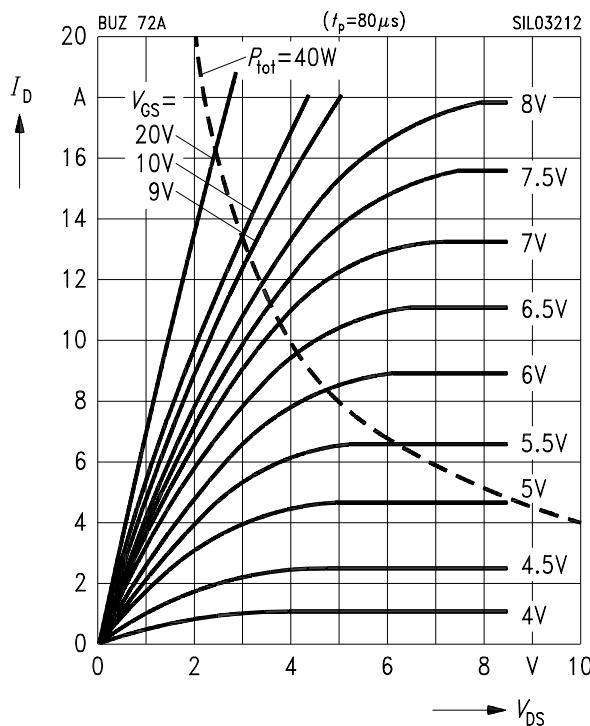


### Typ. output characteristics

$$I_D = f(V_{DS})$$

parameter:  $t_p = 80 \mu\text{s}$

**BUZ 72 A**

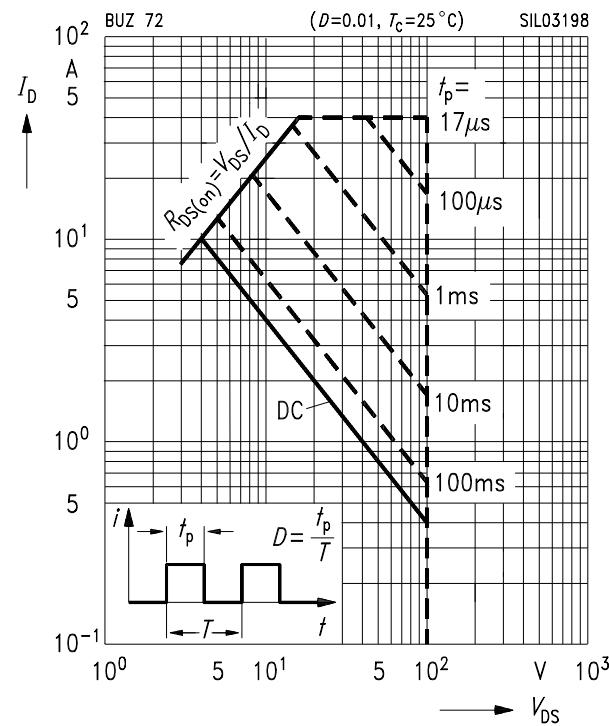


### Safe operating area

$$I_D = f(V_{DS})$$

parameter:  $D = 0.01, T_C = 25^\circ\text{C}$

**BUZ 72**

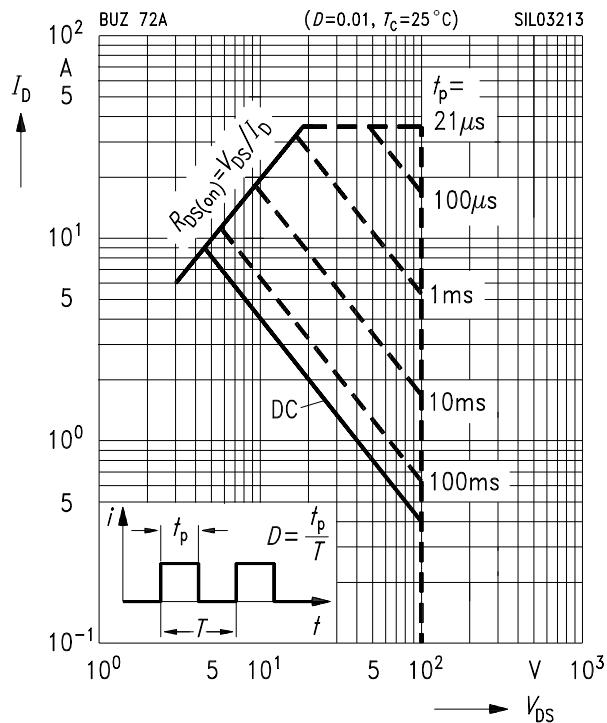


### Safe operating area

$$I_D = f(V_{DS})$$

parameter:  $D = 0.01$ ,  $T_C = 25^\circ\text{C}$

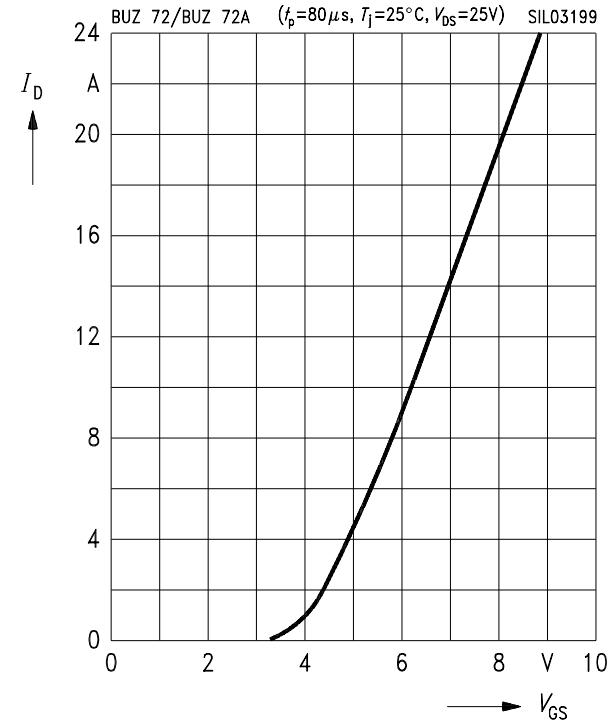
**BUZ 72 A**



### Typ. transfer characteristics

$$I_D = f(V_{GS})$$

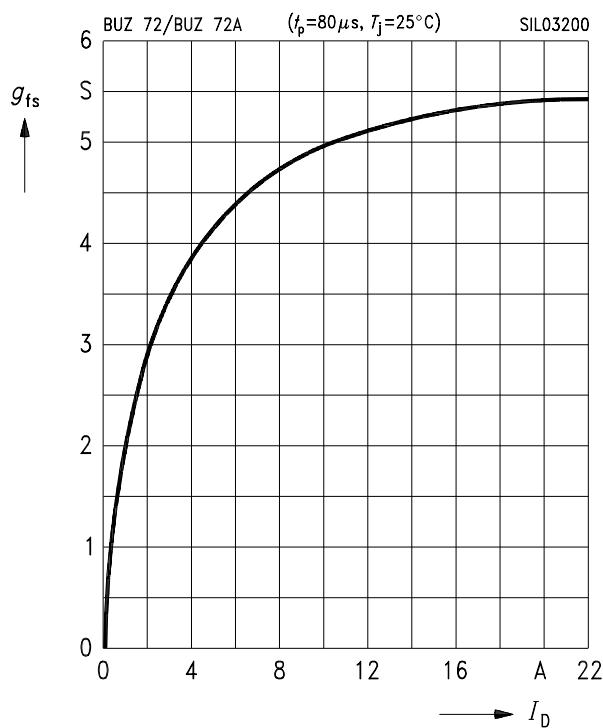
parameter:  $t_p = 80 \mu\text{s}$ ,  $V_{DS} = 25 \text{ V}$



### Typ.forward transconductance

$$g_{fs} = f(I_D)$$

parameter:  $t_p = 80 \mu\text{s}$

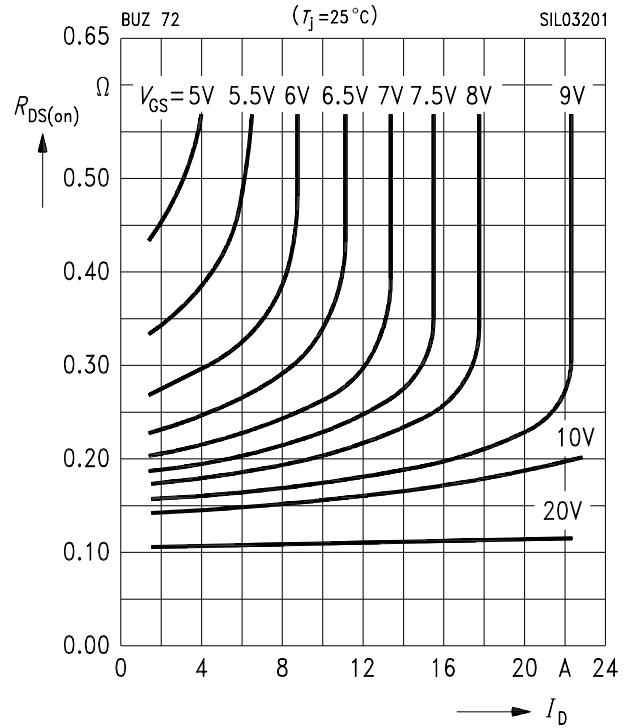


### Typ. drain-source on-resistance

$$R_{DS(on)} = f(I_D)$$

parameter:  $V_{GS}$

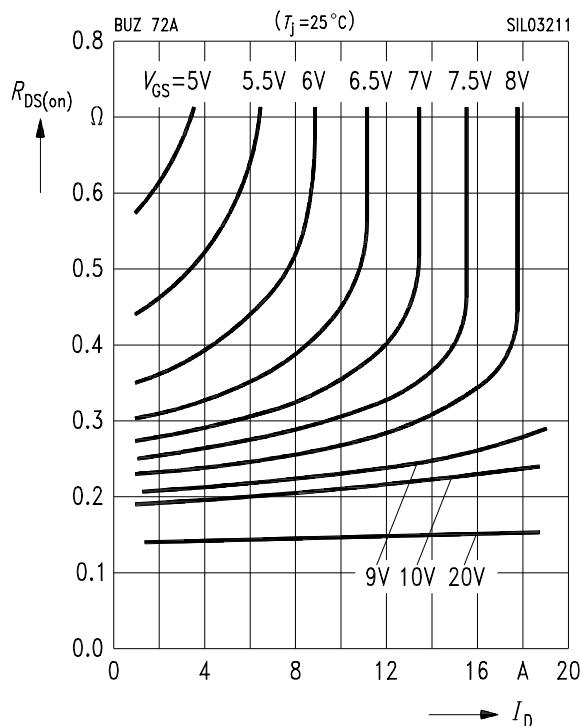
**BUZ 72**



**Typ. drain-source on-resistance**

$R_{DS(on)} = f(I_D)$   
parameter:  $V_{GS}$

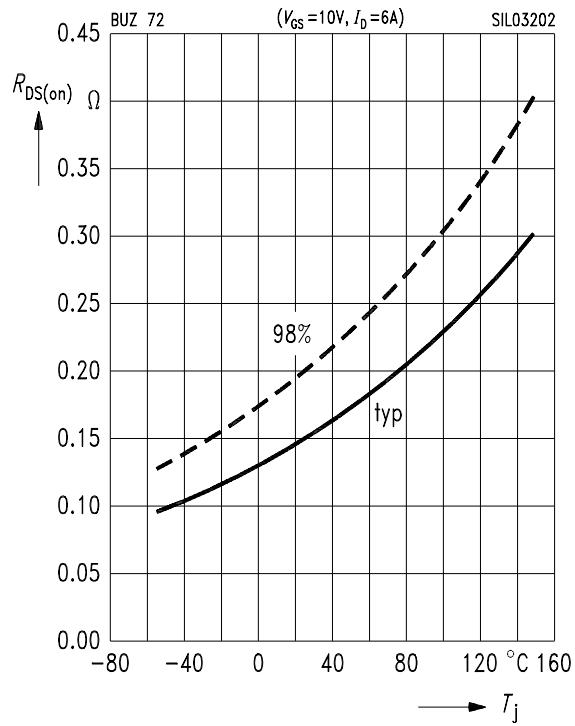
**BUZ 72 A**



**Drain-source on-resistance**

$R_{DS(on)} = f(T_j)$   
parameter:  $I_D = 6\text{ A}$ ,  $V_{GS} = 10\text{ V}$ , (spread)

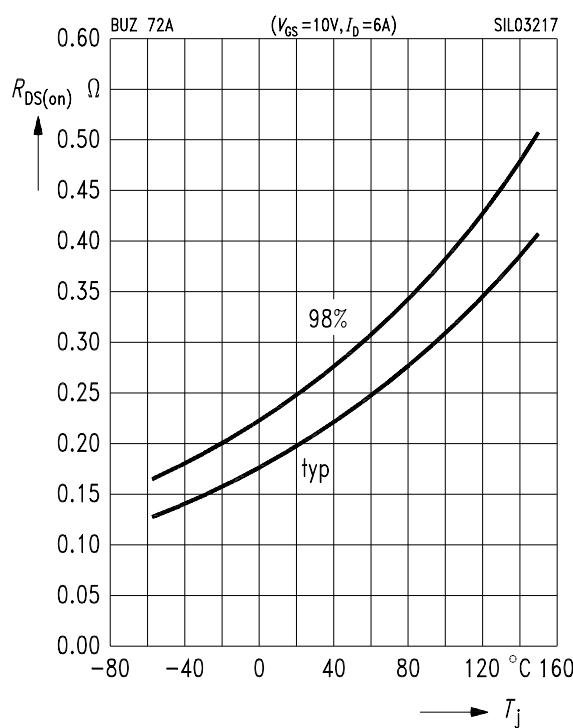
**BUZ 72**



**Drain-source on-resistance**

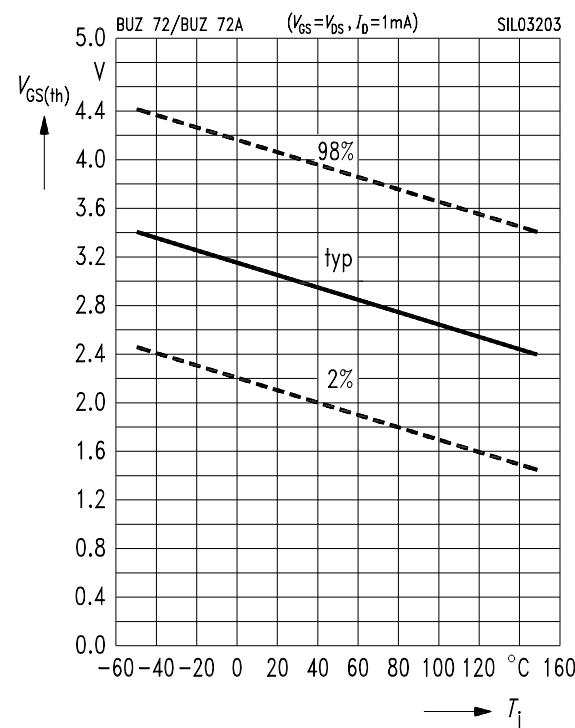
$R_{DS(on)} = f(T_j)$   
parameter:  $I_D = 6\text{ A}$ ,  $V_{GS} = 10\text{ V}$ , (spread)

**BUZ 72 A**



**Gate threshold voltage**

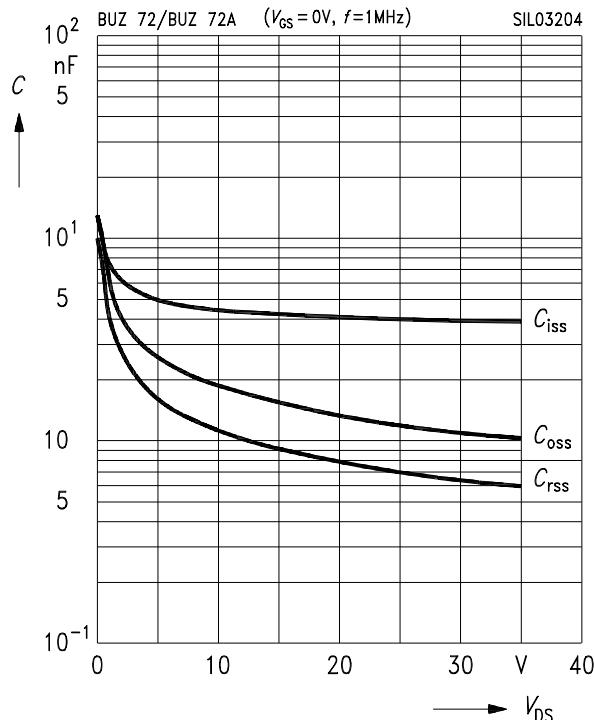
$V_{GS(th)} = f(T_j)$   
parameter:  $V_{GS} = V_{DS}$ ,  $I_D = 1\text{ mA}$ , (spread)



### Typ. capacitances

$$C = f(V_{DS})$$

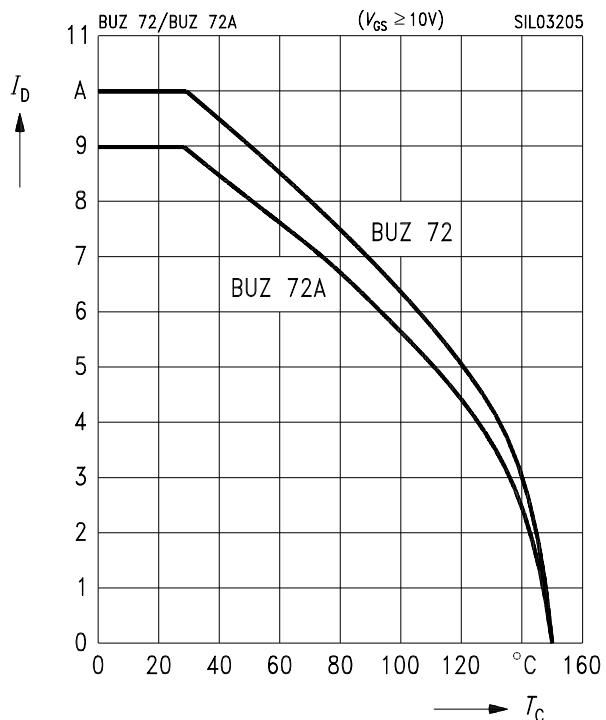
parameter:  $V_{GS} = 0 \text{ V}$ ,  $f = 1 \text{ MHz}$



### Drain current

$$I_D = f(T_C)$$

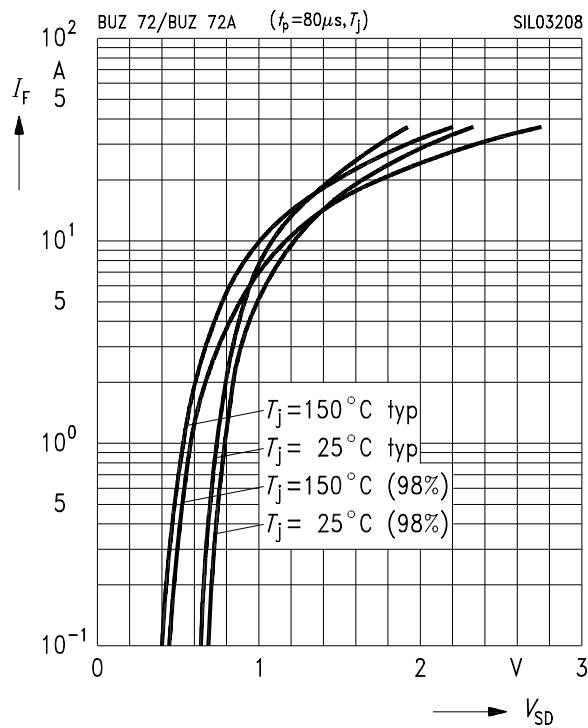
parameter:  $V_{GS} \geq 10 \text{ V}$



### Forward characteristics of reverse diode

$$I_F = f(V_{SD})$$

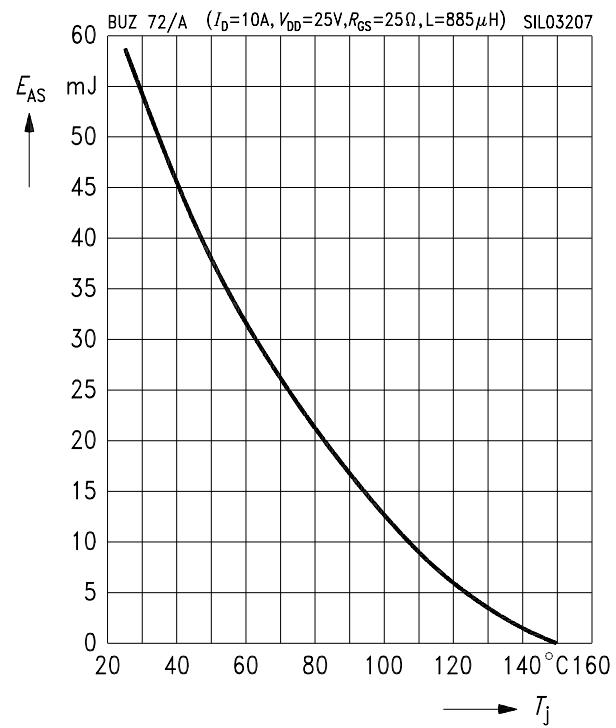
parameter:  $T_j$ ,  $t_p = 80 \mu\text{s}$ , (spread)



### Avalanche energy $E_{AS} = f(T_j)$

parameter:  $I_D = 10 \text{ A}$ ,  $V_{DD} = 25 \text{ V}$

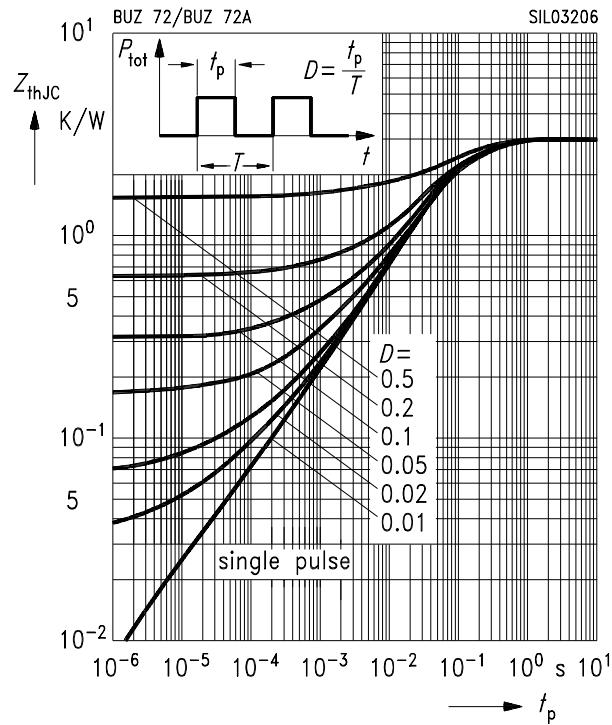
$$R_{GS} = 25 \Omega, L = 885 \mu\text{H}$$



### Transient thermal impedance

$$Z_{\text{thJC}} = f(t_p)$$

parameter:  $D = t_p / T$



### Typ. gate charge

$$V_{\text{GS}} = f(Q_{\text{Gate}})$$

parameter:  $I_{D \text{ puls}} = 21 \text{ A}$

