



IXTH15N70

MegaMOS™ FET

N-Channel Enhancement Mode

$$V_{DSS} = 700 \text{ V}$$

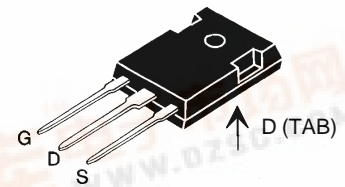
$$I_{D(\text{cont})} = 15 \text{ A}$$

$$R_{DS(\text{on})} = 0.45 \text{ } \Omega$$



Symbol	Test Conditions	Maximum Ratings	
V_{DSS}	$T_J = 25^\circ\text{C}$ to 150°C	700	V
V_{DGR}	$T_J = 25^\circ\text{C}$ to 150°C ; $R_{GS} = 1 \text{ M}\Omega$	700	V
V_{GS}	Continuous	± 20	V
V_{GSM}	Transient	± 30	V
I_{D25}	$T_C = 25^\circ\text{C}$	15	A
I_{DM}	$T_C = 25^\circ\text{C}$, pulse width limited by T_{JM}	60	A
P_D	$T_C = 25^\circ\text{C}$	300	W
T_J		-55 ... +150	$^\circ\text{C}$
T_{JM}		150	$^\circ\text{C}$
T_{stg}		-55 ... +150	$^\circ\text{C}$
M_d	Mounting torque	1.13/10	Nm/lb.in.
Weight		6	g
Maximum lead temperature for soldering 1.6 mm (0.062 in.) from case for 10 s		300	$^\circ\text{C}$

TO-247 AD



G = Gate, D = Drain, S = Source, TAB = Drain

Features

- International standard package JEDEC TO-247 AD
- Low $R_{DS(\text{on})}$ HDMOS™ process
- Rugged polysilicon gate cell structure
- High commutating dv/dt rating
- Fast switching times

Applications

- Switch-mode and resonant-mode power supplies
- Motor controls
- Uninterruptible Power Supplies (UPS)
- DC choppers

Advantages

- Easy to mount with 1 screw (isolated mounting screw hole)
- Space savings
- High power density

Symbol	Test Conditions	Characteristic Values ($T_J = 25^\circ\text{C}$, unless otherwise specified)		
		min.	typ.	max.
V_{DSS}	$V_{GS} = 0 \text{ V}$, $I_D = 250 \text{ } \mu\text{A}$	700		V
$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$, $I_D = 250 \text{ } \mu\text{A}$	2		4.5 V
I_{GSS}	$V_{GS} = \pm 20 \text{ V}$, V_{DC} , $V_{DS} = 0$			$\pm 100 \text{ nA}$
I_{DSS}	$V_{DS} = 0.8 \cdot V_{DSS}$, $V_{GS} = 0 \text{ V}$			$T_J = 25^\circ\text{C}$: 200 μA $T_J = 125^\circ\text{C}$: 1 mA
$R_{DS(\text{on})}$	$V_{GS} = 10 \text{ V}$, $I_D = 0.5 \cdot I_{D25}$ Pulse test, $t \leq 300 \text{ } \mu\text{s}$, duty cycle $d \leq 2 \%$			0.45 Ω



Symbol	Test Conditions	Characteristic Values		
		(T _J = 25°C, unless otherwise specified)		
		min.	typ.	max.
g_{fs}	V _{DS} = 10 V; I _D = 0.5 I _{D25} , pulse test	11	18	S
C_{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		4500	pF
C_{oss}			420	pF
C_{rss}			140	pF
t_{d(on)}	V _{GS} = 10 V, V _{DS} = 0.5 • V _{DSS} , I _D = 0.5 I _{D25} R _G = 2 Ω, (External)		20	40 ns
t_r			43	60 ns
t_{d(off)}			70	90 ns
t_f			40	60 ns
Q_{g(on)}	V _{GS} = 10 V, V _{DS} = 0.5 • V _{DSS} , I _D = 0.5 I _{D25}		150	170 nC
Q_{gs}			29	40 nC
Q_{gd}			60	85 nC
R_{thJC}			0.42	K/W
R_{thCK}		0.25		K/W

Source-Drain Diode

Symbol	Test Conditions	Characteristic Values		
		(T _J = 25°C, unless otherwise specified)		
		min.	typ.	max.
I_S	V _{GS} = 0 V			15 A
I_{SM}	Repetitive; pulse width limited by T _{JM}			60 A
V_{SD}	I _F = I _S , V _{GS} = 0 V, Pulse test, t ≤ 300 μs, duty cycle d ≤ 2 %			1.5 V
t_{rr}	I _F = I _S , -di/dt = 100 A/μs, V _R = 100 V	600		ns

TO-247 AD Outline
