

# N-Channel Enhancement Mode Power MOSFET

## MTN3N60J3

**$BV_{DSS}$  : 600V**  
 **$R_{DS(ON)}$  : 3.6Ω (typ.)**  
 **$I_D$  : 3A**

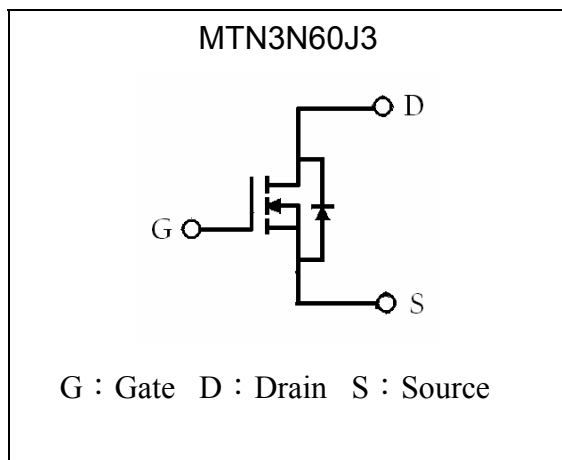
### Features

- Low On Resistance
- Simple Drive Requirement
- Fast Switching Characteristic
- Pb-free lead plating and halogen-free package

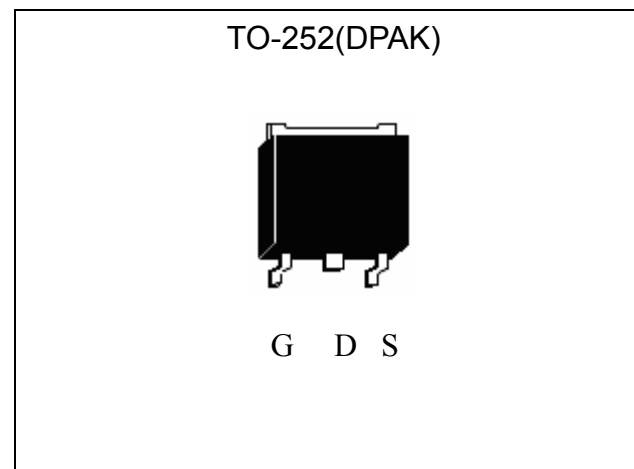
### Applications

- Adapter
- Switching Mode Power Supply

### Symbol

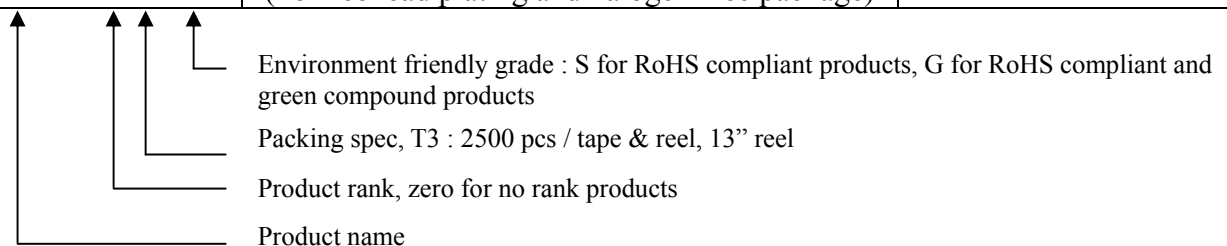


### Outline



### Ordering Information

Device	Package	Shipping
MTN3N60J3-0-T3-G	TO-252 (Pb-free lead plating and halogen-free package)	2500 pcs / Tape & Reel



**Absolute Maximum Ratings** ( $T_c=25^\circ\text{C}$ )

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	$V_{DS}$	600	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Continuous Drain Current	$I_D$	3*	A
Continuous Drain Current @ $T_c=100^\circ\text{C}$	$I_D$	1.8*	A
Pulsed Drain Current @ $V_{GS}=10\text{V}$ (Note 1)	$I_{DM}$	12*	A
Single Pulse Avalanche Energy (Note 2)	$E_{AS}$	24.5	mJ
Avalanche Current (Note 1)	$I_{AR}$	3	A
Repetitive Avalanche Energy (Note 1)	$E_{AR}$	3.3	mJ
Peak Diode Recovery $dv/dt$ (Note 3)	$dv/dt$	4.5	V/ns
Maximum Temperature for Soldering @ Lead at 0.125 in(0.318mm) from case for 10 seconds	$T_L$	300	$^\circ\text{C}$
Total Power Dissipation ( $T_c=25^\circ\text{C}$ )	$P_D$	50	W
Linear Derating Factor		0.4	W/ $^\circ\text{C}$
Operating Junction and Storage Temperature	$T_j, T_{stg}$	-55~+150	$^\circ\text{C}$

\*Drain current limited by maximum junction temperature

Note : 1.Repetitive rating; pulse width limited by maximum junction temperature.

2.  $I_{AS}=3\text{A}$ ,  $V_{DD}=50\text{V}$ ,  $L=5\text{mH}$ ,  $R_G=25\Omega$ , starting  $T_J=+25^\circ\text{C}$ .3.  $I_{SD}\leq 3\text{A}$ ,  $dI/dt\leq 100\text{A}/\mu\text{s}$ ,  $V_{DD}\leq BV_{DSS}$ , starting  $T_J=+25^\circ\text{C}$ .**Thermal Data**

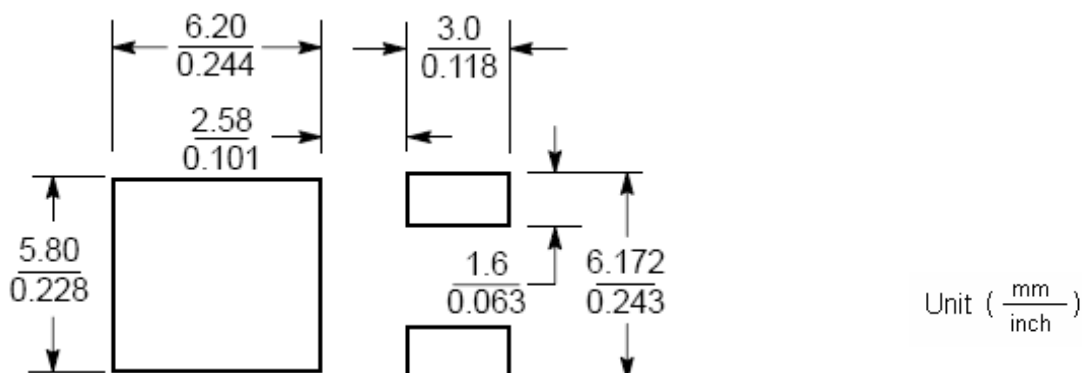
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{th,j-c}$	2.5	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-ambient, max	$R_{th,j-a}$	50 (Note)	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-ambient, max	$R_{th,j-a}$	110	$^\circ\text{C}/\text{W}$

Note : When mounted on the minimum pad size recommended (PCB mount).

**Characteristics (Tj=25°C, unless otherwise specified)**

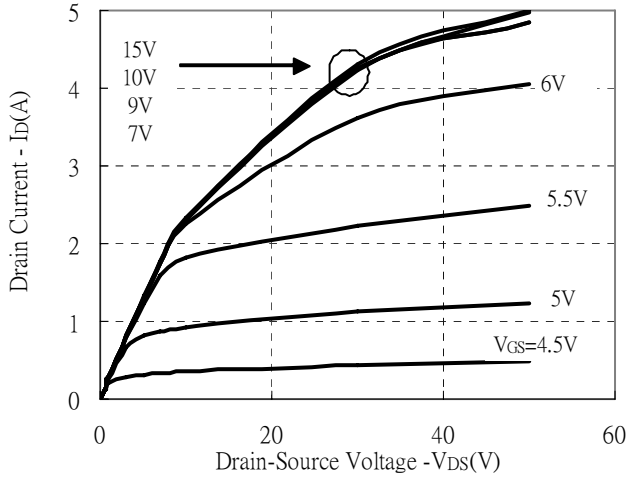
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	600	-	-	V	V <sub>GS</sub> =0, I <sub>D</sub> =250μA, Tj=25°C
ΔBV <sub>DSS</sub> /ΔTj	-	0.65	-	V/°C	Reference to 25°C, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	2.0	-	4.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250μA
*G <sub>FS</sub>	-	1.8	-	S	V <sub>DS</sub> =15V, I <sub>D</sub> =1.5A
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±30
I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =600V, V <sub>GS</sub> =0
	-	-	10		V <sub>DS</sub> =480V, V <sub>GS</sub> =0, Tj=125°C
*R <sub>DS(ON)</sub>	-	3.6	4.5	Ω	V <sub>GS</sub> =10V, I <sub>D</sub> =1.5A
<b>Dynamic</b>					
*Q <sub>g</sub>	-	11	-	nC	V <sub>DS</sub> =480V, I <sub>D</sub> =3A, V <sub>GS</sub> =10V
*Q <sub>gs</sub>	-	2	-		
*Q <sub>gd</sub>	-	5	-		
*t <sub>d(ON)</sub>	-	10	-	ns	V <sub>DS</sub> =300V, I <sub>D</sub> =3A, V <sub>GS</sub> =10V, R <sub>G</sub> =25Ω
*t <sub>r</sub>	-	27	-		
*t <sub>d(OFF)</sub>	-	24	-		
*t <sub>f</sub>	-	30	-		
C <sub>iss</sub>	-	435	-	pF	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1MHz
C <sub>oss</sub>	-	58	-		
C <sub>rss</sub>	-	10.5	-		
<b>Source-Drain Diode</b>					
*I <sub>S</sub>	-	-	3	A	
*I <sub>SM</sub>	-	-	12		
*V <sub>SD</sub>	-	-	1.5	V	I <sub>S</sub> =3A, V <sub>GS</sub> =0V
*t <sub>rr</sub>	-	260	-	ns	V <sub>GS</sub> =0, I <sub>F</sub> =3A, dI/dt=100A/μs
*Q <sub>rr</sub>	-	1.6	-	μC	

\*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

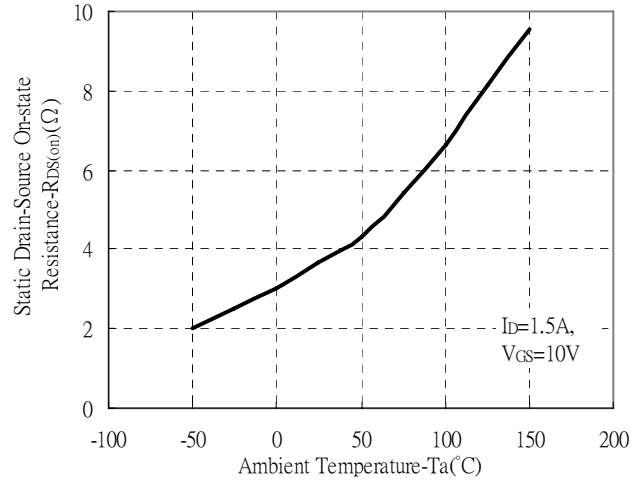
**Recommended soldering footprint**


## Typical Characteristics

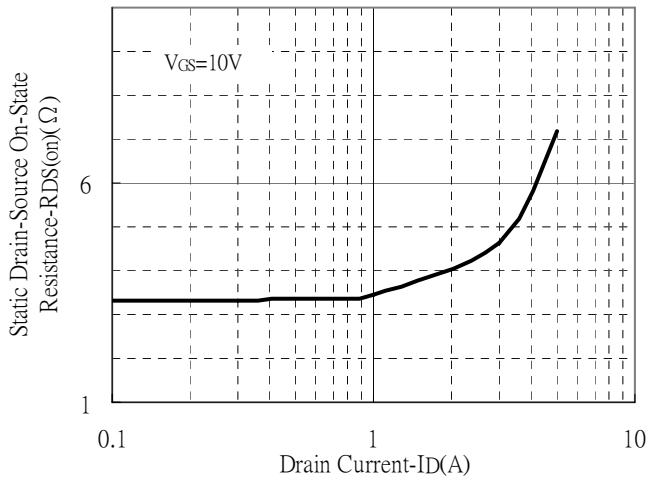
Typical Output Characteristics



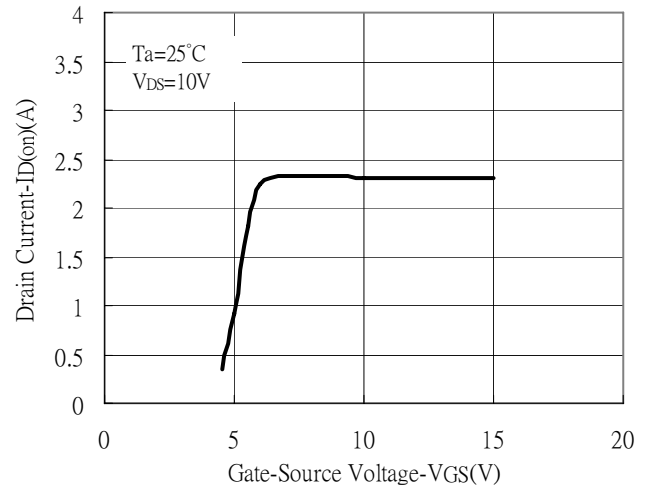
Static Drain-Source On-resistance vs Ambient Temperature



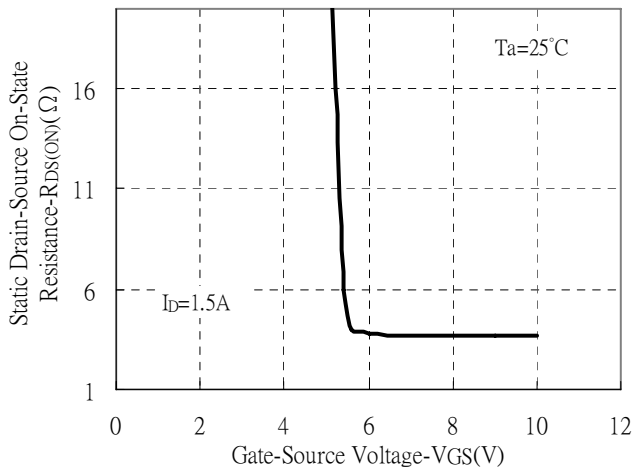
Static Drain-Source On-State resistance vs Drain Current



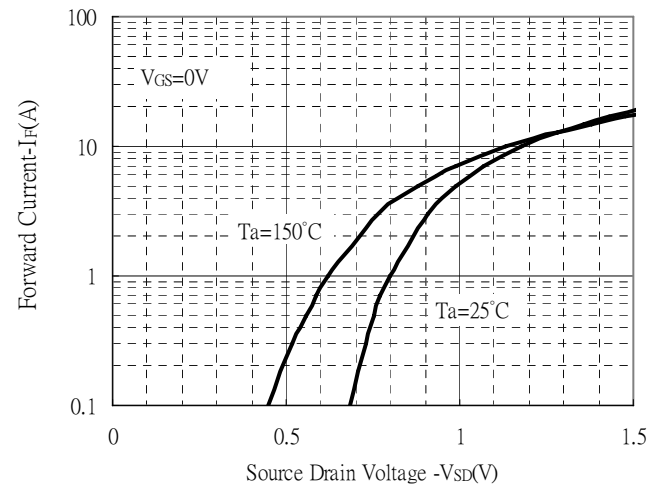
Drain Current vs Gate-Source Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage



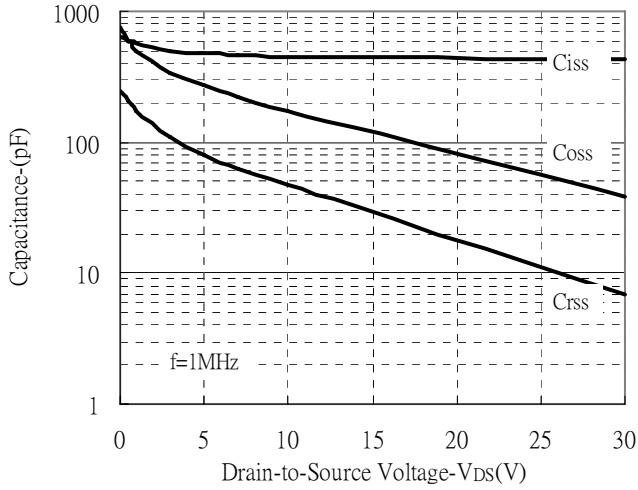
Forward Drain Current vs Source-Drain Voltage



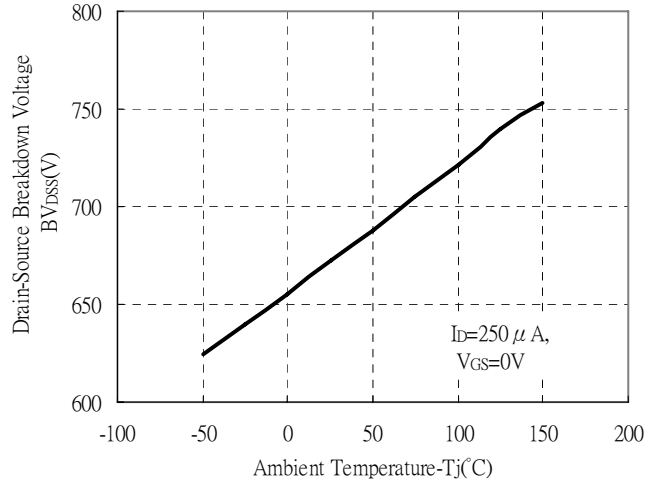


**Typical Characteristics(Cont.)**

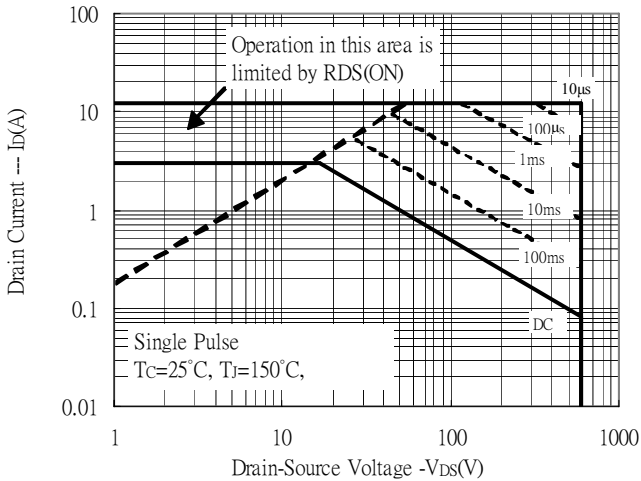
Capacitance vs Reverse Voltage



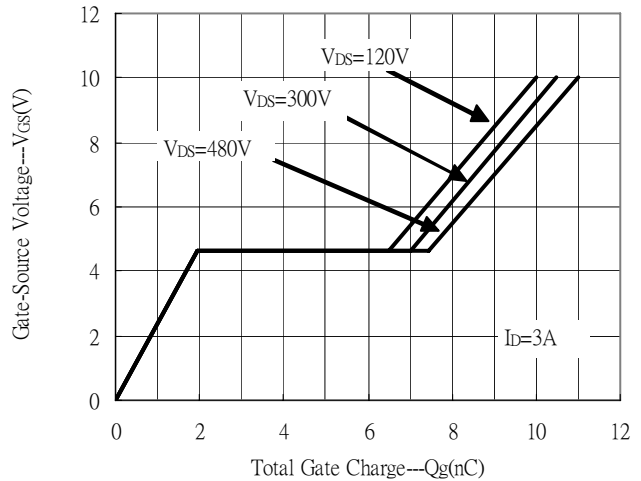
Brekdown Voltage vs Ambient Temperature



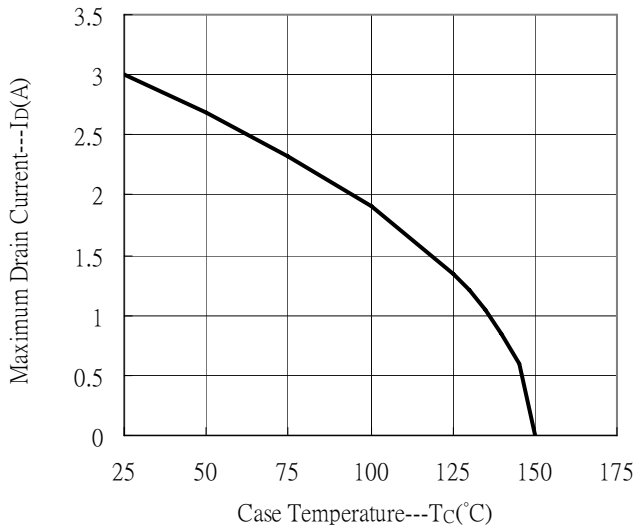
Maximum Safe Operating Area



Gate Charge Characteristics

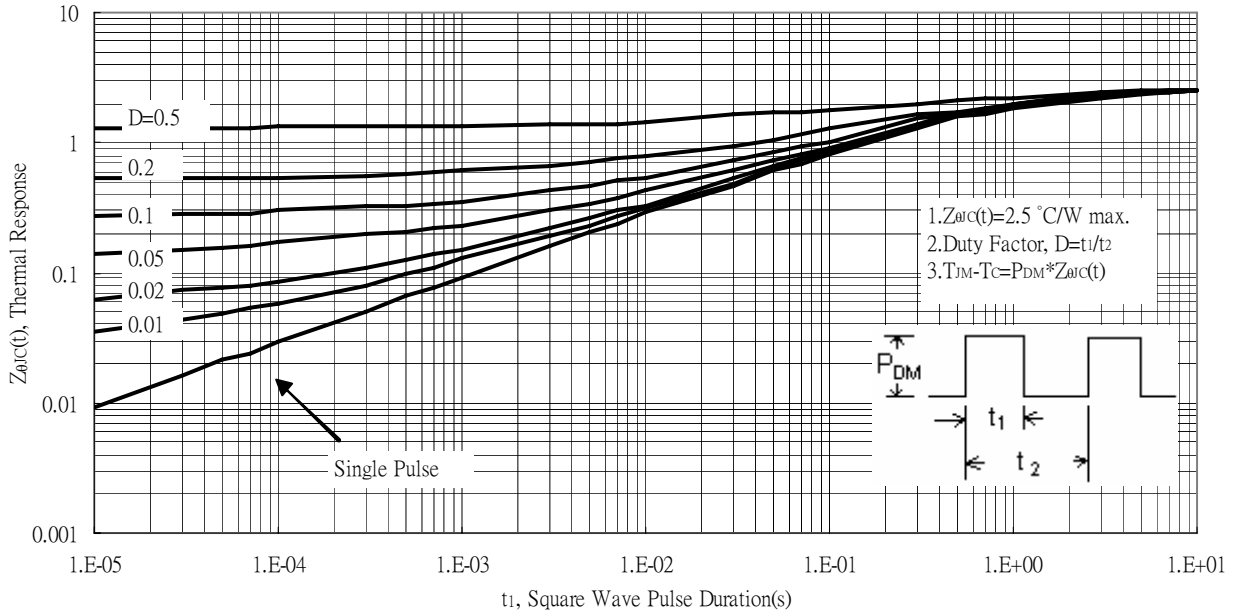


Maximum Drain Current vs Case Temperature

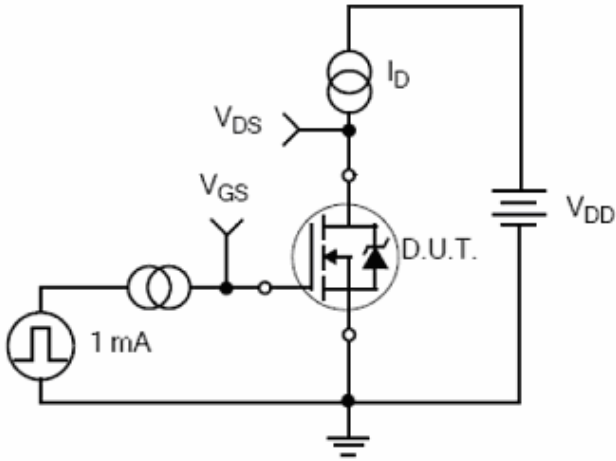


**Typical Characteristics(Cont.)**

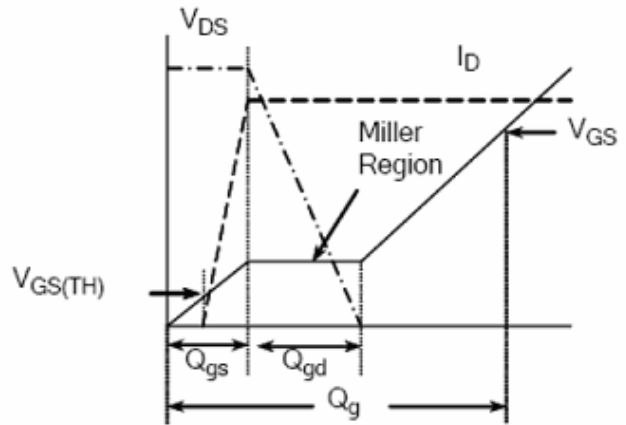
Transient Thermal Response Curves



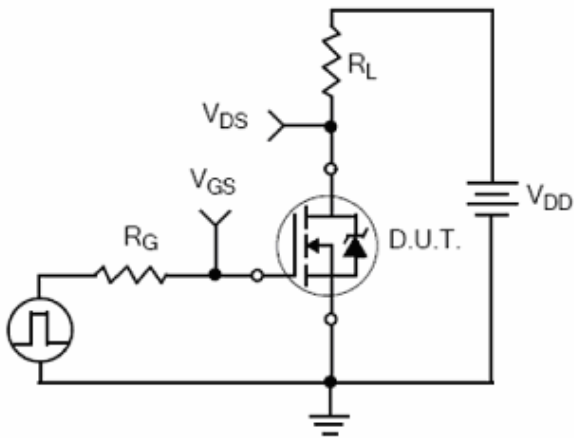
**Test Circuit and Waveforms**



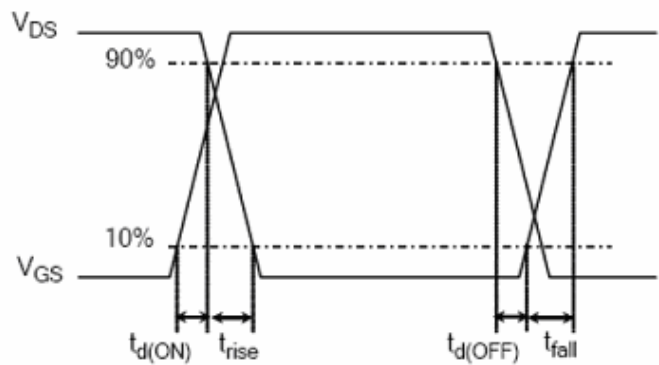
**Gate Charge Test Circuit**



**Gate Charge Waveform**

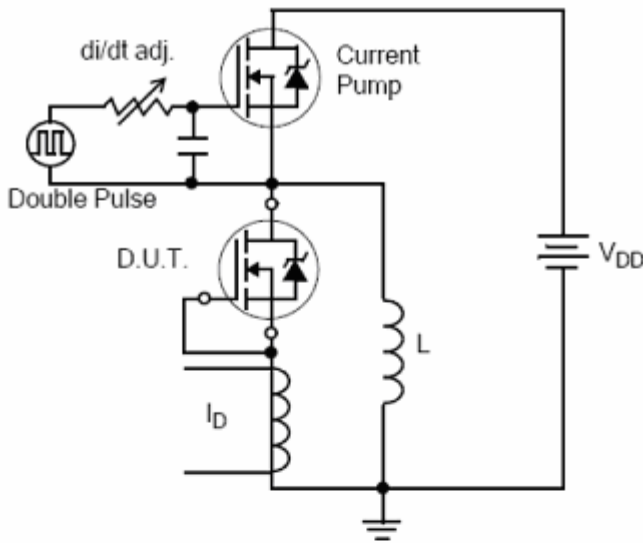


**Resistive Switching Test Circuit**

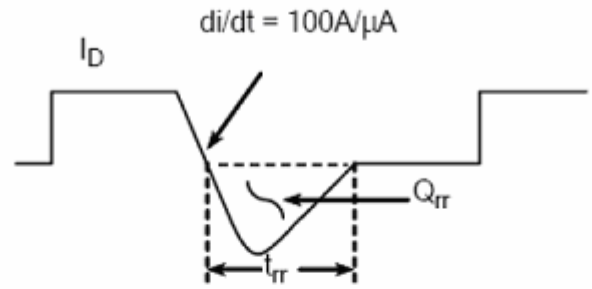


**Resistive Switching Waveforms**

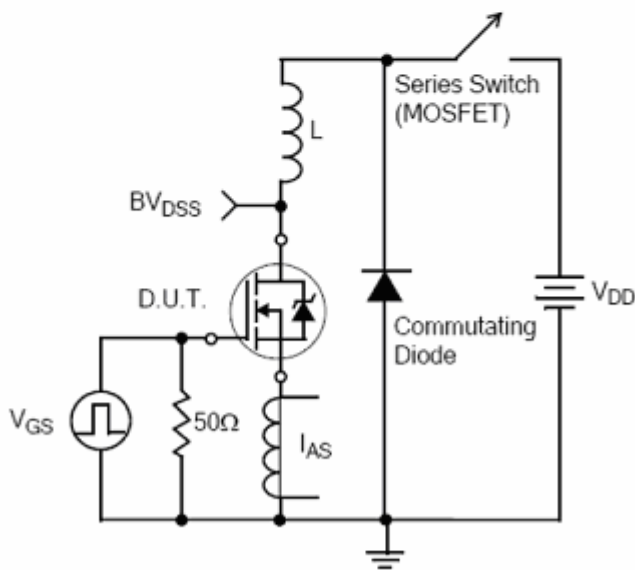
**Test Circuit and Waveforms(Cont.)**



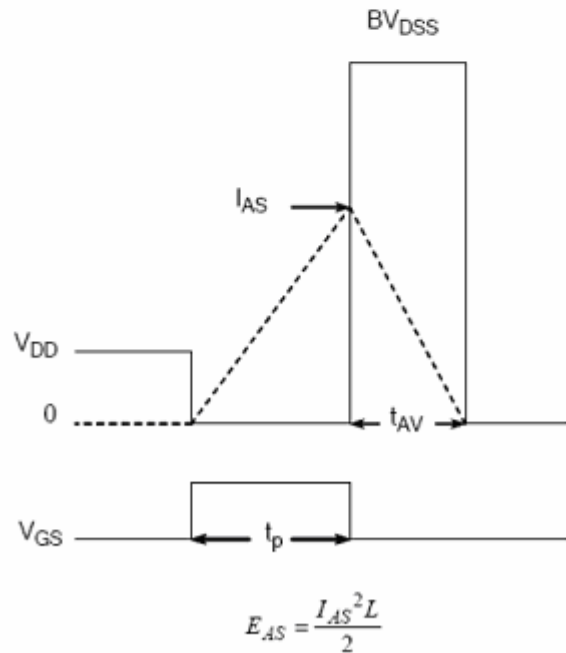
**Diode Reverse Recovery Test Circuit**



**Diode Reverse Recovery Waveform**



**Unclamped Inductive Switching Test Circuit**



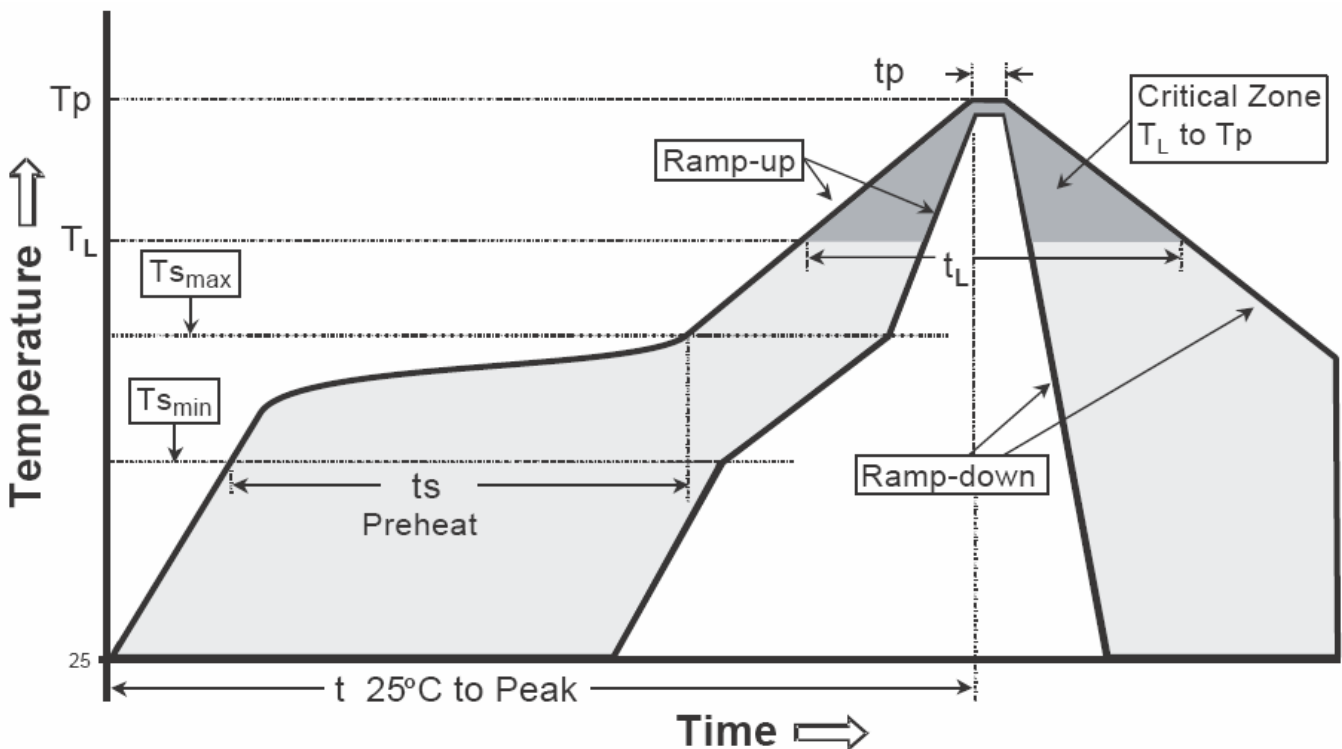
**Unclamped Inductive Switching Waveforms**





**Recommended wave soldering condition**

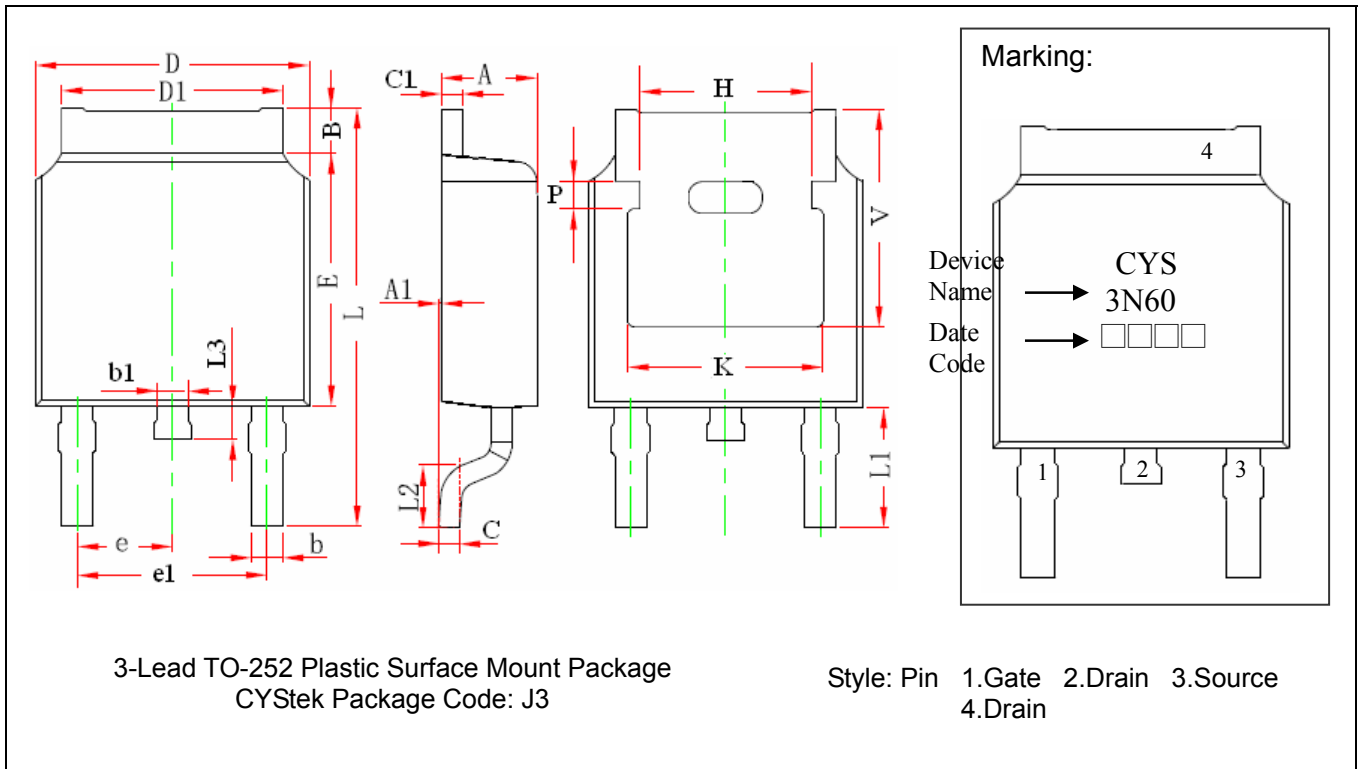
Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

**Recommended temperature profile for IR reflow**


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (Tl)	183°C	217°C
- Time (tl)	60-150 seconds	60-150 seconds
Peak Temperature(Tp)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

**TO-252 Dimension**



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.087	0.094	2.200	2.400	e	0.086	0.094	2.186	2.386
A1	0.000	0.005	0.000	0.127	e1	0.172	0.188	4.372	4.772
B	0.039	0.048	0.990	1.210	H	0.163	REF	4.140	REF
b	0.026	0.034	0.660	0.860	K	0.190	REF	4.830	REF
b1	0.026	0.034	0.660	0.860	L	0.386	0.409	9.800	10.400
C	0.018	0.023	0.460	0.580	L1	0.114	REF	2.900	REF
C1	0.018	0.023	0.460	0.580	L2	0.055	0.067	1.400	1.700
D	0.256	0.264	6.500	6.700	L3	0.024	0.039	0.600	1.000
D1	0.201	0.215	5.100	5.460	P	0.026	REF	0.650	REF
E	0.236	0.244	6.000	6.200	V	0.211	REF	5.350	REF

Notes: 1.Controlling dimension: millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead : Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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