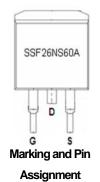


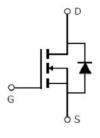
Main Product Characteristics

V _{DSS}	600V
R _{DS} (on)	0.135Ω(typ.)
I _D	20A



D2PAK





Schematic Diagram

Features and Benefits

- High dv/dt and avalanche capabilities
- 100% avalanche tested
- Low input capacitance and gate charge
- Low gate input resistance



Description

The SSF26NS60A series MOSFETs is a new technology, which combines an innovative technology and advance process. This new technology achieves low Rdson, energy saving, high reliability and uniformity, superior power density and space saving.

Absolute Max Rating

Symbol	Parameter	Max.	Units	
I _D @ TC = 25°C	Continuous Drain Current, V _{GS} @ 10V①	20		
I _D @ TC = 100°C	Continuous Drain Current, V _{GS} @ 10V①	13	Α	
I _{DM}	Pulsed Drain Current②	80		
D @TC = 25°C	Power Dissipation③	208	W	
P _D @TC = 25°C	Linear Derating Factor	1.66	W/°C	
V _{DS}	Drain-Source Voltage	600	V	
V _{GS}	Gate-to-Source Voltage	± 30	V	
E _{AS}	Single Pulse Avalanche Energy @ L=13.8mH		mJ	
I _{AS}	Avalanche Current @ L=13.8mH	6	Α	
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to + 150	°C	



Thermal Resistance

Symbol	Characteristics	Тур.	Max.	Units
$R_{ heta JC}$	Junction-to-case③	_	0.6	°C/W
$R_{\theta JA}$	Junction-to-ambient (t $\leq 10s$) (4)	_	62	°C/W

Electrical Characteristics $@T_A=25^{\circ}C$ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions	
V _{(BR)DSS}	Drain-to-Source breakdown voltage	600	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$	
ר	Static Ducin to Course on registance	_	0.135	0.165		V _{GS} =10V,I _D = 10A	
$R_{DS(on)}$	Static Drain-to-Source on-resistance	_	0.31	_	Ω	T _J = 125°C	
\/	Cata threads and walters	2	_	4	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
$V_{GS(th)}$	Gate threshold voltage	_	2.54	_	V	T _J = 125°C	
1	Dunin to Course leakens aument	_	_	1		V _{DS} = 600V,V _{GS} = 0V	
I _{DSS}	Drain-to-Source leakage current	_	_	50	μA	T _J = 125°C	
1	Cata ta Cauraa famuand laakana	_	_	100	A	V _{GS} =30V	
I_{GSS}	Gate-to-Source forward leakage	_	_	-100	nA	V _{GS} = -30V	
Qg	Total gate charge	_	52	_		I _D = 20A,	
Q _{gs}	Gate-to-Source charge	_	11	_		V _{DS} =480V,	
Q_{gd}	Gate-to-Drain("Miller") charge	_	25	_		V _{GS} = 10V	
t _{d(on)}	Turn-on delay time	_	15	_		V _{GS} =10V, V _{DS} =300V,	
t _r	Rise time	_	18	_	nS	$R_L=30\Omega$,	
t _{d(off)}	Turn-Off delay time	_	46	_	113	R_{GEN} =4.7 Ω	
t _f	Fall time	_	16	_		I _D =10A	
C _{iss}	Input capacitance	_	1474	_		V _{GS} = 0V	
Coss	Output capacitance	_	149	_	pF	V _{DS} = 50V	
C _{rss}	Reverse transfer capacitance	_	4	_		f = 1MHz	

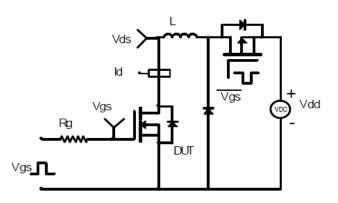
Source-Drain Ratings and Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
1	Continuous Source Current		_	20	Α	MOSFET symbol
I _S	(Body Diode)	_				showing the
I _{SM}	Pulsed Source Current	_	_	80	А	integral reverse
	(Body Diode)					p-n junction diode.
V _{SD}	Diode Forward Voltage	_	0.88	1.3	V	I _S =20A, V _{GS} =0V
t _{rr}	Reverse Recovery Time	_	370	_	nS	$T_J = 25^{\circ}C$, $I_F = 20A$, $di/dt =$
Q _{rr}	Reverse Recovery Charge	_	5	_	uC	100A/μs

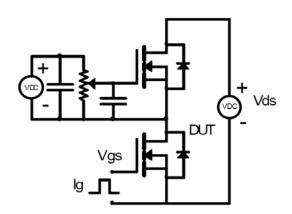


Test circuits and Waveforms

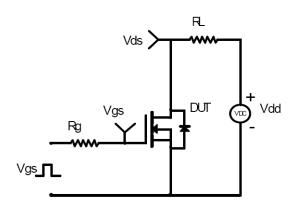
EAS Test Circuit



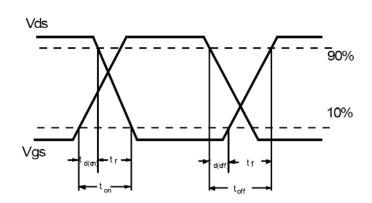
Gate charge test circuit



Switching Time Test Circuit



Switching Waveforms

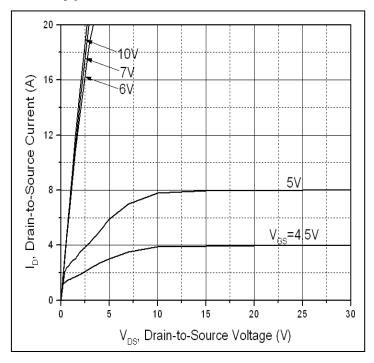


Notes:

- ①Calculated continuous current based on maximum allowable junction temperature.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- ③The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.
- 4The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with TA =25°C



Typical electrical and thermal characteristics



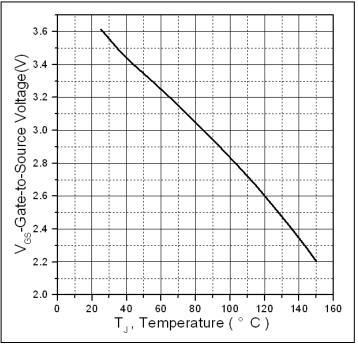


Figure 1: Typical Output Characteristics

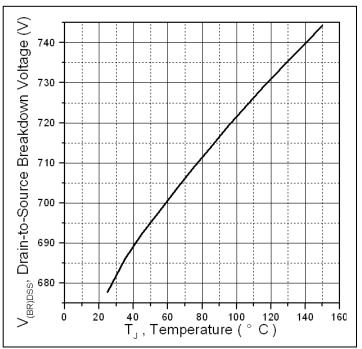


Figure 2. Gate to source cut-off voltage

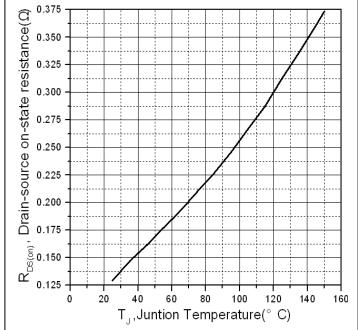


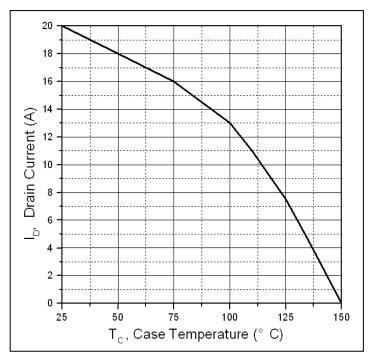
Figure 3. Drain-to-Source Breakdown Voltage vs.
Temperature

Figure 4: Normalized On-Resistance Vs. Case Temperature





Typical electrical and thermal characteristics



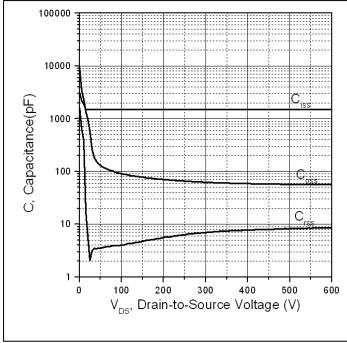


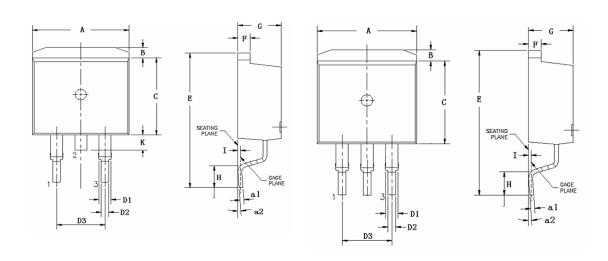
Figure 5. Maximum Drain Current Vs. Case Temperature

Figure 6.Typical Capacitance Vs. Drain-to-Source Voltage



Mechanical Data:

TO263 PACKAGE OUTLINE DIMENSION



Cumb ol	Dimension I	n Millimeters	Dimension In Inches		
Symbol	Min	Max	Min	Max	
Α	9.660	10.280	0.380	0.405	
В	1.020	1.320	0.040	0.052	
С	8.590	9.400	0.338	0.370	
D1	1.140	1.400	0.045	0.055	
D2	0.700	0.950	0.028	0.037	
D3	5.080	(TYP)	0.200 (TYP)		
E	15.090	15.390	0.594	0.606	
F	1.150	1.400	0.045	0.055	
G	4.300	4.700	0.169	0.185	
Н	2.290	2.790	0.090	0.110	
I	0.250 (TYP)		0.010	(TYP)	
K	1.300	1.600	0.051	0.063	
a1	0.450	0.650	0.018	0.026	
a2	00	8 ⁰	1 ⁰	8 ⁰	





Ordering and Marking Information

Device Marking: SSF26NS60A

Package (Available)
TO-263(D2PAK)
Operating Temperature Range
C: -55 to150 °C

Devices per Unit

Package	Units/Tube	Tubes/Inner	Units/Inner	Inner	Units/
Type		Box	Box	Boxes/Carton	Carton
				Box	Box
D2PAK	50	20	1000	6	6000

Reliability Test Program

Test Item	Conditions	Duration	Sample Size
High	T _j =150℃ @ 80% of	168 hours	3 lots x 77 devices
Temperature	Max V _{DSS} /V _{CES} /VR	500 hours	
Reverse		1000 hours	
Bias(HTRB)			
High	T _j =150℃ @ 100% of	168 hours	3 lots x 77 devices
Temperature	Max V _{GSS}	500 hours	
Gate		1000 hours	
Bias(HTGB)			





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