

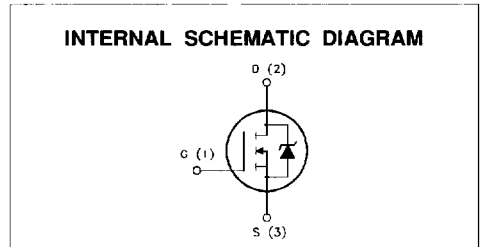
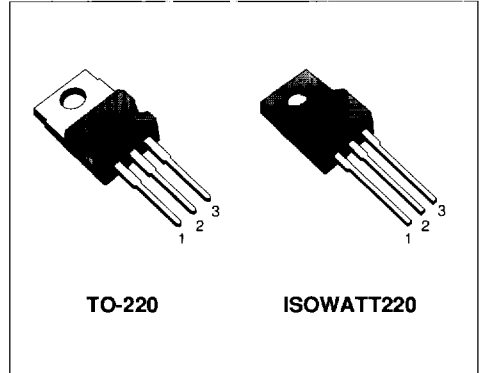
N - CHANNEL ENHANCEMENT MODE POWER MOS TRANSISTORS

TYPE	V _{DSS}	R _{DS(on)}	I _D
STP5N80	800 V	2 Ω	5.5 A
STP5N80FI	800 V	2 Ω	3.1 A

- AVALANCHE RUGGEDNESS TECHNOLOGY
- 100% AVALANCHE TESTED
- REPETITIVE AVALANCHE DATA AT 100°C
- LOW INPUT CAPACITANCE
- LOW GATE CHARGE
- LOW LEAKAGE CURRENT SPECIFIED AT MAXIMUM VOLTAGE
- "DIPS" EDGE TERMINATION TO SUSTAIN HIGH VOLTAGE
- APPLICATION ORIENTED CHARACTERIZATION
- ISOLATED PACKAGE UL RECOGNIZED, ISOLATION TO 2000V DC

APPLICATIONS

- HIGH CURRENT, HIGH SPEED SWITCHING
- SWITCH MODE POWER SUPPLIES (SMPS)
- CONSUMER AND INDUSTRIAL LIGHTING
- DC-AC INVERTERS FOR WELDING EQUIPMENT AND UNINTERRUPTIBLE POWER SUPPLY (UPS)



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value		Unit
		STP5N80	STP5N80FI	
V _{DS}	Drain-source Voltage (V _{GS} = 0)	800		V
V _{DGR}	Drain- gate Voltage (R _{GS} = 20 kΩ)	800		V
V _{GS}	Gate-source Voltage	± 20		V
I _D	Drain Current (continuous) at T _c = 25 °C	5.5	3.1	A
I _D	Drain Current (continuous) at T _c = 100 °C	3.4	2	A
I _{DM} (*)	Drain Current (pulsed)	20	20	A
P _{tot}	Total Dissipation at T _c = 25 °C	125	40	W
	Derating Factor	1	0.32	W/°C
T _{stg}	Storage Temperature	-65 to 150		°C
T _j	Max. Operating Junction Temperature	150		°C

(*) Pulse width limited by safe operating area

THERMAL DATA

			TO-220	ISOWATT220	
$R_{th-j-case}$	Thermal Resistance Junction-case	Max	1	3.12	°C/W
R_{th-amb}	Thermal Resistance Junction-ambient	Max		62.5	°C/W
$R_{th-c-sink}$	Thermal Resistance Case-sink	Typ		0.5	°C/W
T_l	Maximum Lead Temperature For Soldering Purpose			300	°C

AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I_{AR}	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T_J max, $\delta < 1\%$)	5.5	A
E_{AS}	Single Pulse Avalanche Energy (starting $T_J = 25\text{ °C}$, $I_D = I_{AR}$, $V_{DD} = 50\text{ V}$)	320	mJ
E_{AR}	Repetitive Avalanche Energy (pulse width limited by T_J max, $\delta < 1\%$)	16	mJ
I_{AR}	Avalanche Current, Repetitive or Not-Repetitive ($T_c = 100\text{ °C}$, pulse width limited by T_J max, $\delta < 1\%$)	3.2	A

ELECTRICAL CHARACTERISTICS ($T_{case} = 25\text{ °C}$ unless otherwise specified)

OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{(BR),DSS}$	Drain-source Breakdown Voltage	$I_D = 250\text{ }\mu\text{A}$ $V_{GS} = 0$	800			V
I_{DSS}	Zero Gate Voltage Drain Current ($V_{GS} = 0$)	$V_{DS} = \text{Max Rating}$ $V_{DS} = \text{Max Rating} \times 0.8$ $T_c = 125\text{ °C}$			250 1000	μA μA
I_{GSS}	Gate-body Leakage Current ($V_{DS} = 0$)	$V_{GS} = \pm 20\text{ V}$			± 100	nA

ON (*)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$ $I_D = 250\text{ }\mu\text{A}$	2		4	V
$R_{DS(on)}$	Static Drain-source On Resistance	$V_{GS} = 10\text{ V}$ $I_D = 2.5\text{ A}$ $V_{GS} = 10\text{ V}$ $I_D = 2.5\text{ A}$ $T_c = 100\text{ °C}$			2 4	Ω Ω
$I_{D(on)}$	On State Drain Current	$V_{DS} > I_{D(on)} \times R_{DS(on)max}$ $V_{GS} = 10\text{ V}$	5			A

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$g_{fs} (*)$	Forward Transconductance	$V_{DS} > I_{D(on)} \times R_{DS(on)max}$ $I_D = 2.5\text{ A}$	2			S
C_{iss}	Input Capacitance	$V_{DS} = 25\text{ V}$ $f = 1\text{ MHz}$ $V_{GS} = 0$			1400	pF
C_{oss}	Output Capacitance				200	pF
C_{rss}	Reverse Transfer Capacitance				85	pF

ELECTRICAL CHARACTERISTICS (continued)

SWITCHING ON

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on Time	$V_{DD} = 400\text{ V}$ $I_D = 2.5\text{ A}$		50	65	ns
t_r	Rise Time	$R_G = 50\ \Omega$ $V_{GS} = 10\text{ V}$ (see test circuit, figure 3)		85	105	ns
$(di/dt)_{on}$	Turn-on Current Slope	$V_{DD} = 640\text{ V}$ $I_D = 5.5\text{ A}$ $R_G = 50\ \Omega$ $V_{GS} = 10\text{ V}$ (see test circuit, figure 5)		200		A/ μs
Q_g	Total Gate Charge	$V_{DD} = 500\text{ V}$ $I_D = 6\text{ A}$ $V_{GS} = 10\text{ V}$		75	95	nC

SWITCHING OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
t_{rVoff}	Off-voltage Rise Time	$V_{DD} = 640\text{ V}$ $I_D = 5.5\text{ A}$		120	150	ns
t_f	Fall Time	$R_G = 50\ \Omega$ $V_{GS} = 10\text{ V}$ (see test circuit, figure 5)		30	40	ns
t_c	Cross-over Time			160	200	ns

SOURCE DRAIN DIODE

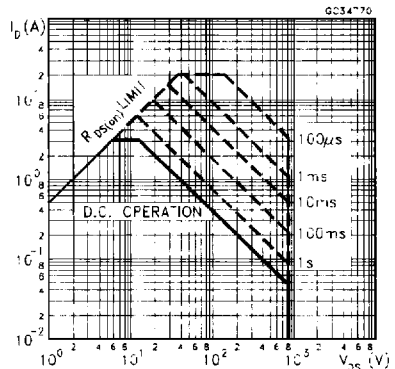
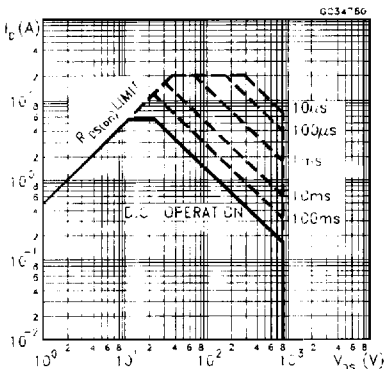
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{SD}	Source drain Current				5.5	A
$I_{SDM}^{(*)}$	Source drain Current (pulsed)				20	A
$V_{SD}^{(*)}$	Forward On Voltage	$I_{SD} = 5.5\text{ A}$ $V_{GS} = 0$			2	V
t_{rr}	Reverse Recovery Time	$I_{SD} = 5.5\text{ A}$ $di/dt = 100\text{ A}/\mu\text{s}$ $V_{DD} = 80\text{ V}$ $T_J = 150\text{ }^\circ\text{C}$		700		ns
Q_{rr}	Reverse Recovery Charge	(see test circuit, figure 5)		7.7		μC
I_{RRM}	Reverse Recovery Current			22		A

(*) Pulsed. Pulse duration = 300 μs , duty cycle 1.5 %

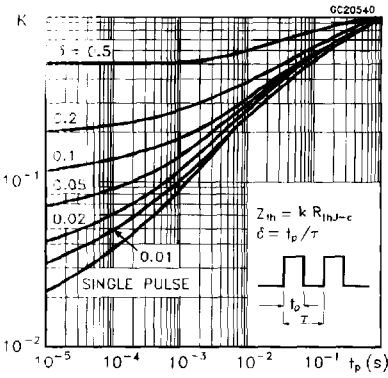
(*) Pulse width limited by safe operating area

Safe Operating Areas For TO-220

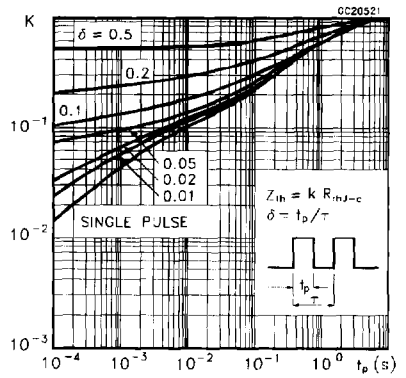
Safe Operating Areas For ISOWATT220



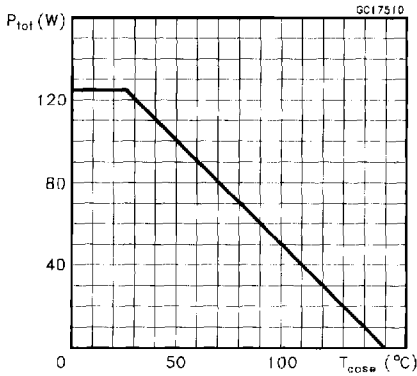
Thermal Impedance For TO-220



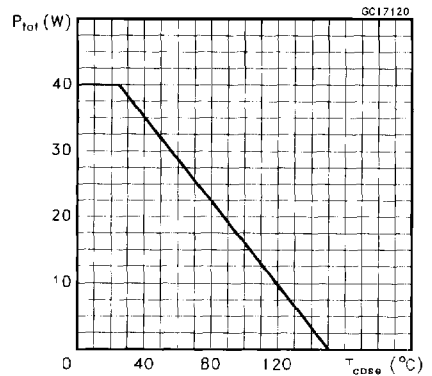
Thermal Impedance For ISOWATT220



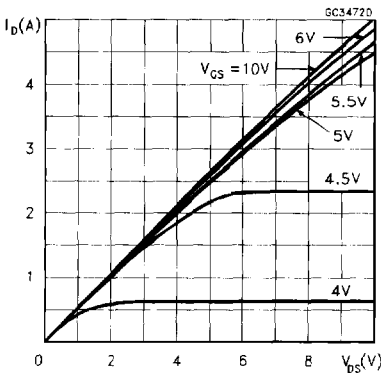
Derating Curve For TO-220



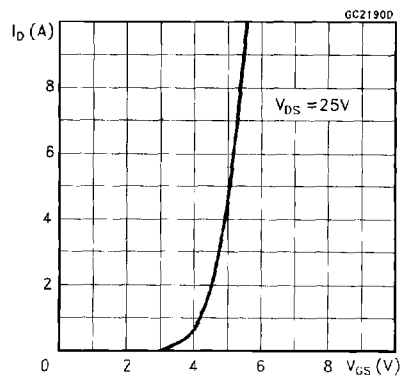
Derating Curve For ISOWATT220



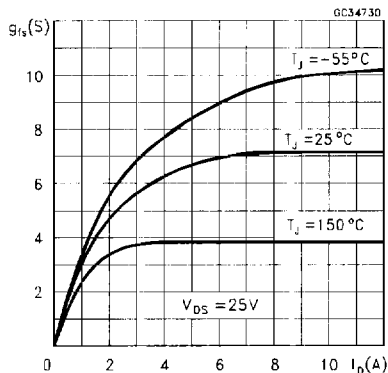
Output Characteristics



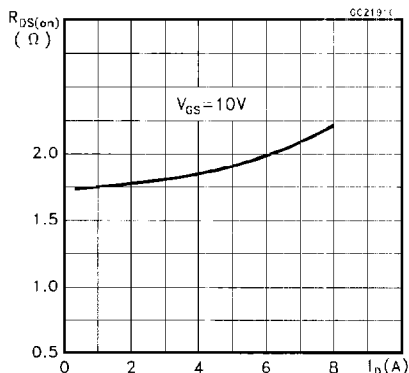
Transfer Characteristics



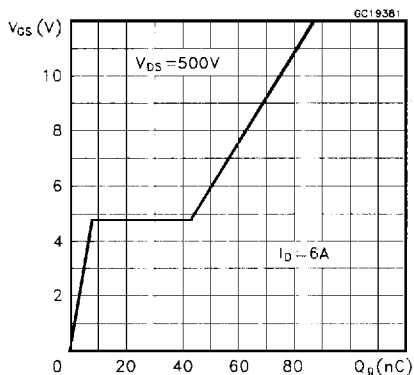
Transconductance



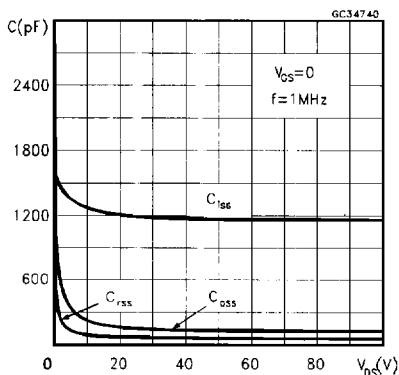
Static Drain-source On Resistance



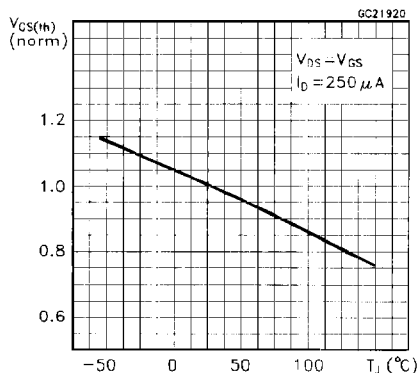
Gate Charge vs Gate-source Voltage



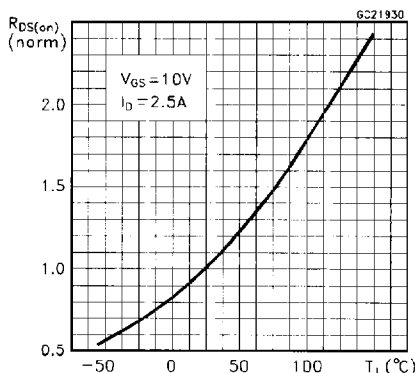
Capacitance Variations



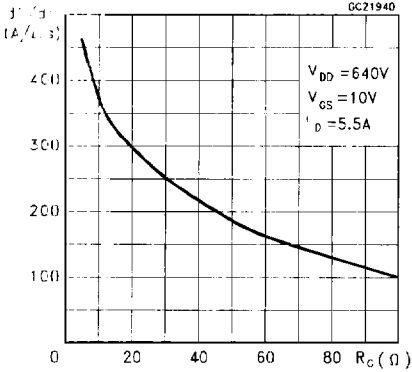
Normalized Gate Threshold Voltage vs Temperature



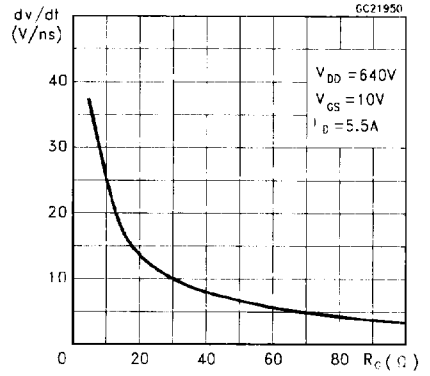
Normalized On Resistance vs Temperature



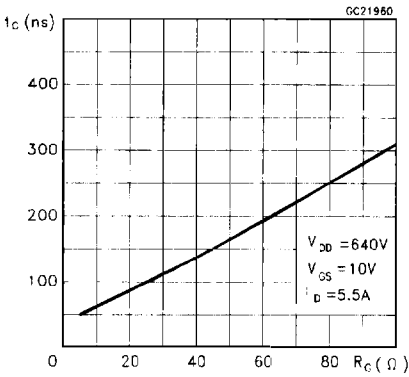
Turn-on Current Slope



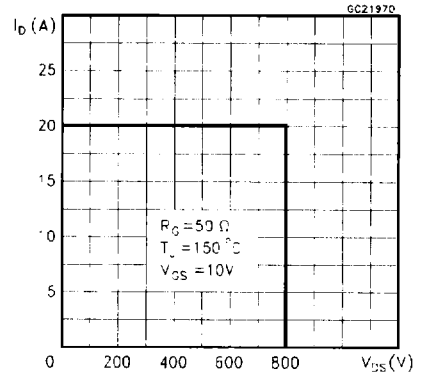
Turn-off Drain-source Voltage Slope



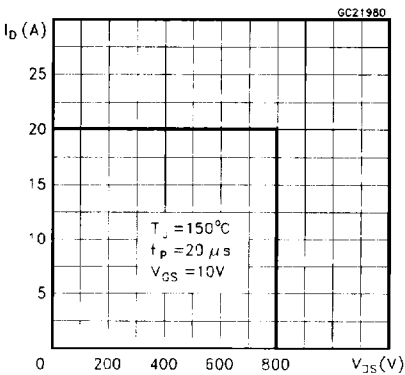
Cross-over Time



Switching Safe Operating Area



Accidental Overload Area



Source-drain Diode Forward Characteristics

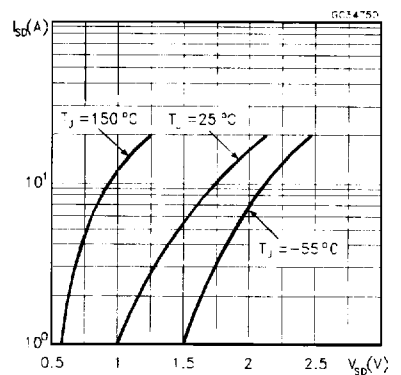


Fig. 1: Unclamped Inductive Load Test Circuits

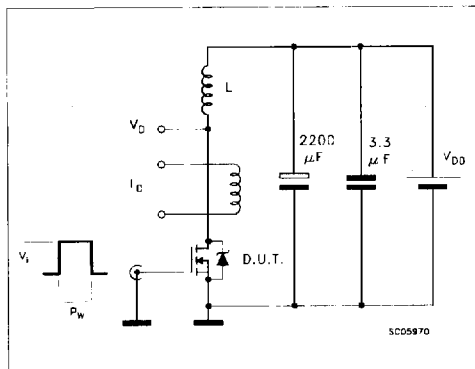


Fig. 2: Unclamped Inductive Waveforms

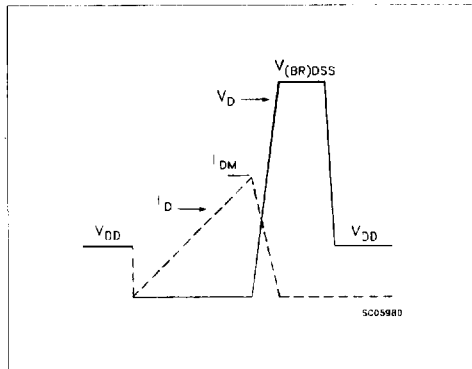


Fig. 3: Switching Times Test Circuits For Resistive Load

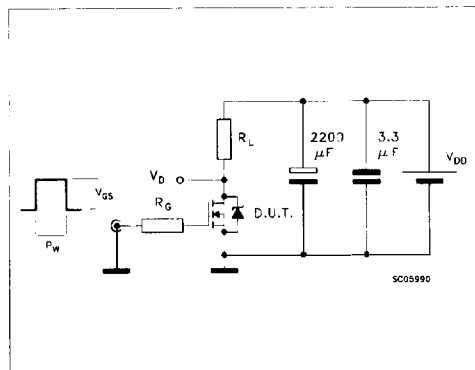


Fig. 4: Gate Charge Test Circuit

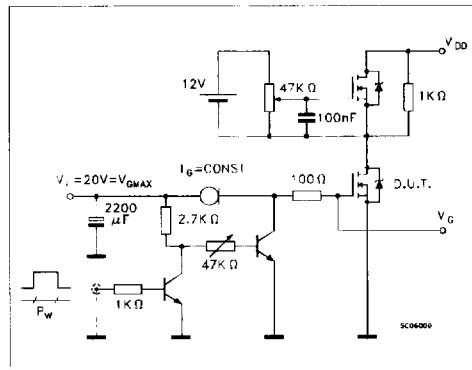


Fig. 5: Test Circuit For Inductive Load Switching And Diode Reverse Recovery Time

