

1200 V / 225 A / 13.5 m Ω

APE HT-2102-A

High-Temperature Silicon Carbide (SiC) Half-Bridge Power Module

N-Channel MOSFET Version

FEATURES

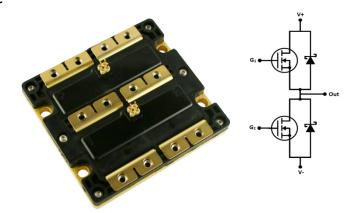
• High temperature: T_{c(max)} = 225 °C

 $T_{J(max)} = 225 \, ^{\circ}C$

- AS9100:Rev. C-certified manufacturing, traceable throughout value chain
- Ultra-fast switching (<30 ns), low inductance
- Enables high system efficiency
- Low profile, small form factor

APPLICATIONS

- High-efficiency converters / inverters
- Motor drives
- Aerospace: Military & Commercial
- Smart grid/grid-tie distributed generation
- Industrial and automotive traction drives



DESCRIPTION

The APE HT-2102-A Silicon Carbide (SiC) half-bridge power module was designed specifically to address the growing demand for higher power densities, higher temperatures, and higher switching frequencies.

COMPANION PARTS

Maximum performance may be obtained through use of the companion high-temperature gate driver, part number APE MTGD2-2011, designed especially for driving the Silicon Carbide module.

Power Module Absolute Maximum Ratings (T _C = 25 °C unless otherwise specified)							
Symbol	Parameter	Condition(s)	Value	Units			
V_{DSS}	Drain-source voltage		1200	V			
V_{GSS}	Gate-source voltage		-5 to 20	V			
		T _C = 25 °C	225				
I_D	Continuous drain current	T _C = 100 °C	175	Α			
		T _C = 200 °C	80				
I _{DM}	Peak pulsed drain current	Pulse width ≤ 10 μs, duty cycle ≤ 2%	TBD	Α			
P_D	Maximum power dissipated		1600	W			
T _{C(max)}	Maximum case temperature ¹		225	°C			
T _{J(min)}	Minimum operating junction temperature		- 50	°C			
T _{J(max)}	Maximum operating junction temperature		225] ''			
T_{stg}	Storage temperature		- 50 to 225	°C			
V_{isol}	Insulation test voltage	AC, 1 min.	TBD	V			
	Insulation test voltage	AC, 1 s.		V			

¹The packaging materials have been qualified at this temperature.



APE HT-2102-A

Switch Po	sition Electrical Characteristics (To	= 25 °C unless otherwise specified)				
Compleale	Do no no orbani	0 - 111 - 11	Values			
Symbols	Parameter	Condition(s)	Min.	Typical	Max.	Units
$V_{(BR)DSS}$	Drain-source breakdown voltage	V _{GS} = 0 V, I _D = 1 mA	1200	-	-	V
V	Cata source threshold voltage	$V_{DS} = V_{GS}$, $I_D = 1 \text{ mA}$	-	2.1	-	V
$V_{GS(th)}$	Gate-source threshold voltage	$V_{DS} = V_{GS}$, $I_{D} = 1$ mA, $T_{j} = 205$ °C	-	1.1	-	
	Due in comment	V _{GS} = - 5 V, V _{DS} = 1200 V	-	-	200	μΑ
I _{DSS}	Drain-source leakage current	$V_{GS} = -5 \text{ V}, V_{DS} = 1200 \text{ V}, T_j = 205 ^{\circ}\text{C}$	-	-	2000	
I _{GSS}	Gate-source leakage current	V _{GS} = 20 V, V _{DS} = 0 V	-	-	250	nA
R _{DS(on)} Dr	Drain-source turn-on resistance	V _{GS} = 20 V, I _D = 75 A	-	13.5	-	mΩ
		$V_{GS} = 20 \text{ V}, I_D = 75 \text{ A}, T_i = 225 ^{\circ}\text{C}$	-	26.6	-	
C _{iss}	Input capacitance	$V_{GS} = 0 \text{ V}$	-	11,490	-	
Coss	Output capacitance	V _{DS} = 800 V	-	720	-	рF
C _{rss}	Reverse transfer capacitance	f = 1 MHz	-	78	-	
t _{d(on)}	Turn-on delay time	V 600 V V 41 20 V	-	36	-	
t _{rv}	Rise time	$V_{DD} = 600 \text{ V}, V_{GS} = -4 \text{ to } 20 \text{ V}$ $I_{D} = 120 \text{ A}$ $R_{G(ext)} = 0 \Omega, R_{L} = 60 \Omega$	-	20	-]
t _{d(off)}	Turn-off delay time		-	68	-	ns
t _{fv}	Fall time		-	25	-]

Switch Position Gate Charge Electrical Characteristics (T _C = 25 °C unless otherwise specified)								
Cumbala	Parameter	Condition(s)	Values			Linita		
Symbols			Min.	Typical	Max.	Units		
Q _{gs}	Gate to source charge	$V_{DD} = 800 \text{ V}, V_{GS} = -4 \text{ to } 20 \text{ V}$	-	143	-			
Q_{gd}	Gate to drain charge	I _D = 120 A	-	260	-	nC		
Qg	Gate charge total		-	545	-			

Diode Pos	Diode Position Electrical Characteristics (T _C = 25 °C unless otherwise specified)						
Symbols	Parameter	Condition(s)	Values			I I to i to	
		Condition(s)	Min.	Typical	Max.	Units	
V _{FM}	Forward voltage	I _F = 60 A	-	1.65	-	V	
		I _F = 60 A, T _J = 200 °C	-	2.5	-		
I _R	David and a summer of	V _R = 1200 V	-	TBD	-		
	Reverse current	V _R = 1200 V, T _J = 200 °C	-	TBD	-	μΑ	
Qc	Capacitive charge	V _R = 1200 V, I _F = 120 A, di/dt = 7500	- 780	790		nC	
		A/μs		-	nC		

Thermal Characteristics (T _J = 25 °C unless otherwise specified)							
Symbols	Parameter	Condition(s) Values Min. Typical		Units			
	Parameter		Min.	Typical	Max.	Units	
$R_{\theta(j-c)}$	FET thermal resistance junction-case			0.125		°C/W	



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Power Mo	Power Module Mechanical Characteristics							
Symbols	Parameter	Condition(s)	Values			I I seite		
			Min.	Typical	Max.	Units		
W	Weight			140		g		
Ms	Lead frame mounting torque	6-32 steel screw for lead frame, 10- 32 steel screw for baseplate		40		in∙lb		

TYPICAL PERFORMANCE CURVES

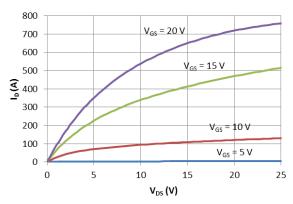


Fig. 1 - Typical Output Characteristics, $T_J = 25$ °C

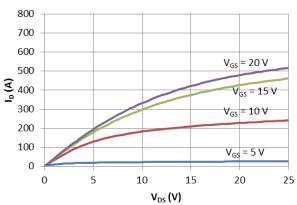


Fig. 2 - Typical Output Characteristics, T₁ = 205°C

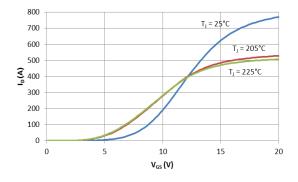


Fig. 3 - Transconductance

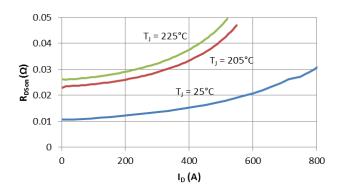


Fig. 4 - Typical On Resistance, V_{GS} = 20 V



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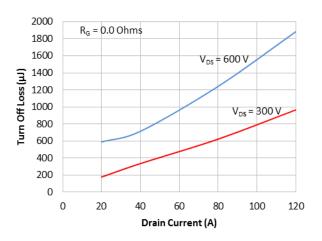


Fig. 5 – Turn off loss versus drain current

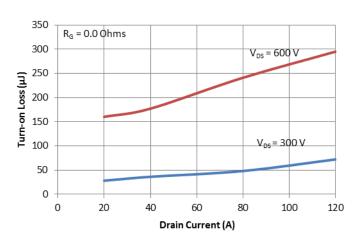


Fig. 6 – Turn on loss versus drain current

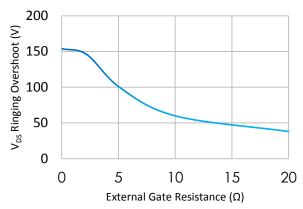


Fig. 7 – Ringing voltage overshoot versus external gate resistance

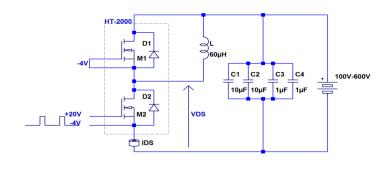


Fig. 8 – Energy values obtained using companion gate driver (T_{amb} = 25 °C).

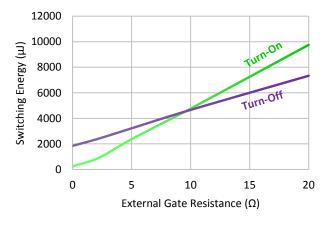


Fig. 9 – Switching energy versus external gate resistance

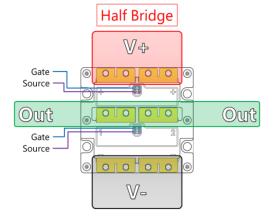


Fig 10 - Half Bridge Connection



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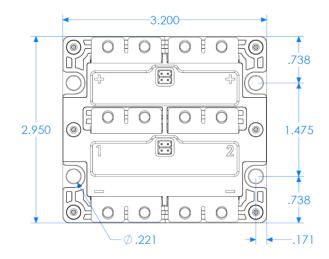
MOUNTING DIMENSIONS

All dimensions are listed in inches

#10-32 bolts are recommended for mounting

A torque of 40 in·lb is recommended

CAD models are available upon request



POWER CONTACT DIMENSIONS

All dimensions are listed in inches

#6-32 bolts required for the power contacts

A torque of 40 in·lb is recommended

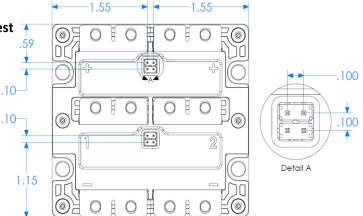
CAD models are available upon request

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GATE DRIVE CONNECTIONS

All dimensions are listed in inches

CAD models are available upon request





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