

## **High Temperature Normally-ON Trench Silicon Carbide Power JFET**

#### **FEATURES**

• High temperature: T<sub>c(max)</sub> = 225 °C

 $T_{i(max)} = 225$  °C

• AS9100:Rev. C-certified manufacturing, traceable throughout value chain

• < 20 ns switching, high system efficiency

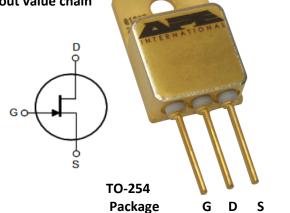
• Hermetic seal; flux free, void free packaging

• Backside isolation

• High reliability

#### **APPLICATIONS**

- Downhole tools
- High efficiency converters
- Motor drives
- Aerospace: Military & Commercial
- Smart grid/grid-tie distributed generation



1200 V / 50 A / 45 mΩ

Absolute Maximum Ratings <sup>1</sup>						
Symbol	Parameter	Condition(s)	Value	Units		
V <sub>DSS</sub>	Drain-source voltage		TBD <sup>3</sup>	٧		
V <sub>GSS</sub>	Gate-source voltage	AC, $R_{G(ext)} = 1 \Omega$ , $t_p \le 200 \text{ ns}$	-15 to 15	V		
		T <sub>c</sub> = 25 °C	50			
I <sub>D</sub>	Continuous drain current <sup>2</sup>	T <sub>c</sub> = 150 °C	TBD <sup>3</sup>	Α		
		T <sub>c</sub> = 225 °C	TBD <sup>3</sup>			
I <sub>DM</sub>	Peak pulsed drain current <sup>2</sup>	Pulse width limited by $T_{j(max)}$ , $T_j = 25$ °C	140	Α		
t <sub>sc</sub>	Short circuit withstand time <sup>2</sup>	V <sub>DD</sub> < 800 V, T <sub>c</sub> < 125 °C	50	μs		
E <sub>AS</sub>	Single-pulse avalanche energy		-	J		
E <sub>AR</sub>	Repetitive avalanche energy		-	J		
I <sub>AR</sub>	Repetitive avalanche current		-	Α		
		T <sub>c</sub> = 25 °C	TBD <sup>3</sup>			
$P_{tot}$	Power dissipation	T <sub>c</sub> = 100 °C	TBD <sup>3</sup>	W		
		T <sub>c</sub> = 225 °C	TBD <sup>3</sup>			
Tj	Operating junction temperature		-50 to 205 <sup>3</sup>	°C		
T <sub>stg</sub>	Storage temperature		-50 to 225 <sup>3</sup>	°C		
V	Insulation test voltage	AC, 1 min.	TBD	V		
V <sub>isol</sub>	Insulation test voltage	AC, 1 s.	TBD	V		

 $<sup>^{\</sup>rm 1}$  Obtained from SemiSouth Laboratories, Inc. SJDC120R045 Rev. 3.0 datasheet

<sup>&</sup>lt;sup>2</sup> As tested for TO-247 package; P/N SJDP120R045

<sup>&</sup>lt;sup>3</sup> Data verified through APEI experimentation and/or calculation



## **PRELIMINARY**

## **APE HT-0102**

	Electrical Characteristics <sup>1</sup> Parameter	Condition(s)	Values			l le !te
Symbols			Min.	Typical	Max.	Units
$V_{(BR)DSS}$	Drain-source breakdown voltage	V <sub>GS</sub> = -15 V, I <sub>D</sub> = 1200 μA	1200	-	-	V
$V_{GS(th)}$	Gate-source threshold voltage	$V_{DS} = 1 \text{ V, } I_{D} = 70 \text{ mA}$	-	-5	-	V
I <sub>DSS</sub>	Zero gate voltage drain current	$V_{DS} = 1200 \text{ V}, V_{GS} = -15 \text{ V}, T_j = 25 ^{\circ}\text{C}$	-	20	-	μА
		$V_{DS} = 1200 \text{ V}, V_{GS} = -15 \text{ V}, T_c = 150 ^{\circ}\text{C}$	-	200	-	
1	Cata source leakage surrent	$V_{GS} = -15 \text{ V}, V_{DS} = 0 \text{ V}$	-	-0.2	-0.6	mΛ
I <sub>GSS</sub>	Gate-source leakage current	$V_{GS} = -15 \text{ V}, V_{DS} = 1200 \text{ V}$	-	-0.2	-	mA
D	Drain-source turn-on resistance	$V_{GS} = 2 \text{ V}, I_D = 30 \text{ A}, T_j = 25 ^{\circ}\text{C}$	-	35	45	mΩ
$R_{DS(on)}$	Drain-source turn-on resistance	$V_{GS} = 2 \text{ V, } I_D = 30 \text{ A, } T_j = 100 ^{\circ}\text{C}$	-	60	-	
I <sub>GFWD</sub>	Gate forward current	V <sub>GS</sub> = 2 V	-	41	-	μΑ
$R_{G}$	Internal gate resistance	f = 1 MHz, drain-source shorted V <sub>GS</sub> > 2.7 V	-	4	-	Ω
R <sub>G(on)</sub>	internal gate resistance		-	0.25	-	
$C_{iss}$	Input capacitance		-	510	-	
$C_{oss}$	Output capacitance	V 100 V V 15 V f - 100 kHz	-	160	-	pF
C <sub>rss</sub>	Reverse transfer capacitance	$V_{DD} = 100 \text{ V}, V_{GS} = -15 \text{ V}, f = 100 \text{ kHz}$	-	160	-	
C <sub>o(er)</sub>	Effective output capacitance, Energy related	V <sub>DS</sub> = 0 V to 600 V, V <sub>GS</sub> = 0 V	-	100	-	
t <sub>d(on)</sub>	Turn-on delay time		-	12	-	
t <sub>rv</sub>	Rise time		-	24	-	
t <sub>d(off)</sub>	Turn-off delay time	.,	-	20	-	ns
t <sub>fv</sub>	Fall time	$V_{DS} = 600 \text{ V}, I_{D} = 30 \text{ A},$	-	22	-	
E <sub>on</sub>	Turn-On energy	Inductive load, $T_j = 25$ °C $V_{GS} = 15$ to -15 V,	-	180	-	
E <sub>off</sub>	Turn-Off energy	$R_{G(ext)} = 2.5 \Omega$	-	200	-	μЈ
E <sub>ts</sub>	Total switching energy		-	380	-	
t <sub>d(on)</sub>	Turn-on delay time	$V_{DS} = 600 \text{ V, } I_D = 30 \text{ A,}$ Inductive load, $T_j = 150 ^{\circ}\text{C}$ $V_{GS} = 15 \text{ to } -15 \text{ V,}$ $R_{G(ext)} = 2.5  \Omega$	-	12	-	
t <sub>rv</sub>	Rise time		-	26	-	
t <sub>d(off)</sub>	Turn-off delay time		-	20	-	ns
t <sub>fv</sub>	Fall time		-	24	-	
E <sub>on</sub>	Turn-On energy		-	185	-	
E <sub>off</sub>	Turn-Off energy		-	215	-	μЈ
E <sub>ts</sub>	Total switching energy	1		400	_	



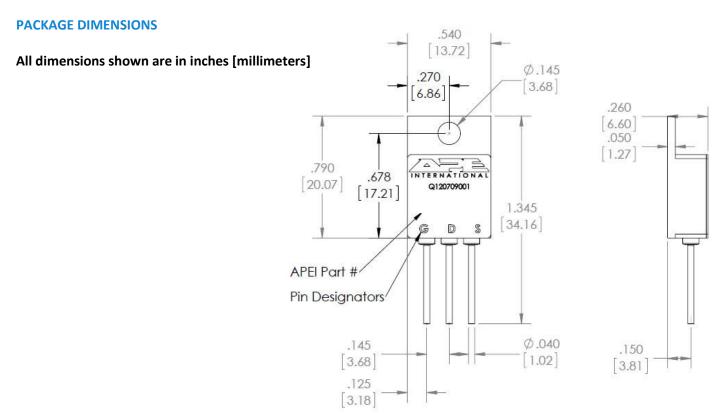
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SiC JFET Gate Charge Electrical Characteristics <sup>1</sup>							
Cumphala	Dougraphou	Condition(s)	Values			Heite	
Symbols	Parameter		Min.	Typical	Max.	Units	
Q <sub>gs</sub>	Gate to source charge	$V_{DS} = 600 \text{ V}, V_{GS} = 2.5 \text{ V}$ $I_D = 15 \text{ A}$	-	4	-		
Q <sub>gd</sub>	Gate to drain charge		-	54	-	nC	
Qg	Gate charge total		-	65	-		

Thermal Characteristics							
Symbols	Parameter	Condition(s) Values			Units		
Symbols	Parameter	condition(s)	Min.	Typical	pical Max.	Units	
$R_{\theta(j-c)}$	Thermal resistance junction-case	Calculated at 200 °C		TBD	1.0	°C/W	

Mechanical Characteristics							
Symbols	Parameter	Condition(s)	Values			l lmita	
	Parameter	Condition(s)	Min.	Typical	Max.	Units	
W	Weight			9.0		g	
Ms	Mounting torque	6-32 steel screw, Al heat sink		0.78	1.04	N-m	



PART NUMBER	PACKAGE	MARKING
APE HT-xxxx	TO-254	Q120709001



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Silicon Carbide Schottky Diode, APE-HT-xxxx Low-Temperature Single-Channel Gate Driver, APE-B110321001 Low-Temperature Dual-Channel Gate Driver, APE-B110629001 High-Temperature Dual-Channel Gate Driver, APE-xxxx

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