

International
IR Rectifier

SCHOTTKY RECTIFIER
HIGH EFFICIENCY SERIES

PD-94035C

30SCLJQ060
30 Amp, 60V

Major Ratings and Characteristics

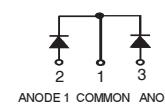
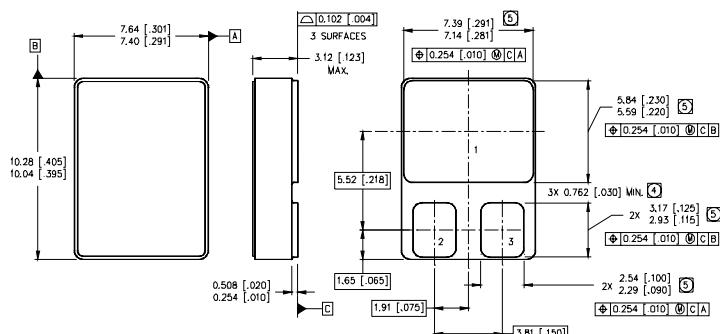
Characteristics	Limits	Units
I _{F(AV)}	30	A
V _{RRM} (Per Leg)	60	V
I _{FSM} @ t _p = 8.3ms half-sine (Per Leg)	120	A
V _F @ 30Apk, T _J = 125°C (Per Leg)	1.01	V
T _J , T _{stg} Operating and storage	-55 to 150	°C

Description/Features

The 30SCLJQ060 center tap Schottky rectifier has been expressly designed to meet the rigorous requirements of HiRel environments. It is packaged in the hermetic surface mount SMD-0.5 ceramic package. The device's forward voltage drop and reverse leakage current are optimized for the lowest power loss and the highest circuit efficiency for typical high frequency switching power supplies and resonant power converters. Full MIL-PRF-19500 quality conformance testing is available on source control drawings to TX, TXV and S quality levels.

- Hermetically Sealed
- Center Tap
- Low Forward Voltage Drop
- High Frequency Operation
- Guard Ring for Enhanced Ruggedness and Long term Reliability
- Surface Mount
- Lightweight
- ESD Rating: Class NS per MIL-STD-750, Method 1020

CASE STYLE



Case Outline and Dimensions - SMD-0.5

Voltage Ratings

Part number	30SCLJQ060		
V_R Max. DC Reverse Voltage (V) (Per Leg)	60		
V_{RWM} Max. Working Peak Reverse Voltage (V) (Per Leg)	60		

Absolute Maximum Ratings

Parameters	Limits	Units	Conditions
$I_{F(AV)}$ Max. Average Forward Current See Fig. 5	30	A	50% duty cycle @ $T_C = 77^\circ\text{C}$, square waveform
I_{FSM} Max. Peak One Cycle Non - Repetitive Surge Current (Per Leg)	120	A	@ $t_p = 8.3 \text{ ms}$ half-sine

Electrical Specifications

Parameters	Limits	Units	Conditions		
V_{FM} Max. Forward Voltage Drop (Per Leg) See Fig. 1 ①	0.86	V	@ 15A	$T_J = -55^\circ\text{C}$	
	1.22	V	@ 30A		
	0.90	V	@ 15A		
	1.24	V	@ 30A	$T_J = 25^\circ\text{C}$	
	0.78	V	@ 15A		
	1.01	V	@ 30A		
I_{RM} Max. Reverse Leakage Current (Per Leg) See Fig. 2 ①	0.045	mA	$T_J = 25^\circ\text{C}$	$V_R = \text{rated } V_R$	
	50	mA	$T_J = 125^\circ\text{C}$		
C_T Max. Junction Capacitance (Per Leg)	600	pF	$V_R = 5\text{V}_{\text{DC}}$ (1MHz, 25°C)		
L_S Typical Series Inductance (Per Leg)	4.8	nH	Measured from center of cathode pad to center of anode pad		

Thermal-Mechanical Specifications

Parameters	Limits	Units	Conditions	
T_J Max. Junction Temperature Range	-55 to 150	°C		
T_{stg} Max. Storage Temperature Range	-55 to 150	°C		
R_{thJC} Max. Thermal Resistance, Junction to Case (Per Leg)	3.5	°C/W	DC operation	See Fig. 4
R_{thPC} Max. Thermal Resistance, Junction to Case (Per Package)	1.75	°C/W	DC operation	
wt Weight (Typical)	1.0	g		
Die Size (Typical)	70X92	mils		
Case Style	SMD-0.5			

① Pulse Width < 300μs, Duty Cycle < 2%

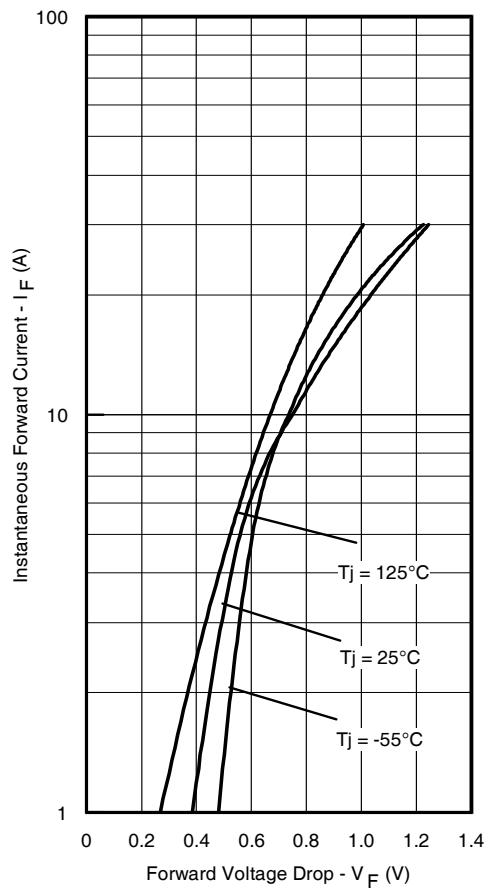


Fig. 1 - Max. Forward Voltage Drop Characteristics
 (Per Leg)

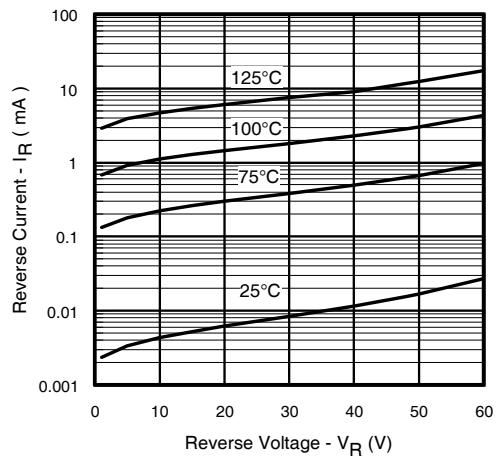


Fig. 2 - Typical Values of Reverse Current
 Vs. Reverse Voltage (Per Leg)

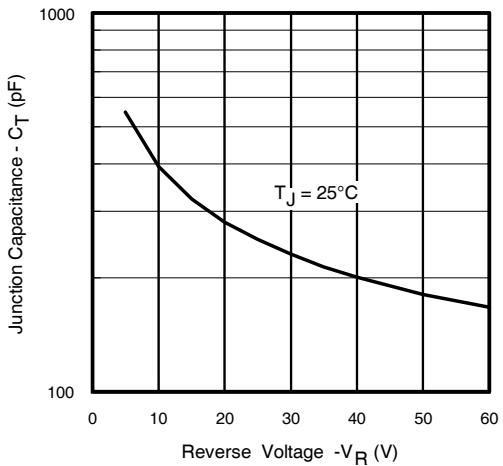
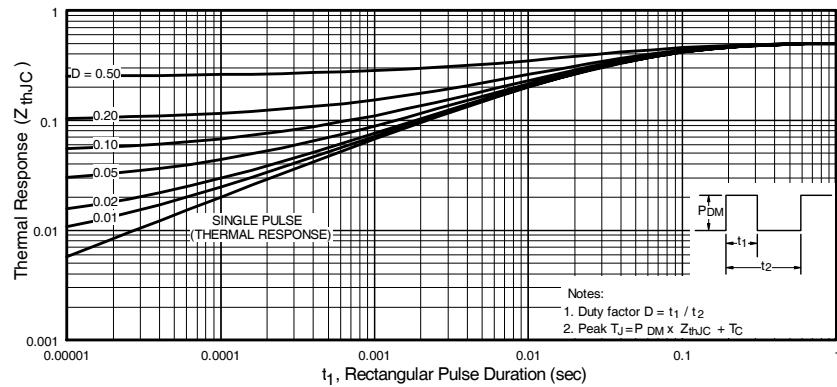
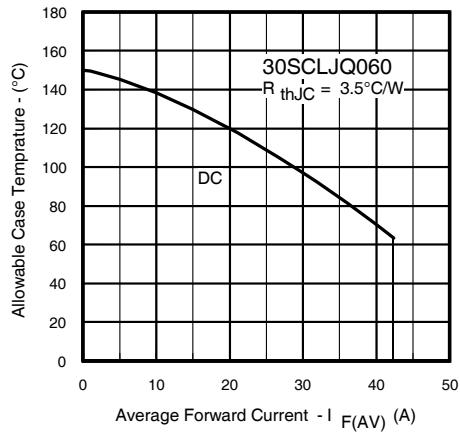


Fig. 3 - Typical Junction Capacitance Vs.
 Reverse Voltage (Per Leg)

Fig. 4 - Max. Thermal Impedance Z_{thJC} Characteristics (Per Leg)Fig. 5 - Max. Allowable Case Temperature Vs.
Average Forward Current (Per Leg)
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Data and specifications subject to change without notice. 02/2013