

General Description

The MDS3754A uses advanced Magnachip's Trench MOSFET Technology to provide high performance in on-state resistance, switching performance and reliability.

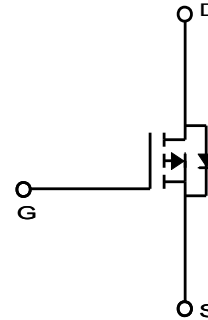
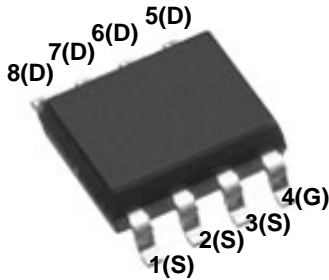
Low $R_{DS(ON)}$, Low Gate Charge can be offering superior benefit in the application.

Features

- $V_{DS} = -40V$
- $I_D = -6.0$ @ $V_{GS} = -10V$
- $R_{DS(ON)} < 45m\Omega$ @ $V_{GS} = -10V$
- $< 60m\Omega$ @ $V_{GS} = -4.5V$

Applications

- Inverters
- General purpose applications



Absolute Maximum Ratings ($T_C = 25^\circ$)

Characteristics	Symbol	Rating	Unit
Drain-Source Voltage	V_{DSS}	-40	V
Gate-Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current (Note 1)	I_D	-6.0	A
Pulsed Drain Current	I_{DM}	-50	A
Power Dissipation	P_D	2.5	W
Single Pulse Avalanche Energy (Note 2)	E_{AS}	32	mJ
Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150	$^\circ C$

Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	50	$^\circ C/W$
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	25	

Ordering Information

Part Number	Temp. Range	Package	Packing	RoHS Status
MDS3754ARH	-55~150°C	SIOC-8	Tape & Reel	Halogen Free

Electrical Characteristics (T_J=25°C unless otherwise noted)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D = -250μA, V _{GS} = 0V	-40	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-1.0	-2.0	-3.0	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -32V, V _{GS} = 0V	-		-1	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V	-	-	±0.1	
Drain-Source ON Resistance	R _{DS(ON)}	V _{GS} = -10V, I _D = -6A	-	34	45	mΩ
		V _{GS} = -4.5V, I _D = -4A		46	60	
Forward Transconductance	g _{FS}	V _{DS} = -10V, I _D = -6A		12	-	S
Dynamic Characteristics						
Total Gate Charge	Q _g	V _{DS} = -28V, I _D = -4.8A, V _{GS} = -10V	-	15.6	-	nC
Gate-Source Charge	Q _{gs}		-	2.4	-	
Gate-Drain Charge	Q _{gd}		-	3.8	-	
Input Capacitance	C _{iss}	V _{DS} = -25V, V _{GS} = 0V, f = 1.0MHz	-	660	-	pF
Reverse Transfer Capacitance	C _{rss}		-	57	-	
Output Capacitance	C _{oss}		-	100	-	
Turn-On Delay Time	t _{d(on)}	V _{GS} = -10V, V _{DS} = -20V, I _D = 1A, R _{GEN} = 3.3Ω	-	14.7	-	ns
Turn-On Rise Time	t _r		-	7.2	-	
Turn-Off Delay Time	t _{d(off)}		-	32.1	-	
Turn-Off Fall Time	t _f		-	12.5	-	
Drain-Source Body Diode Characteristics						
Source-Drain Diode Forward Voltage	V _{SD}	I _S = -6A, V _{GS} = 0V	-	-	1.2	V
Reverse Recovery Time	t _{rr}	I _S = -4.8A, di/dt=100A/us	-	27.2	-	ns
Reverse Recovery Charge	Q _{rr}		-	19.6	-	nC

Note :

1. Surface mounted RF4 board with 2oz. Copper.
2. Starting T_J=25°C, L=1mH, I_{AS}=-8A V_{DD}=-20V, V_{GS}=-10V

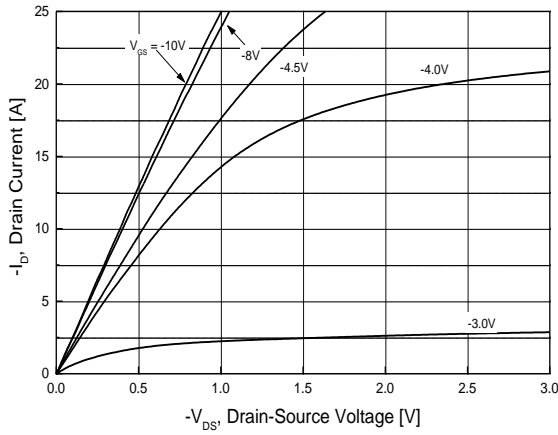


Fig.1 On-Region Characteristics

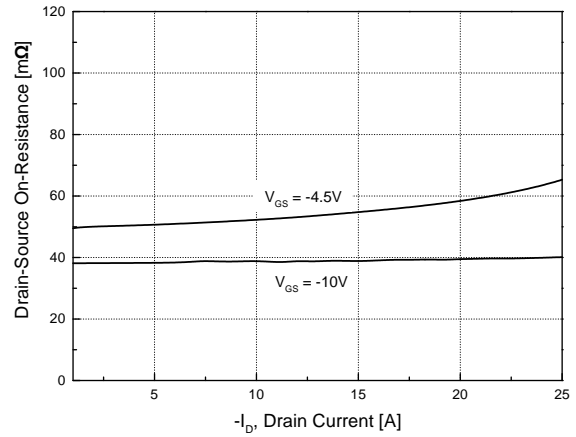


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

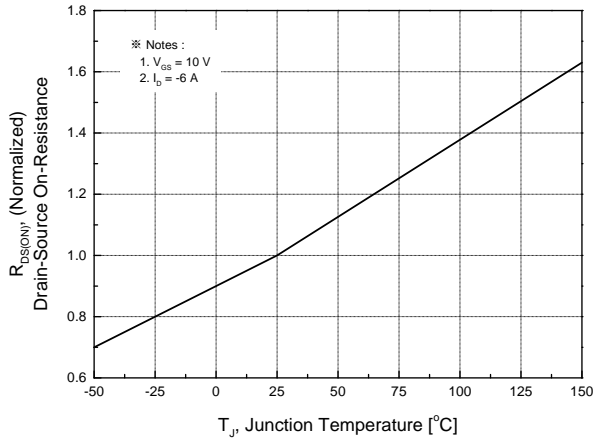


Fig.3 On-Resistance Variation with Temperature

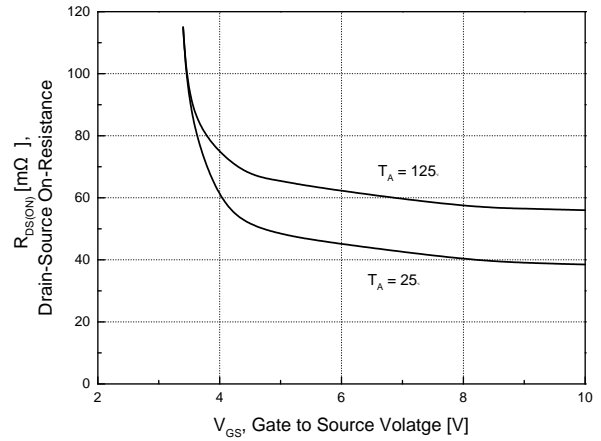


Fig.4 On-Resistance Variation with Gate to Source Voltage

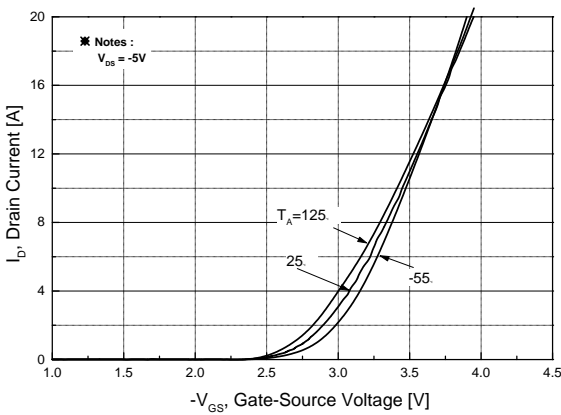


Fig.5 Transfer Characteristics

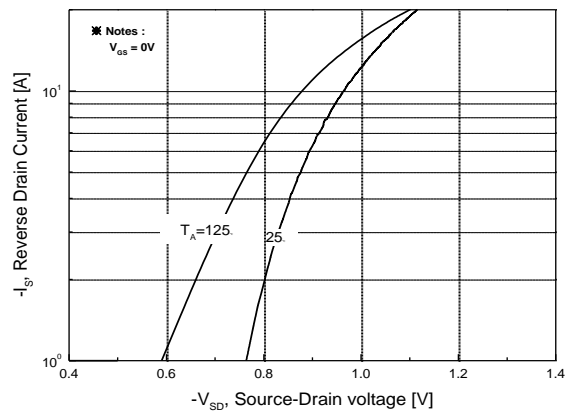


Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature

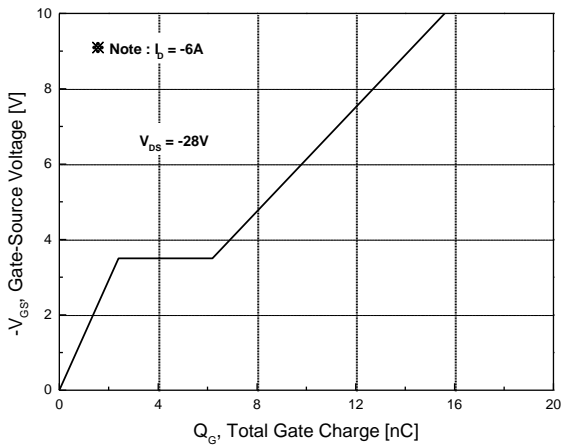


Fig.7 Gate Charge Characteristics

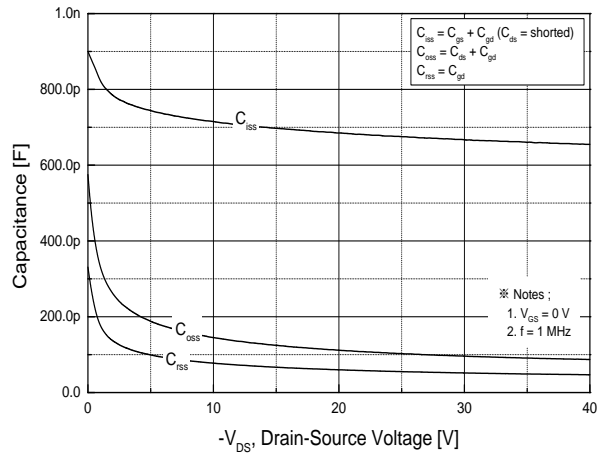


Fig.8 Capacitance Characteristics

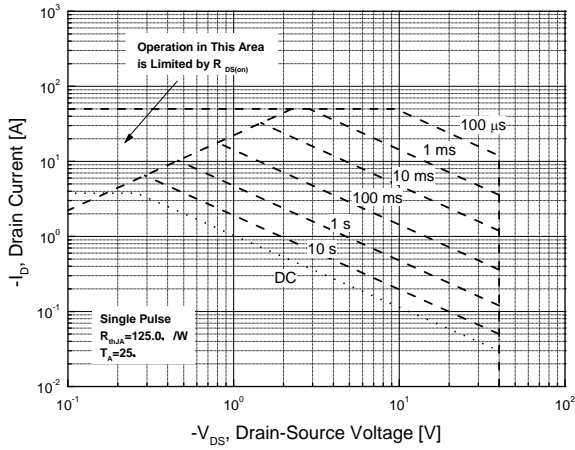


Fig.9 Maximum Safe Operating Area

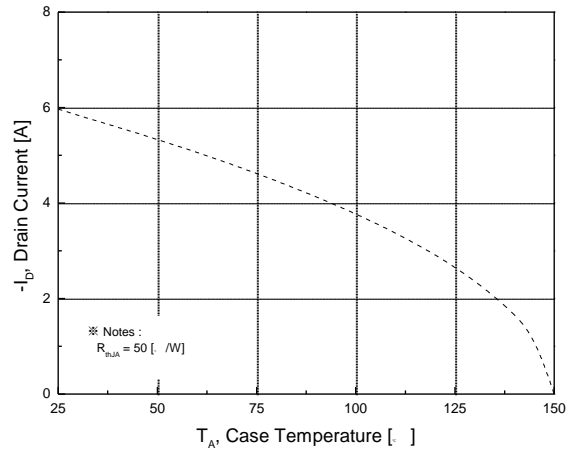


Fig.10 Maximum Drain Current vs. Case Temperature

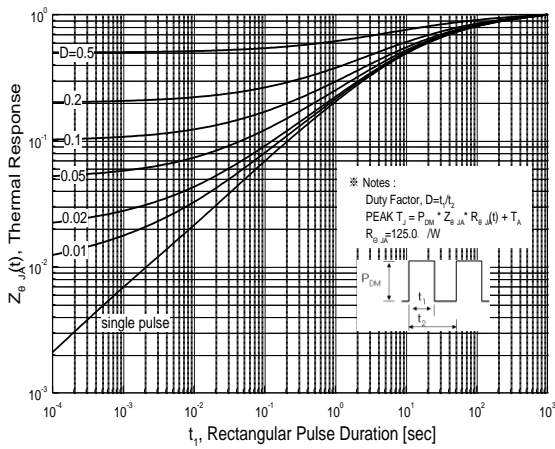
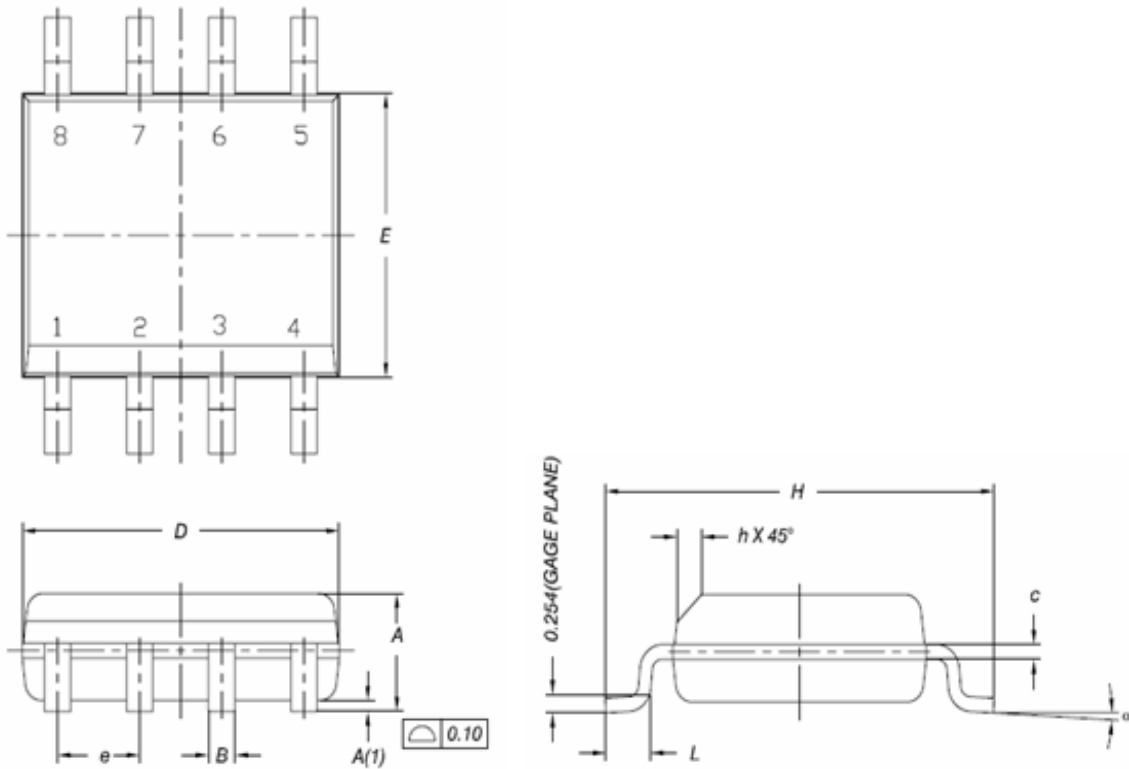


Fig.11 Transient Thermal Response Curve

Physical Dimensions

8 Leads, SOIC

Dimensions are in millimeters unless otherwise specified



Symbol	Min.	Nom.	Max.
A	-	-	1.75
A(1)	0.10	-	0.25
B	0.31	-	0.51
C	0.10	-	0.25
D	-	4.9 BSC	-
E	-	3.9 BSC	-
e	-	1.27BSC	-
H	-	6.0 BSC	-
L	0.40	-	1.27
a	0	-	8
h	0.250	-	0.500
L2(Gage Plane)	0.25 BSC		

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