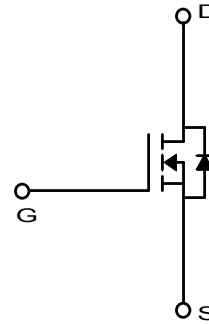
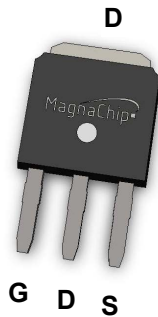


### General Description

The MDIS1502 uses advanced MagnaChip's MOSFET Technology, which provides high performance in on-state resistance, fast switching performance and excellent quality. MDIS1502 is suitable device for DC to DC converter and general purpose applications.

### Features

- $V_{DS} = 30V$
- $I_D = 45.7A @ V_{GS} = 10V$
- $R_{DS(ON) (MAX)} < 8.5m\Omega @ V_{GS} = 10V$   
 $< 13.0m\Omega @ V_{GS} = 4.5V$
- 100% UIL Tested
- 100% Rg Tested



### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-Source Voltage		$V_{DSS}$	30	V
Gate-Source Voltage		$V_{GSS}$	±20	V
Continuous Drain Current <sup>(1)</sup>	$T_C=25^\circ C$	$I_D$	45.7	A
	$T_C=70^\circ C$		36.6	
	$T_A=25^\circ C$		20.4 <sup>(3)</sup>	
	$T_A=70^\circ C$		16.3 <sup>(3)</sup>	
Pulsed Drain Current		$I_{DM}$	100	A
Power Dissipation	$T_C=25^\circ C$	$P_D$	31.2	W
	$T_C=70^\circ C$		20.0	
	$T_A=25^\circ C$		6.2 <sup>(3)</sup>	
	$T_A=70^\circ C$		4.0 <sup>(3)</sup>	
Single Pulse Avalanche Energy <sup>(2)</sup>		$E_{AS}$	47	mJ
Junction and Storage Temperature Range		$T_J, T_{stg}$	-55~150	°C

### Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient <sup>(1)</sup>	$R_{\theta JA}$	20.0	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	4.0	

## Ordering Information

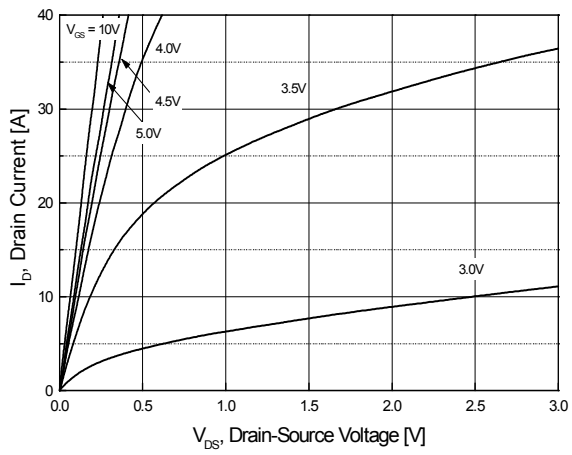
Part Number	Temp. Range	Package	Packing	Quantity	Rohs Status
MDIS1502TH	-55~150°C	TO-251-VS(IPAK)	Tube	75 units /Tube	Halogen Free

## Electrical Characteristics (T<sub>J</sub> =25°C)

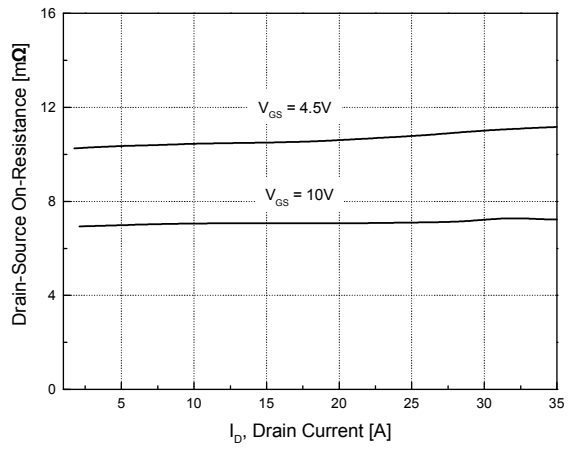
Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	30	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1.3	1.9	2.7	
Drain Cut-Off Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V	-	-	1	μA
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V	-	-	±0.1	
Drain-Source ON Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 16A	-	7.4	8.5	mΩ
		T <sub>J</sub> =125°C	-	10.7	12.3	
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 10A	-	25	-	S
<b>Dynamic Characteristics</b>						
Total Gate Charge	Q <sub>g(10V)</sub>	V <sub>DS</sub> = 15.0V, I <sub>D</sub> = 16A, V <sub>GS</sub> = 10V	10.7	14.3	17.9	nC
Total Gate Charge	Q <sub>g(4.5V)</sub>		5.0	6.7	8.4	
Gate-Source Charge	Q <sub>gs</sub>		-	2.6	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	2.3	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 15.0V, V <sub>GS</sub> = 0V, f = 1.0MHz	696	928	1160	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		68	90	113	
Output Capacitance	C <sub>oss</sub>		132	176	220	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 15.0V, I <sub>D</sub> = 16A, R <sub>G</sub> = 3.0Ω	-	7.2	-	ns
Rise Time	t <sub>r</sub>		-	12.0	-	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	22.8	-	
Fall Time	t <sub>f</sub>		-	8.1	-	
Gate Resistance	R <sub>g</sub>	f=1 MHz	-	3.5	5.0	Ω
<b>Drain-Source Body Diode Characteristics</b>						
Source-Drain Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = 16A, V <sub>GS</sub> = 0V	-	0.8	1.1	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 16A, di/dt = 100A/μs	-	20.4	30.6	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	11.9	17.9	nC

Note :

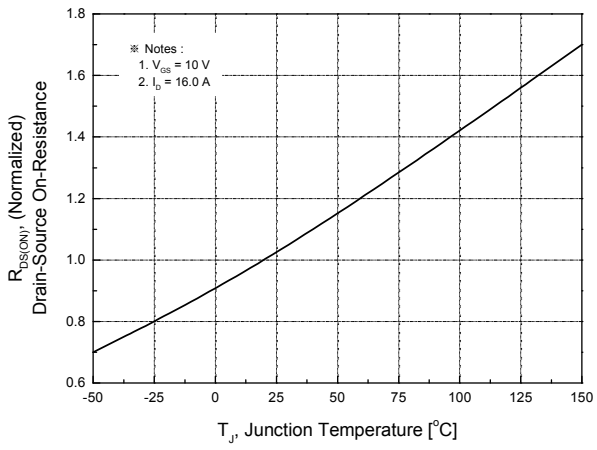
- Surface mounted FR-4 board by JEDEC (jesd51-7)
- E<sub>AS</sub> is tested at starting T<sub>J</sub> = 25°C, L = 0.1mH, I<sub>AS</sub> = 17.0A, V<sub>DD</sub> = 27V, V<sub>GS</sub> = 10V.
- T < 10sec.



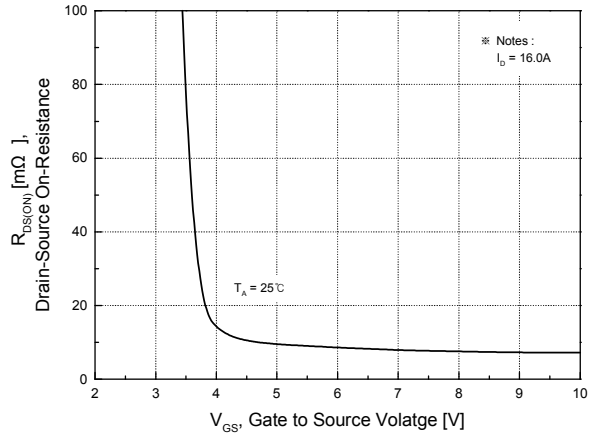
**Fig.1 On-Region Characteristics**



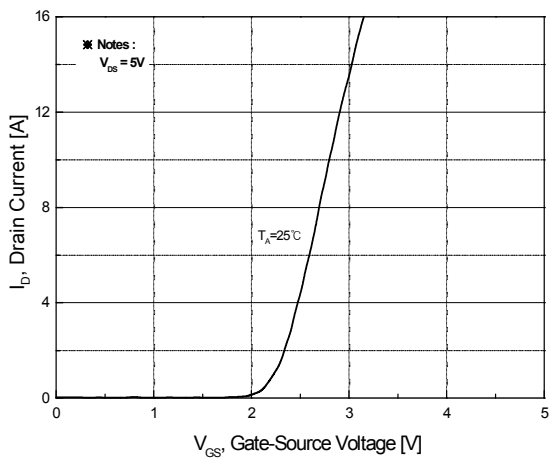
**Fig.2 On-Resistance Variation with Drain Current and Gate Voltage**



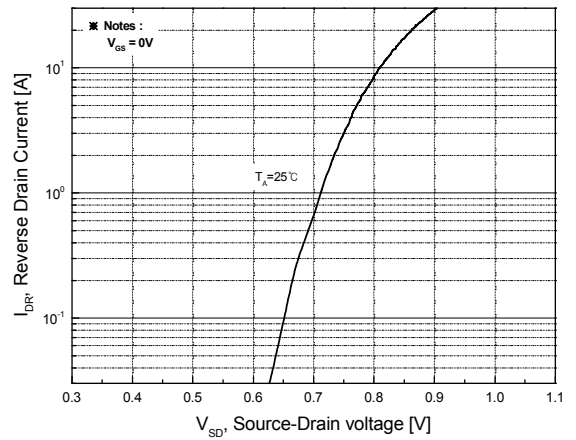
**Fig.3 On-Resistance Variation with Temperature**



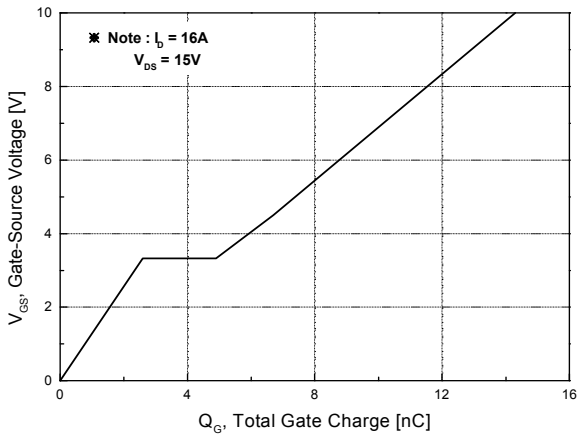
**Fig.4 On-Resistance Variation with Gate to Source Voltage**



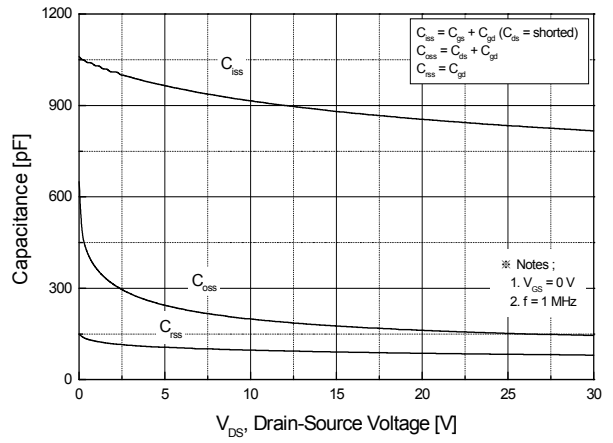
**Fig.5 Transfer Characteristics**



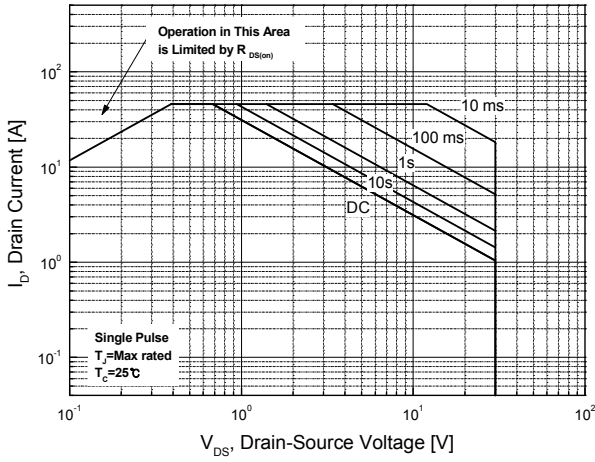
**Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature**



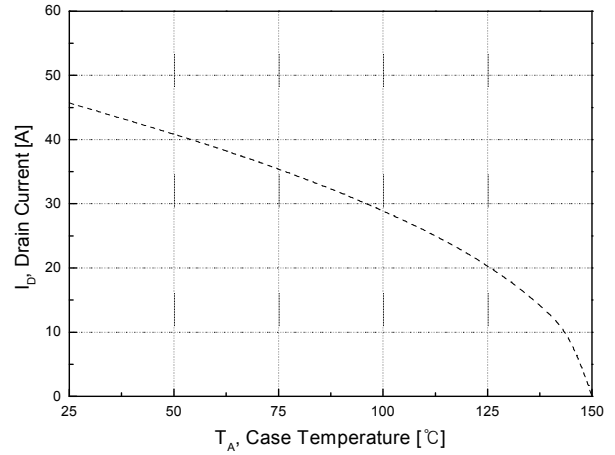
**Fig.7 Gate Charge Characteristics**



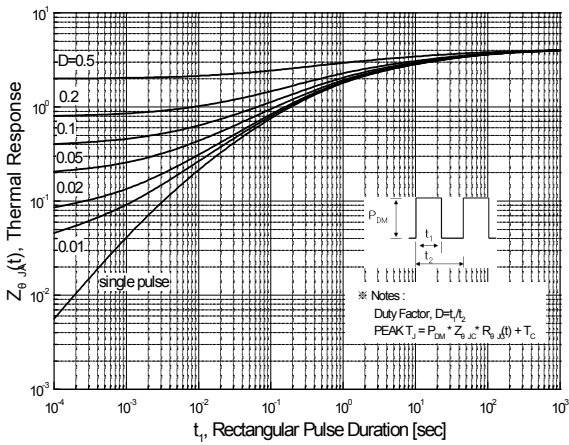
**Fig.8 Capacitance Characteristics**



**Fig.9 Maximum Safe Operating Area**



**Fig.10 Maximum Drain Current vs. Case Temperature**

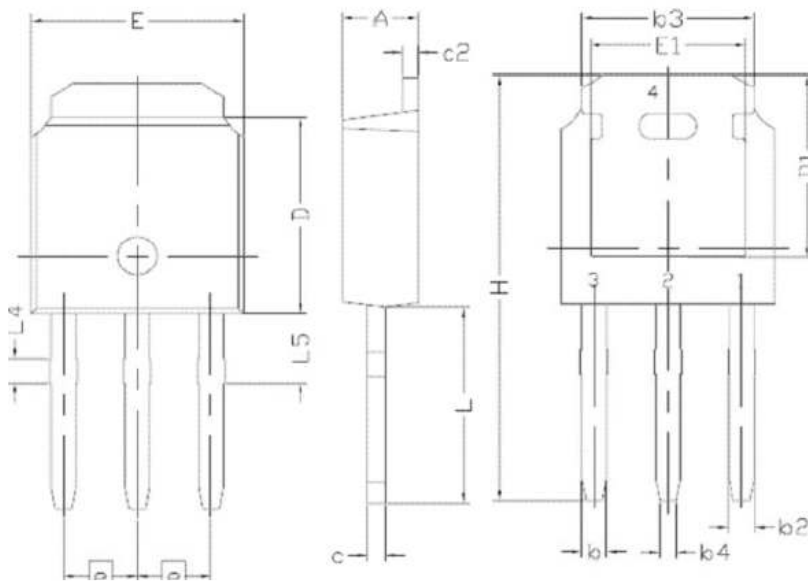


**Fig.11 Transient Thermal Response Curve**

## Package Dimension

### TO-251-VS (IPAK)

Dimensions are in millimeters, unless otherwise specified



SYMBOL	DIMENSIONAL REQMTS		
	MIN	NOM	MAX
E	6.40	6.60	6.731
L	5.88	6.08	6.28
L4	0.66	0.76	0.86
L5	1.96	2.16	2.36
D	6.00	6.10	6.223
H	12.90	13.20	13.50
b	0.64	0.76	0.88
b2	0.77	0.84	1.14
b3	5.21	5.34	5.46
b4	0.41	0.51	0.61
e	2.286 BSC		
A	2.20	2.30	2.38
c	0.40	0.50	0.60
c2	0.40	0.50	0.60
D1	5.30	--	--
E1	4.40	--	--

#### DISCLAIMER:

The Products are not designed for use in hostile environments, including, without limitation, aircraft, nuclear power generation, medical appliances, and devices or systems in which malfunction of any Product can reasonably be expected to result in a personal injury. Seller's customers using or selling Seller's products for use in such applications do so at their own risk and agree to fully defend and indemnify Seller.

MagnaChip reserves the right to change the specifications and circuitry without notice at any time. MagnaChip does not consider responsibility for use of any circuitry other than circuitry entirely included in a MagnaChip product. [MagnaChip](#) is a registered trademark of MagnaChip Semiconductor Ltd.