



3CG1

PNP Silicon High Frequency Low Power Transistor



Features:

1. Using epitaxy planar technology structure. High working frequency. Metallic packaging.
2. Small volume, light weight, easy installation.
3. Use for high frequency oscillation, high frequency small signal amplification, low power source adjustment circuit. Make up push pull amplifying circuit with NPN.
4. Quality Class: GS, G. Implementation of standards: QZJ840611

TECHNICAL DATA:

(Ta = 25°C)

Parameter name	Symbols	Unit	Specifications							Test Condition
			A	B	C	D	E	F	G	
Total Dissipation	P _{tot}	mW	300							T _a =25°C
Max. Collector Current	I _{CM}	mA	30							
Junction Temperature	T _{jm}	°C	175							
Storage Temperature	T _{stg}	°C	-55~+175							
C-E Breakdown Voltage	V _{(BR)CEO}	V	15	20	30	40	50	60	70	I _c =0.1mA
E-B Breakdown Voltage	V _{(BR)EBO}	V	4							I _E =0.1mA
Collector- Emitter Saturation Voltage Drop	V _{CE(sat)}	V	0.5							I _c =30mA, I _B =3mA
C-E Leakage Current	I _{CEO}	uA	1.0							V _{CE} =10V
DC Current Gain	h _{FE}		25~270							V _{CE} =10V, I _c =10mA
Transition frequency	f _T	MHz	150							V _{CE} =10V, I _c =10mA f=30MHz

h_{FE} Colored:

Color	Orange	Yellow	Green	Blue	Purple	Gray
h _{FE}	25~40	40~55	55~80	80~120	120~180	180~270

Outline and Dimensions: